



SPEC® CINT2006 Result

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Intel Corporation

SPECint®_rate2006 = 78.8

Intel DQ45CB motherboard (Intel Core 2 Quad Q9650)

SPECint_rate_base2006 = 73.8

CPU2006 license: 13

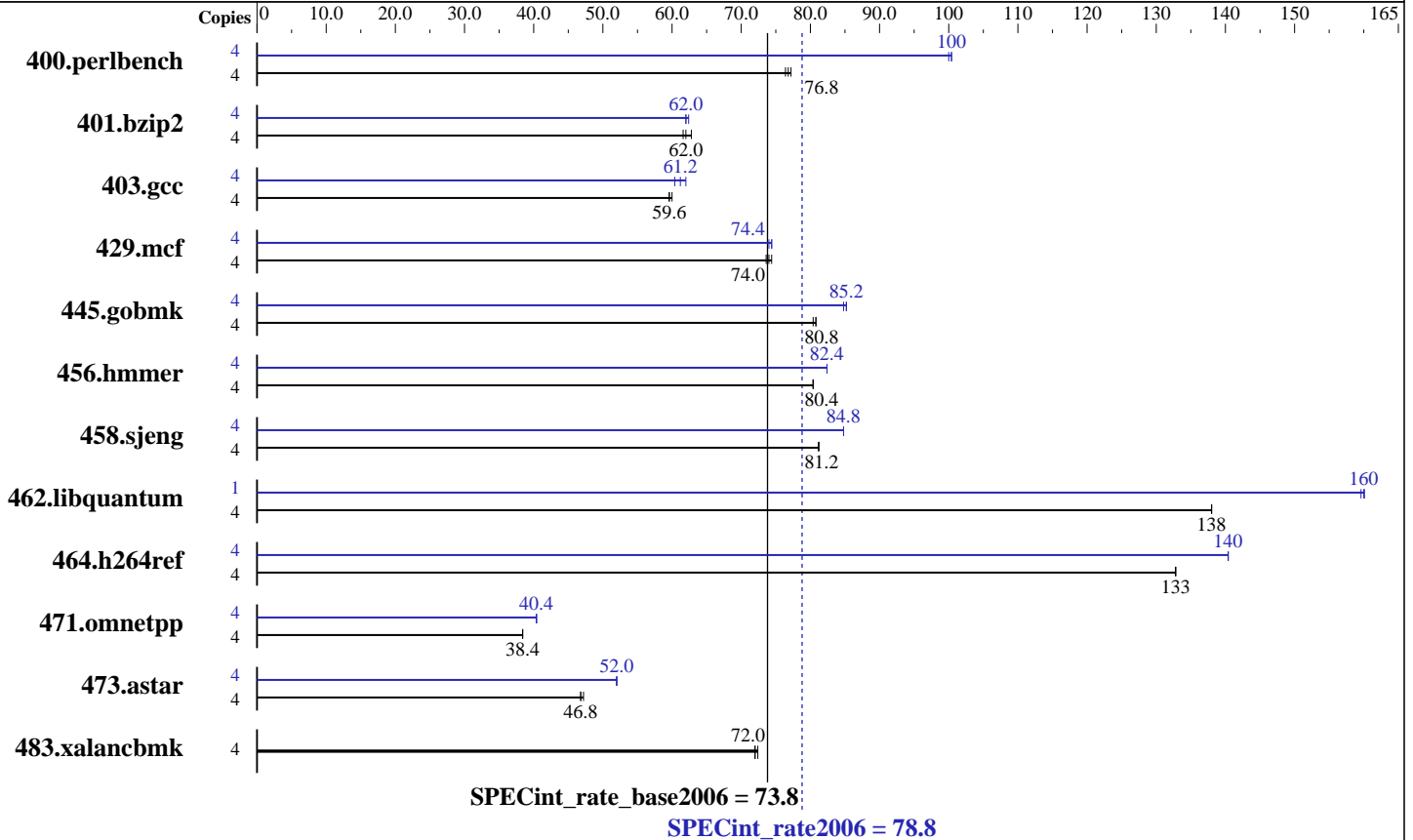
Test date: Apr-2009

Test sponsor: Intel Corporation

Hardware Availability: May-2009

Tested by: Intel Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Quad Q9650
 CPU Characteristics:
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1GB DDR2-800 CL5)
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	509	76.8	511	76.4	507	77.2	4	390	100	390	100	389	100
401.bzip2	4	617	62.8	624	62.0	627	61.6	4	622	62.0	617	62.4	622	62.0
403.gcc	4	540	59.6	538	60.0	541	59.6	4	533	60.4	525	61.2	519	62.0
429.mcf	4	491	74.4	495	73.6	492	74.0	4	493	74.0	491	74.4	491	74.4
445.gobmk	4	521	80.4	520	80.8	521	80.8	4	492	85.2	492	85.2	494	84.8
456.hammer	4	464	80.4	464	80.4	464	80.4	4	454	82.4	454	82.4	454	82.4
458.sjeng	4	595	81.2	595	81.2	595	81.2	4	570	84.8	570	84.8	570	84.8
462.libquantum	4	600	138	600	138	600	138	1	129	160	130	160	130	160
464.h264ref	4	667	133	666	133	666	133	4	631	140	631	140	631	140
471.omnetpp	4	651	38.4	650	38.4	651	38.4	4	620	40.4	620	40.4	619	40.4
473.astar	4	599	46.8	601	46.8	597	47.2	4	540	52.0	542	52.0	540	52.0
483.xalancbmk	4	382	72.0	382	72.4	383	72.0	4	382	72.0	382	72.4	383	72.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Tested systems can be used with Shin-G ATX case,
 Antec Truepower Trio power supply TP3-650
 Binaries were built on Windows Vista Ultimate (32-bit)
 Binaries were built on Windows Vista Ultimate (32-bit)
 OMP_NUM_THREADS set to number of logical processors as seen by the OS
 KMP_AFFINITY set to physical,0
 submit disabled for 462.libquantum peak

Base Compiler Invocation

C benchmarks:
 icl -Qvc9 -Qc99

C++ benchmarks:
 icl -Qvc9



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Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Base Optimization Flags

C benchmarks:

```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:

```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

401.bzp2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
/F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qparallel -Qpar-runtime-control -Qvec-guard-write
/F512000000

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml>

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For other inquiries, please contact webmaster@spec.org.

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