



# SPEC<sup>®</sup> CFP2006 Result

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## ASUSTeK Computer Inc.

### SPECfp<sup>®</sup>\_rate2006 = 207

ASUS TS700-E6 (Z8PE-D12X) server system (Intel Xeon W5580)

### SPECfp\_rate\_base2006 = 201

CPU2006 license: 9016

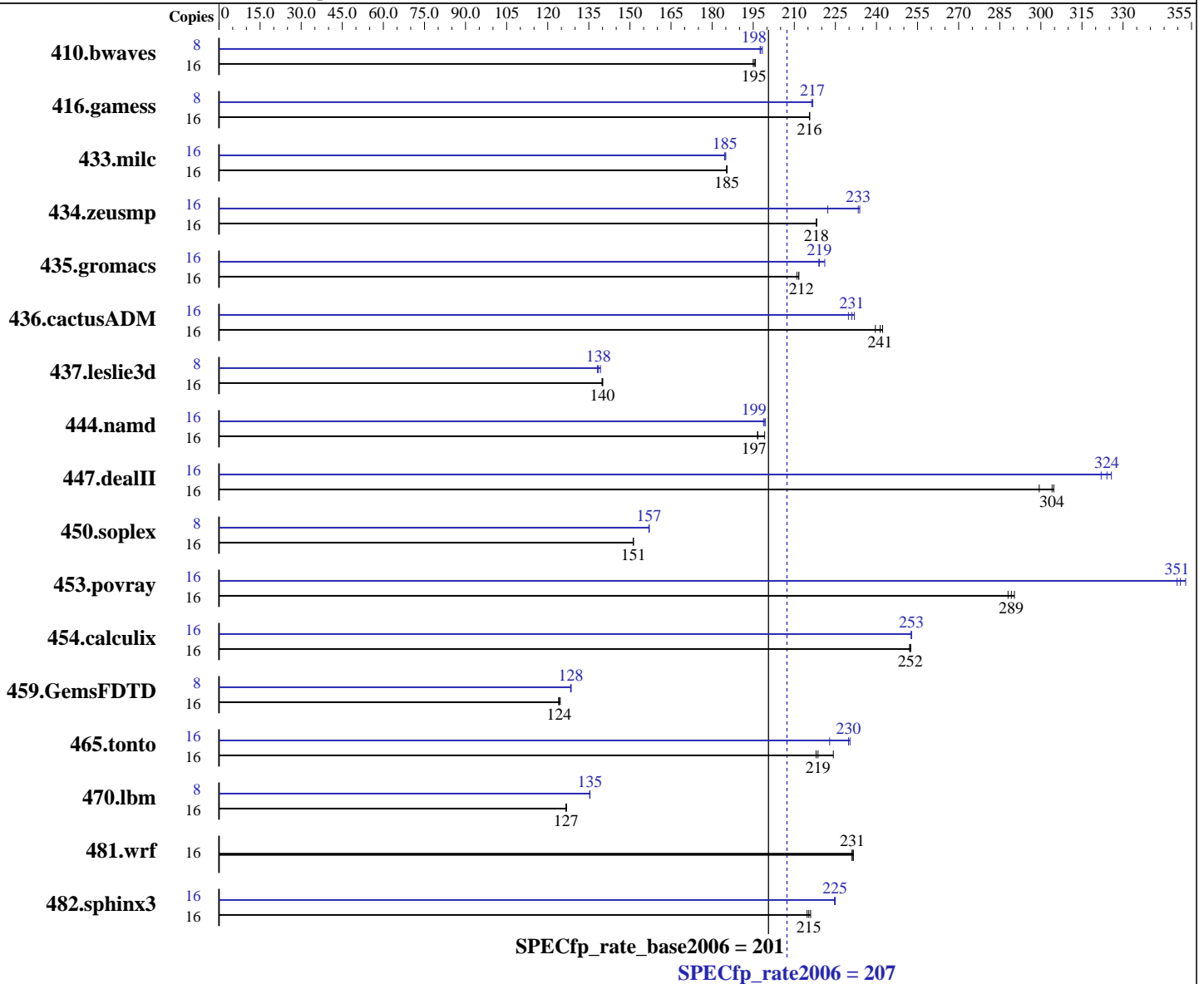
Test date: Apr-2009

Test sponsor: ASUSTeK Computer Inc.

Hardware Availability: Mar-2009

Tested by: ASUSTeK Computer Inc.

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon W5580  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2  
 Kernel 2.6.16.60-0.34-smp  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux  
 Build 20090131 Package ID: l\_cproc\_p\_11.0.080,  
 l\_cprof\_p\_11.0.080  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 X 4 GB PC3-10600R, CL=9)  
Disk Subsystem: HITACHI HDT725050VLA360 500GB SATAII, 7200RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1111	196	<u>1113</u>	<u>195</u>	1115	195	8	550	198	548	198	<u>550</u>	<u>198</u>
416.gamess	16	<u>1453</u>	<u>216</u>	1453	216	1454	216	8	723	217	724	216	<u>723</u>	<u>217</u>
433.milc	16	<u>792</u>	<u>185</u>	792	185	793	185	16	<u>795</u>	<u>185</u>	796	185	794	185
434.zeusmp	16	<u>668</u>	<u>218</u>	668	218	667	218	16	622	234	655	222	<u>624</u>	<u>233</u>
435.gromacs	16	540	212	<u>540</u>	<u>212</u>	542	211	16	517	221	<u>521</u>	<u>219</u>	522	219
436.cactusADM	16	798	240	<u>792</u>	<u>241</u>	789	242	16	832	230	<u>828</u>	<u>231</u>	824	232
437.leslie3d	16	1073	140	<u>1075</u>	<u>140</u>	1076	140	8	545	138	<u>543</u>	<u>138</u>	540	139
444.namd	16	653	197	<u>652</u>	<u>197</u>	644	199	16	646	199	644	199	<u>644</u>	<u>199</u>
447.dealII	16	<u>602</u>	<u>304</u>	611	299	601	305	16	<u>565</u>	<u>324</u>	562	326	568	322
450.soplex	16	882	151	<u>882</u>	<u>151</u>	883	151	8	425	157	<u>425</u>	<u>157</u>	425	157
453.povray	16	293	290	296	288	<u>294</u>	<u>289</u>	16	<u>243</u>	<u>351</u>	241	353	243	350
454.calculix	16	523	253	<u>523</u>	<u>252</u>	524	252	16	<u>522</u>	<u>253</u>	522	253	522	253
459.GemsFDTD	16	1363	125	<u>1367</u>	<u>124</u>	1369	124	8	661	128	660	129	<u>661</u>	<u>128</u>
465.tonto	16	702	224	<u>720</u>	<u>219</u>	722	218	16	684	230	706	223	<u>685</u>	<u>230</u>
470.lbm	16	1733	127	<u>1735</u>	<u>127</u>	1736	127	8	812	135	<u>812</u>	<u>135</u>	812	135
481.wrf	16	<u>773</u>	<u>231</u>	772	232	773	231	16	<u>773</u>	<u>231</u>	772	232	773	231
482.sphinx3	16	1444	216	<u>1448</u>	<u>215</u>	1453	215	16	1386	225	<u>1387</u>	<u>225</u>	1388	225

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS setting:  
Hardware Prefetcher: Enabled  
Adjacent Cache Line Prefetch: Enabled  
Tested system case compliance with Intel EEB 3.61 spec

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## Platform Notes (Continued)

SSI Server Power Supply 650W or higher  
System was configured with ASPEED AST2050 VGA (on board VGA)

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc`

`482.sphinx3: icc -m32`

C++ benchmarks (except as noted below):

`icpc`

`450.soplex: icpc -m32`

Fortran benchmarks (except as noted below):

`ifort`

`437.leslie3d: ifort -m32`

Benchmarks using both Fortran and C:

`icc ifort`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
 416.gamess: `-DSPEC_CPU_LP64`  
 433.milc: `-DSPEC_CPU_LP64`  
 434.zeusmp: `-DSPEC_CPU_LP64`  
 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`  
 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`  
 444.namd: `-DSPEC_CPU_LP64`  
 447.dealII: `-DSPEC_CPU_LP64`  
 453.povray: `-DSPEC_CPU_LP64`  
 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`  
 459.GemsFDTD: `-DSPEC_CPU_LP64`  
 465.tonto: `-DSPEC_CPU_LP64`  
 470.lbm: `-DSPEC_CPU_LP64`  
 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`



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## Peak Optimization Flags

### C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto

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## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -opt-prefetch -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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