



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®2006 = 23.5

PowerEdge T100 (Intel Xeon X3330, 2.66 GHz)

SPECint_base2006 = 21.0

CPU2006 license: 55

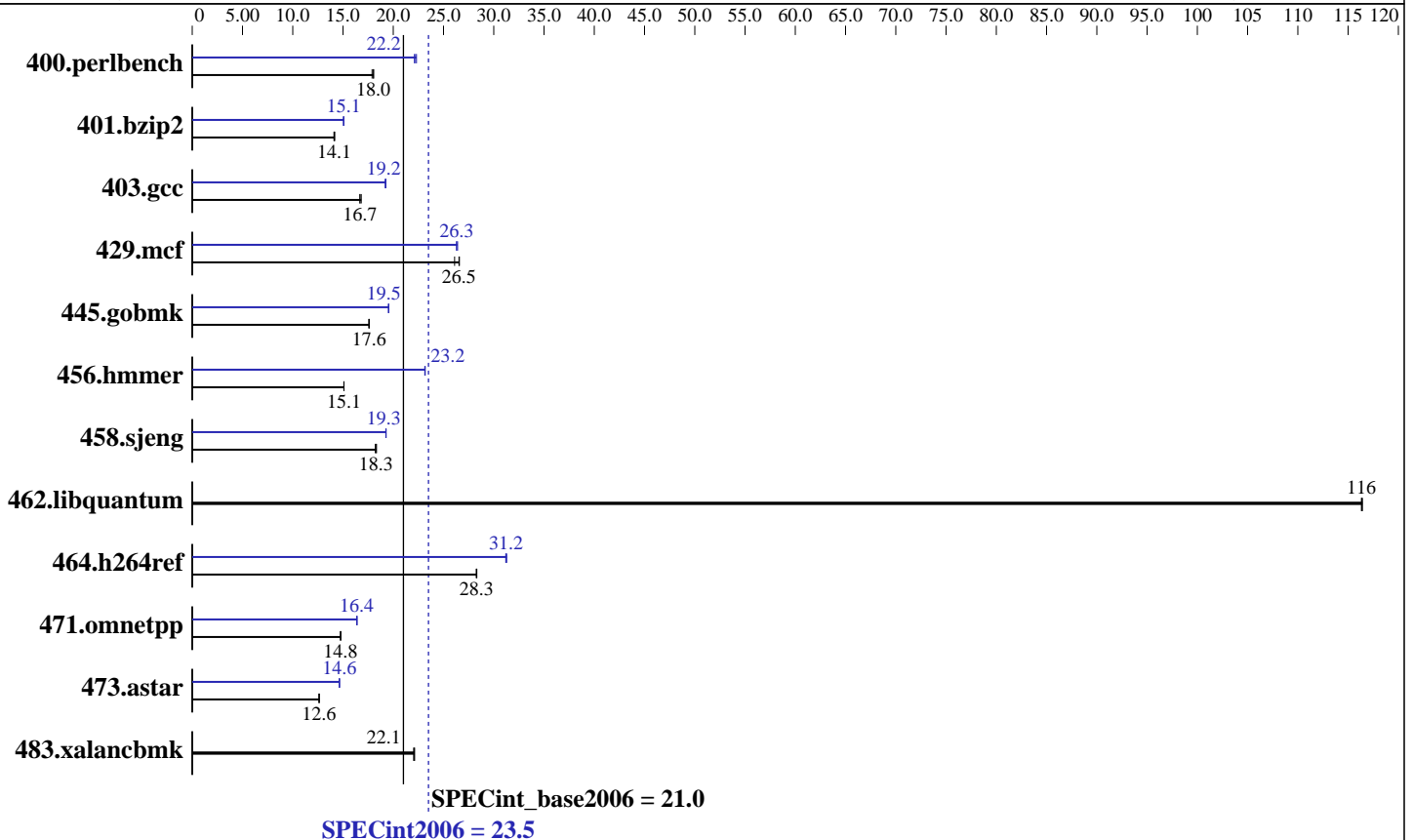
Test date: Oct-2008

Test sponsor: Dell Inc.

Hardware Availability: Oct-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X3330
 CPU Characteristics: 2666
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR2-800)
 Disk Subsystem: 1 x 160 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>542</u>	<u>18.0</u>	546	17.9	541	18.0	438	22.3	442	22.1	<u>440</u>	<u>22.2</u>
401.bzip2	681	14.2	684	14.1	<u>683</u>	<u>14.1</u>	640	15.1	641	15.0	<u>640</u>	<u>15.1</u>
403.gcc	479	16.8	<u>482</u>	<u>16.7</u>	482	16.7	418	19.3	<u>418</u>	<u>19.2</u>	419	19.2
429.mcf	349	26.1	343	26.6	<u>344</u>	<u>26.5</u>	345	26.4	<u>347</u>	<u>26.3</u>	347	26.3
445.gobmk	597	17.6	<u>596</u>	<u>17.6</u>	596	17.6	<u>537</u>	<u>19.5</u>	537	19.5	537	19.5
456.hammer	619	15.1	<u>618</u>	<u>15.1</u>	618	15.1	<u>403</u>	<u>23.2</u>	403	23.2	403	23.1
458.sjeng	660	18.3	664	18.2	<u>662</u>	<u>18.3</u>	628	19.3	627	19.3	<u>628</u>	<u>19.3</u>
462.libquantum	178	116	<u>178</u>	<u>116</u>	178	116	178	116	<u>178</u>	<u>116</u>	178	116
464.h264ref	<u>782</u>	<u>28.3</u>	783	28.3	782	28.3	707	31.3	709	31.2	<u>709</u>	<u>31.2</u>
471.omnetpp	423	14.8	<u>424</u>	<u>14.8</u>	424	14.8	382	16.4	381	16.4	<u>381</u>	<u>16.4</u>
473.astar	555	12.6	556	12.6	<u>556</u>	<u>12.6</u>	480	14.6	<u>479</u>	<u>14.6</u>	479	14.7
483.xalancbmk	312	22.1	313	22.0	<u>313</u>	<u>22.1</u>	312	22.1	313	22.0	<u>313</u>	<u>22.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml>

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For other inquiries, please contact webmaster@spec.org.

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