SPEC® CINT2006 Result

YOYOtech
(Test Sponsor: Future Publishing Ltd.)

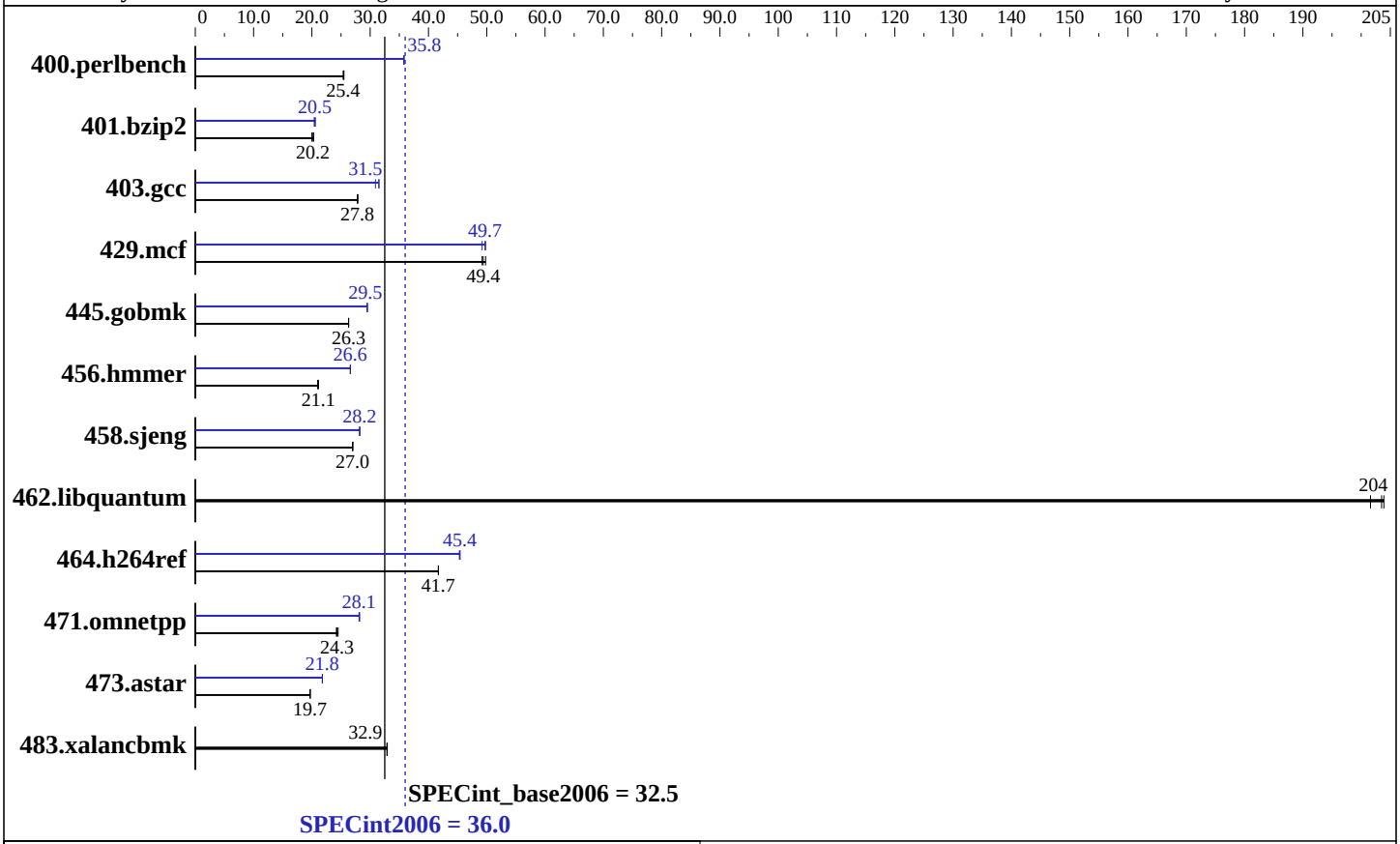
Fi7EPOWER MLK1610 (Intel Core i7-965)

SPECint®2006 = 36.0

SPECint_base2006 = 32.5

CPU2006 license: 3772
Test sponsor: Future Publishing Ltd.
Tested by: Future Publishing Ltd.

Test date: Oct-2008
Hardware Availability: Nov-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core i7-965 Extreme Edition
CPU Characteristics:	Intel Turbo Boost Technology disabled, clocked at 3.73 GHz
CPU MHz:	3733
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	9 GB (3x 2GB and 3x 1GB Corsair DDR3-1066, 9-9-9-24)
Disk Subsystem:	80 GB SATA, SSD
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from http://www.microquill.com/

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	384	25.5	385	25.4	**384**	**25.4**	273	35.8	273	35.8	**273**	**35.8**
401.bzip2	476	20.3	**477**	**20.2**	481	20.0	**472**	**20.5**	468	20.6	472	20.4
403.gcc	288	27.9	290	27.8	**289**	**27.8**	261	30.9	256	31.5	**256**	**31.5**
429.mcf	183	49.8	**185**	**49.4**	185	49.2	185	49.2	**184**	**49.7**	183	49.8
445.gobmk	399	26.3	**399**	**26.3**	399	26.3	356	29.5	355	29.5	**355**	**29.5**
456.hmmer	**443**	**21.1**	443	21.0	442	21.1	350	26.6	350	26.6	**350**	**26.6**
458.sjeng	**449**	**27.0**	449	27.0	448	27.0	**429**	**28.2**	428	28.2	429	28.2
462.libquantum	102	204	103	202	**102**	**204**	102	204	103	202	**102**	**204**
464.h264ref	531	41.7	531	41.7	**531**	**41.7**	488	45.3	**488**	**45.4**	488	45.4
471.omnetpp	255	24.5	258	24.2	**257**	**24.3**	223	28.1	**222**	**28.1**	222	28.2
473.astar	**357**	**19.7**	357	19.7	357	19.7	**322**	**21.8**	322	21.8	322	21.8
483.xalancbmk	210	32.9	**210**	**32.9**	210	32.9	210	32.9	**210**	**32.9**	210	32.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
System was configured with HIS Radeon HD 4870 X2 discrete graphics card
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qc99
```

C++ benchmarks:
```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qc99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch`
`/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE`

401.bzip2: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias`
`/F512000000`

403.gcc: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2)
`-Qipo -O3 -Qprec-div- /F512000000`

Continued on next page

Peak Optimization Flags (Continued)

429.mcf: `-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

445.gobmk: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000`

456.hmmer: `-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000`

458.sjeng: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000`

462.libquantum: `basepeak = yes`

464.h264ref: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000`

C++ benchmarks:

471.omnetpp: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

473.astar: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.html
http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.xml
http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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