SPEC® CINT2006 Result

NEC Corporation

Express5800/R140a-4
(Intel Xeon E7420)

SPECint®2006 =　19.2

SPECint_base2006 =　16.9

CPU2006 license: 9006

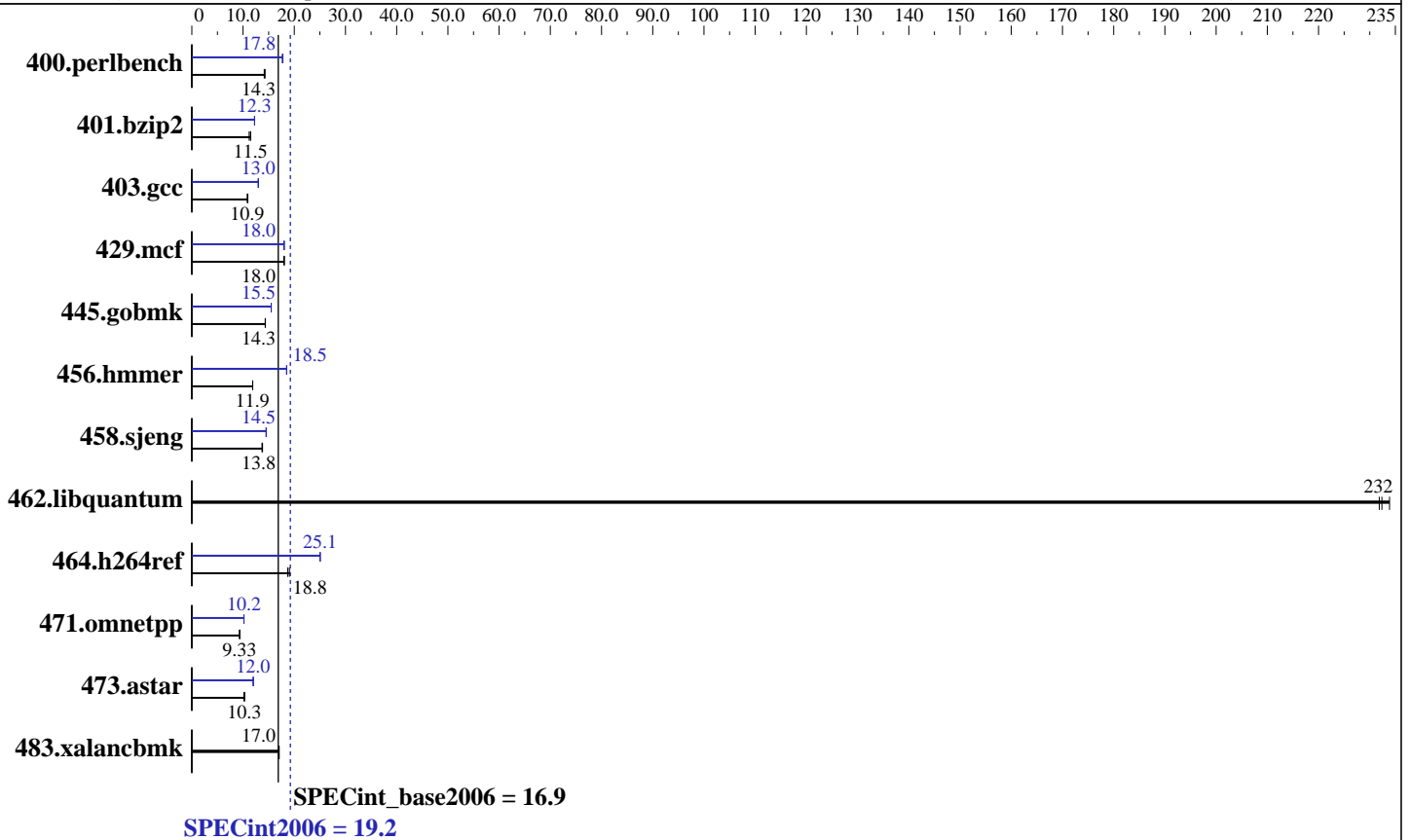**Test sponsor:** NEC Corporation

Tested by: NEC Corporation

Test date: Nov-2008

Hardware Availability: Nov-2008

Software Availability: Nov-2008

400.perlbench	17.8 / 14.3
401.bzip2	12.3 / 11.5
403.gcc	13.0 / 10.9
429.mcf	18.0 / 18.0
445.gobmk	15.5 / 14.3
456.hmmer	18.5 / 11.9
458.sjeng	14.5 / 13.8
462.libquantum	232
464.h264ref	25.1 / 18.8
471.omnetpp	10.2 / 9.33
473.astar	12.0 / 10.3
483.xalancbmk	17.0

SPECint_base2006 = 16.9

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Hardware

CPU Name: Intel Xeon E7420

CPU Characteristics: 1066 MHz system bus

CPU MHz: 2133

FPU: Integrated

CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip

CPU(s) orderable: 1,2,3,4 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 6 MB I+D on chip per chip, 3 MB shared / 2 cores

L3 Cache: 8 MB I+D on chip per chip

Other Cache: None

Memory: 32 GB (16x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)

Disk Subsystem: 1x73.2 GB SAS, 15000RPM

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp

Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.044

Auto Parallel: Yes

File System: ext2

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: MicroQuill SmartHeap Library 8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**685**	**14.3**	685	14.3	691	14.1	549	17.8	**550**	**17.8**	553	17.7
401.bzip2	862	11.2	**842**	**11.5**	841	11.5	789	12.2	**787**	**12.3**	787	12.3
403.gcc	743	10.8	736	10.9	**741**	**10.9**	**620**	**13.0**	621	13.0	620	13.0
429.mcf	508	18.0	**505**	**18.0**	505	18.1	505	18.1	508	18.0	**505**	**18.0**
445.gobmk	731	14.3	**731**	**14.3**	731	14.3	**677**	**15.5**	677	15.5	676	15.5
456.hmmer	**785**	**11.9**	784	11.9	785	11.9	503	18.5	504	18.5	**504**	**18.5**
458.sjeng	**878**	**13.8**	876	13.8	883	13.7	**834**	**14.5**	832	14.6	834	14.5
462.libquantum	**89.2**	**232**	89.4	232	88.6	234	**89.2**	**232**	89.4	232	88.6	234
464.h264ref	1161	19.1	**1180**	**18.8**	1181	18.7	882	25.1	885	25.0	**883**	**25.1**
471.omnetpp	669	9.34	**670**	**9.33**	670	9.33	614	10.2	**614**	**10.2**	614	10.2
473.astar	682	10.3	690	10.2	**683**	**10.3**	585	12.0	586	12.0	**585**	**12.0**
483.xalancbmk	404	17.1	**406**	**17.0**	406	17.0	404	17.1	**406**	**17.0**	406	17.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"
```

Platform Notes

```
Bios settings:
Hardware Prefetcher:        Enabled
Adjacent Cache Line Prefetch: Enabled
```

General Notes

```
The NEC Express5800/R140a-4(Intel Xeon E7420) and
the Bull NovaScale R480 E1(Intel Xeon E7420, 2.13 GHz) models are electronically equivalent.
The results have been measured on a NEC Express5800/R140a-4(Intel Xeon E7420) model.
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/044/bin/intel64/icc
        -L/opt/intel/Compiler/11.0/044/ipp/em64t/lib
        -I/opt/intel/Compiler/11.0/044/ipp/em64t/include
```

```
456.hmmer: /opt/intel/Compiler/11.0/044/bin/intel64/icc
        -L/opt/intel/Compiler/11.0/044/ipp/em64t/lib
        -I/opt/intel/Compiler/11.0/044/ipp/em64t/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

```
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -ansi-alias -opt-prefetch

   401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
              -no-prec-div -static -auto-ilp32 -opt-prefetch
              -ansi-alias

     403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
              -opt-malloc-options=3

     429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

   445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
              -no-prec-div -ansi-alias

   456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
              -ansi-alias -auto-ilp32

   458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
              -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

  464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -unroll2 -ansi-alias
```

C++ benchmarks:

```
  471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -ansi-alias -opt-ra-region-strategy=block
               -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

    473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
               -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.20090713.html
http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revD.20090713.xml
http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml

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