



# SPEC<sup>®</sup> CINT2006 Result

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Dell Inc.

SPECint<sup>®</sup>2006 = 26.6

PowerEdge T300 (Intel Xeon E3113, 3.00 GHz)

SPECint\_base2006 = 23.9

CPU2006 license: 55

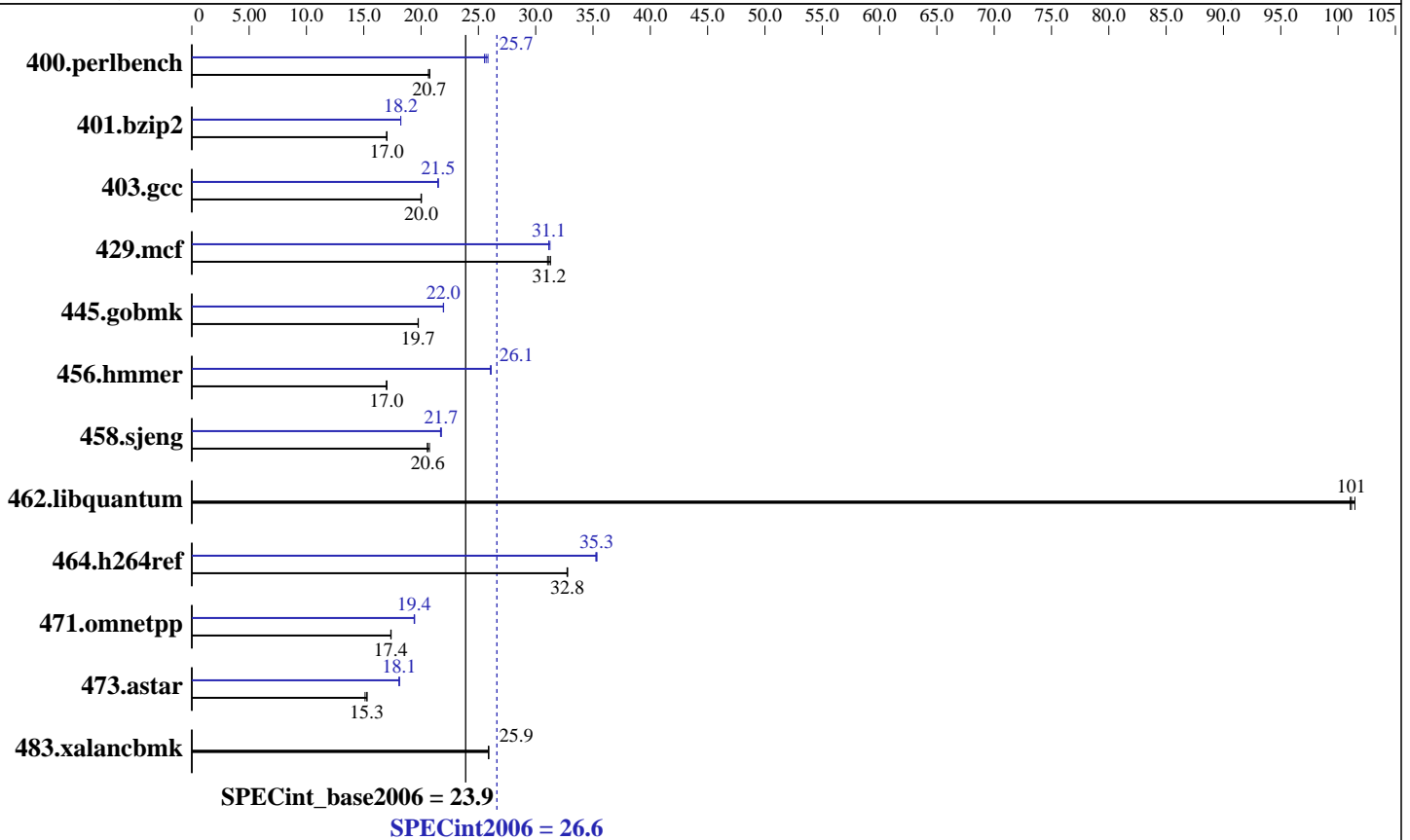
Test date: Oct-2008

Test sponsor: Dell Inc.

Hardware Availability: Mar-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon E3113  
 CPU Characteristics:  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB DDR2-667)  
 Disk Subsystem: 1 x 300 GB SAS 15000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16-60.0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>471</u></b>	<b><u>20.7</u></b>	473	20.6	471	20.8	<b><u>381</u></b>	<b><u>25.7</u></b>	383	25.5	378	25.8
401.bzip2	568	17.0	<b><u>567</u></b>	<b><u>17.0</u></b>	567	17.0	529	18.2	530	18.2	<b><u>529</u></b>	<b><u>18.2</u></b>
403.gcc	403	20.0	<b><u>402</u></b>	<b><u>20.0</u></b>	402	20.0	375	21.5	<b><u>375</u></b>	<b><u>21.5</u></b>	374	21.5
429.mcf	294	31.0	<b><u>293</u></b>	<b><u>31.2</u></b>	291	31.3	293	31.1	<b><u>293</u></b>	<b><u>31.1</u></b>	292	31.2
445.gobmk	531	19.8	531	19.7	<b><u>531</u></b>	<b><u>19.7</u></b>	<b><u>478</u></b>	<b><u>22.0</u></b>	478	21.9	478	22.0
456.hammer	549	17.0	<b><u>549</u></b>	<b><u>17.0</u></b>	549	17.0	<b><u>358</u></b>	<b><u>26.1</u></b>	358	26.1	357	26.1
458.sjeng	589	20.6	584	20.7	<b><u>588</u></b>	<b><u>20.6</u></b>	558	21.7	556	21.8	<b><u>557</u></b>	<b><u>21.7</u></b>
462.libquantum	204	101	<b><u>205</u></b>	<b><u>101</u></b>	205	101	204	101	<b><u>205</u></b>	<b><u>101</u></b>	205	101
464.h264ref	676	32.7	675	32.8	<b><u>675</u></b>	<b><u>32.8</u></b>	628	35.2	626	35.3	<b><u>627</u></b>	<b><u>35.3</u></b>
471.omnetpp	<b><u>360</u></b>	<b><u>17.4</u></b>	360	17.4	359	17.4	<b><u>322</u></b>	<b><u>19.4</u></b>	322	19.4	321	19.4
473.astar	464	15.1	<b><u>460</u></b>	<b><u>15.3</u></b>	459	15.3	388	18.1	387	18.1	<b><u>388</u></b>	<b><u>18.1</u></b>
483.xalancbmk	266	25.9	267	25.9	<b><u>267</u></b>	<b><u>25.9</u></b>	266	25.9	267	25.9	<b><u>267</u></b>	<b><u>25.9</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -auto-ilp32 -opt-prefetch  
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmcr: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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