



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®2006 = 29.1

PowerEdge 1950 III (Intel Xeon X5260, 3.33 GHz)

SPECint\_base2006 = 25.6

CPU2006 license: 55

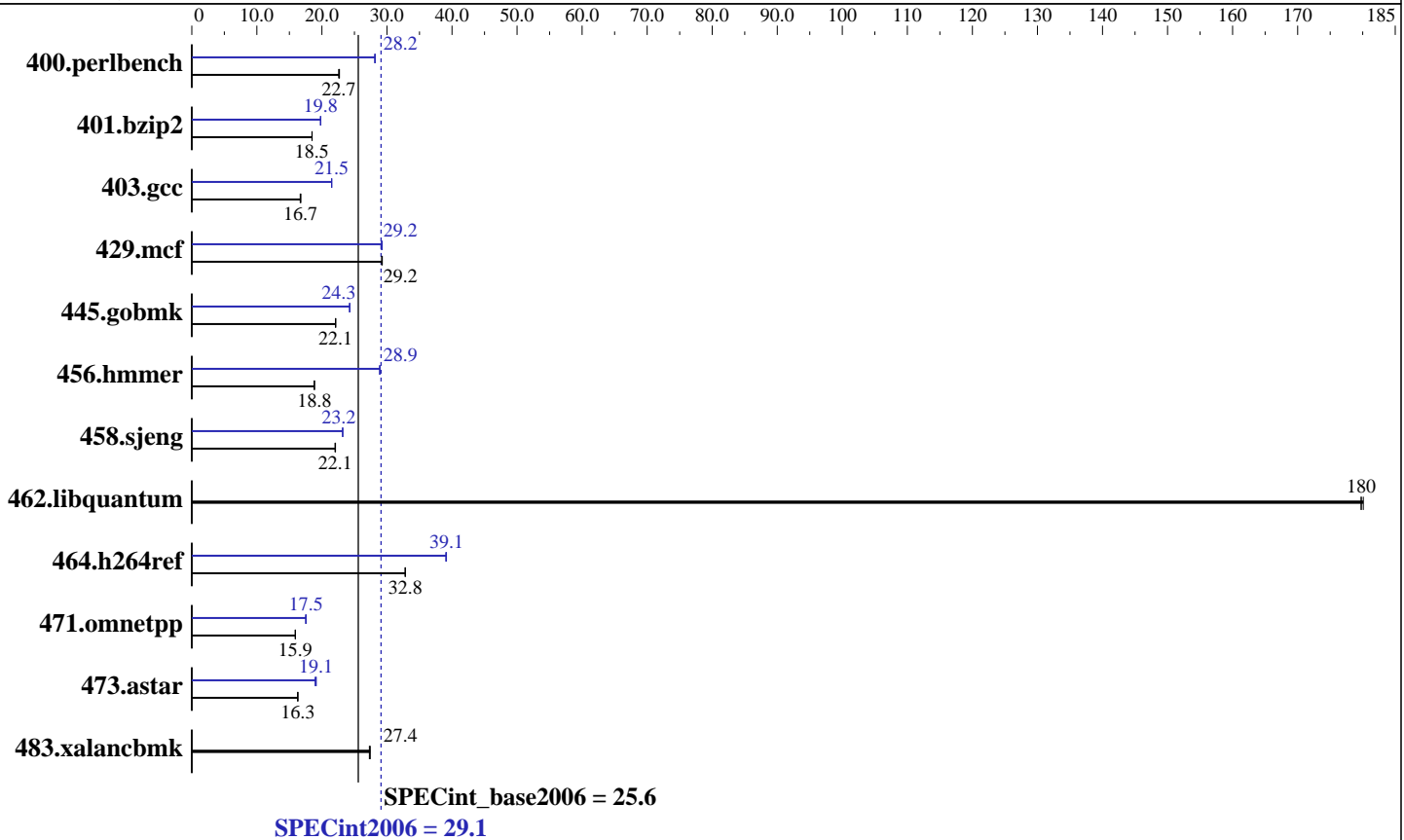
Test date: Oct-2008

Test sponsor: Dell Inc.

Hardware Availability: Sep-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



## Hardware

CPU Name: Intel Xeon X5260  
 CPU Characteristics:  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-667 FB-DIMM)  
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16-60.0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	433	22.6	<b><u>431</u></b>	<b><u>22.7</u></b>	430	22.7	<b><u>347</u></b>	<b><u>28.2</u></b>	348	28.1	346	28.2
401.bzip2	522	18.5	523	18.5	<b><u>522</u></b>	<b><u>18.5</u></b>	<b><u>487</u></b>	<b><u>19.8</u></b>	487	19.8	488	19.8
403.gcc	<b><u>481</u></b>	<b><u>16.7</u></b>	483	16.7	480	16.8	<b><u>374</u></b>	<b><u>21.5</u></b>	374	21.5	375	21.5
429.mcf	312	29.2	311	29.3	<b><u>312</u></b>	<b><u>29.2</u></b>	312	29.3	<b><u>312</u></b>	<b><u>29.2</u></b>	313	29.2
445.gobmk	475	22.1	<b><u>475</u></b>	<b><u>22.1</u></b>	474	22.1	432	24.3	432	24.3	<b><u>432</u></b>	<b><u>24.3</u></b>
456.hammer	<b><u>495</u></b>	<b><u>18.8</u></b>	495	18.8	495	18.8	<b><u>323</u></b>	<b><u>28.9</u></b>	323	28.9	323	28.9
458.sjeng	<b><u>548</u></b>	<b><u>22.1</u></b>	548	22.1	550	22.0	522	23.2	522	23.2	<b><u>522</u></b>	<b><u>23.2</u></b>
462.libquantum	115	180	115	180	<b><u>115</u></b>	<b><u>180</u></b>	115	180	115	180	<b><u>115</u></b>	<b><u>180</u></b>
464.h264ref	674	32.8	675	32.8	<b><u>674</u></b>	<b><u>32.8</u></b>	567	39.0	565	39.1	<b><u>565</u></b>	<b><u>39.1</u></b>
471.omnetpp	<b><u>392</u></b>	<b><u>15.9</u></b>	392	15.9	394	15.9	356	17.6	357	17.5	<b><u>356</u></b>	<b><u>17.5</u></b>
473.astar	<b><u>431</u></b>	<b><u>16.3</u></b>	430	16.3	432	16.3	370	19.0	367	19.1	<b><u>368</u></b>	<b><u>19.1</u></b>
483.xalancbmk	252	27.4	<b><u>252</u></b>	<b><u>27.4</u></b>	253	27.3	252	27.4	<b><u>252</u></b>	<b><u>27.4</u></b>	253	27.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -auto-ilp32 -opt-prefetch  
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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