SPEC® CFP2006 Result
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Fujitsu Siemens Computers

PRIMERGY RX600 S4, Intel Xeon E7450, 2.40 GHz

$SPECfp®_rate2006 = $ 139

$SPECfp_rate_base2006 = $ 130
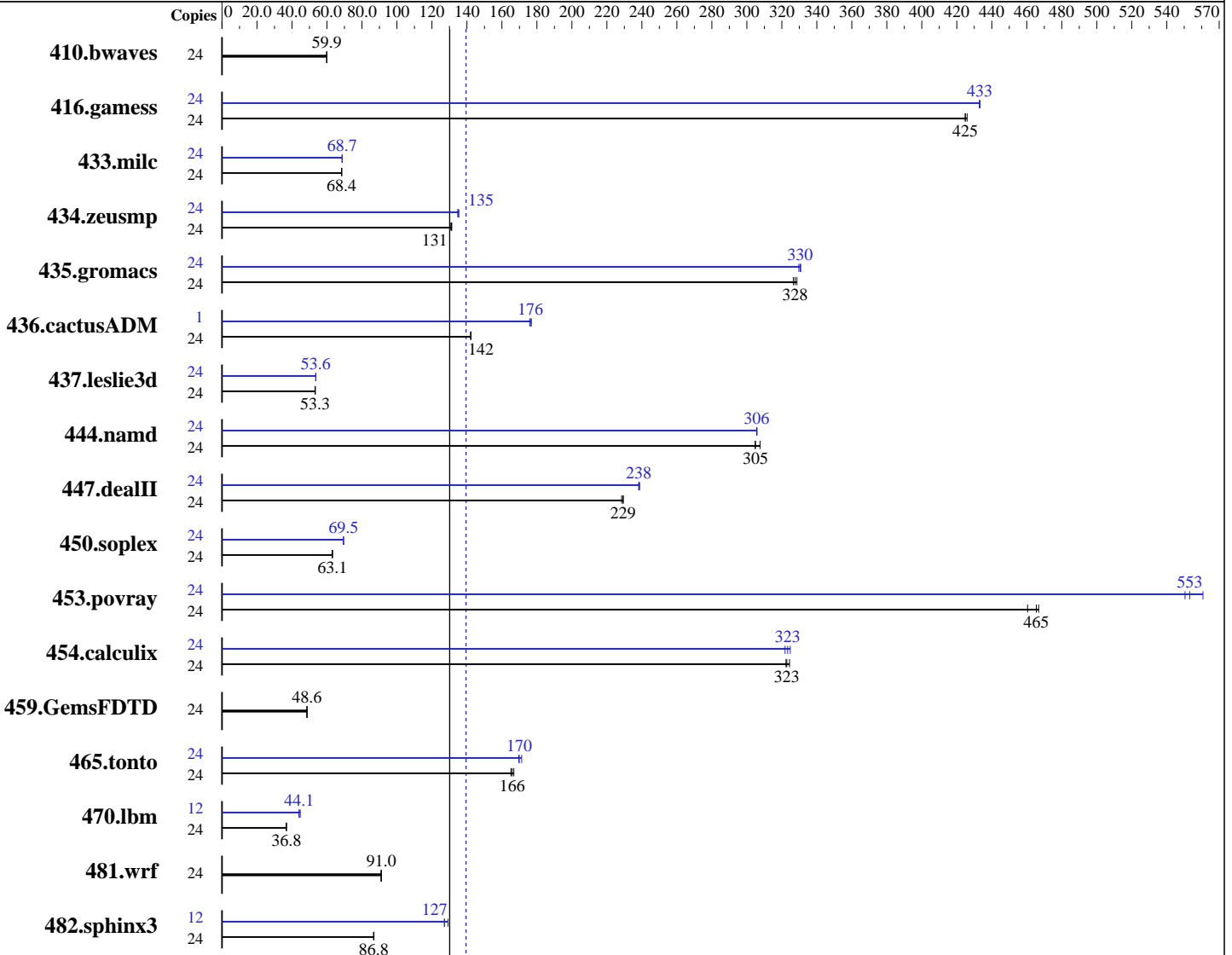
CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

SPECfp_rate_base2006 = 130
SPECfp_rate2006 = 139

Hardware

CPU Name: Intel Xeon E7450
CPU Characteristics: 1067 MHz system bus
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip
CPU(s) orderable: 1,2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 9 MB I+D on chip per chip, 3 MB shared / 2 cores

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730
Auto Parallel: Yes
File System: ext3
System State: Multi-User Run Level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit

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L3 Cache:	12 MB I+D on chip per chip	Other Software:	Binutils 2.18.50.0.7.20080502
Other Cache:	None		
Memory:	64 GB (16x4 GB PC2-5300F, 2 rank, CL5-5-5, ECC)		
Disk Subsystem:	1x SAS, 36 GB, 10000 rpm		
Other Hardware:	None		

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	5450	59.9	**5446**	**59.9**	5446	59.9	24	5450	59.9	**5446**	**59.9**	5446	59.9
416.gamess	24	1103	426	1106	425	**1106**	**425**	24	1086	433	1084	433	**1085**	**433**
433.milc	24	3225	68.3	3220	68.4	**3220**	**68.4**	24	**3207**	**68.7**	3208	68.7	3207	68.7
434.zeusmp	24	1669	131	1663	131	**1665**	**131**	24	1613	135	1620	135	**1619**	**135**
435.gromacs	24	521	329	525	327	**523**	**328**	24	520	330	518	331	**519**	**330**
436.cactusADM	24	2018	142	2016	142	**2017**	**142**	1	**67.8**	**176**	67.6	177	67.9	176
437.leslie3d	24	4241	53.2	**4233**	**53.3**	4233	53.3	24	4209	53.6	**4208**	**53.6**	4206	53.6
444.namd	24	632	305	**631**	**305**	626	308	24	630	306	629	306	**630**	**306**
447.dealII	24	1202	228	**1197**	**229**	1197	229	24	1150	239	1152	238	**1152**	**238**
450.soplex	24	3176	63.0	**3172**	**63.1**	3170	63.1	24	2883	69.4	**2882**	**69.5**	2881	69.5
453.povray	24	277	460	**274**	**465**	274	467	24	232	550	228	561	**231**	**553**
454.calculix	24	**613**	**323**	614	322	610	324	24	**612**	**323**	615	322	610	325
459.GemsFDTD	24	5250	48.5	**5240**	**48.6**	5238	48.6	24	5250	48.5	**5240**	**48.6**	5238	48.6
465.tonto	24	1416	167	1430	165	**1424**	**166**	24	1379	171	**1390**	**170**	1392	170
470.lbm	24	8969	36.8	**8959**	**36.8**	8955	36.8	12	3686	44.7	**3739**	**44.1**	3741	44.1
481.wrf	24	2949	90.9	2943	91.1	**2945**	**91.0**	24	2949	90.9	2943	91.1	**2945**	**91.0**
482.sphinx3	24	5384	86.9	**5390**	**86.8**	5401	86.6	12	1811	129	1840	127	**1839**	**127**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

```
    All binaries were built with 64-bit mode except:
    437.leslie3d, 450.soplex and 482.sphinx3 in peak
    were built with 32-bit mode.
```

Submit Notes

```
    The config file option 'submit' was used.
     taskset has been used to bind processes to cores except
     for 436.cactusADM peak
     For peak modules using 1/2 the number of available cores,
     each copy was assigned to a single L2 cache using mysubmit.pl script.
     See the flags description file for mysubmit.pl details.
```

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Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Platform Notes

```
BIOS configuration:
High Bandwidth option = Enable
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icc ifort

Base Portability Flags

```
      410.bwaves: -DSPEC_CPU_LP64
      416.gamess: -DSPEC_CPU_LP64
        433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
   436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
     437.leslie3d: -DSPEC_CPU_LP64
       444.namd: -DSPEC_CPU_LP64
      447.dealII: -DSPEC_CPU_LP64
      450.soplex: -DSPEC_CPU_LP64
      453.povray: -DSPEC_CPU_LP64
     454.calculix: -DSPEC_CPU_LP64 -nofor_main
    459.GemsFDTD: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
```

Fortran benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc
            -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
            -I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

C++ benchmarks (except as noted below):
```
icpc
```

```
450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc
            -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
            -I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

Fortran benchmarks (except as noted below):
```
ifort
```

```
437.leslie3d: /opt/intel/Compiler/11.0/042/bin/ia32/ifort
            -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
            -I/opt/intel/Compiler/11.0/042/ipp/ia32/include
```

Benchmarks using both Fortran and C:
```
icc ifort
```

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Peak Portability Flags

```
     410.bwaves: -DSPEC_CPU_LP64
     416.gamess: -DSPEC_CPU_LP64
       433.milc: -DSPEC_CPU_LP64
      434.zeusmp: -DSPEC_CPU_LP64
     435.gromacs: -DSPEC_CPU_LP64 -nofor_main
   436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       444.namd: -DSPEC_CPU_LP64
      447.dealII: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
    459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
        433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -fno-alias

         470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
                  -auto-ilp32

     482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
```

C++ benchmarks:

```
        444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -fno-alias -auto-ilp32

       447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

      450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -opt-malloc-options=3

      453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -unroll4 -ansi-alias
```

Fortran benchmarks:

```
      410.bwaves: basepeak = yes

      416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
                  -no-prec-div -static -unroll2 -Ob0 -ansi-alias
                  -scalar-rep-
```

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Peak Optimization Flags (Continued)

434.zeusmp: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static`

437.leslie3d: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32`

436.cactusADM: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32`

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.11.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.08.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.11.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.08.xml

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