



# SPEC® CINT2006 Result

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IBM Corporation

SPECint®\_rate2006 = 44.7

IBM System x3350 (Intel Xeon E3120)

SPECint\_rate\_base2006 = 38.1

CPU2006 license: 11

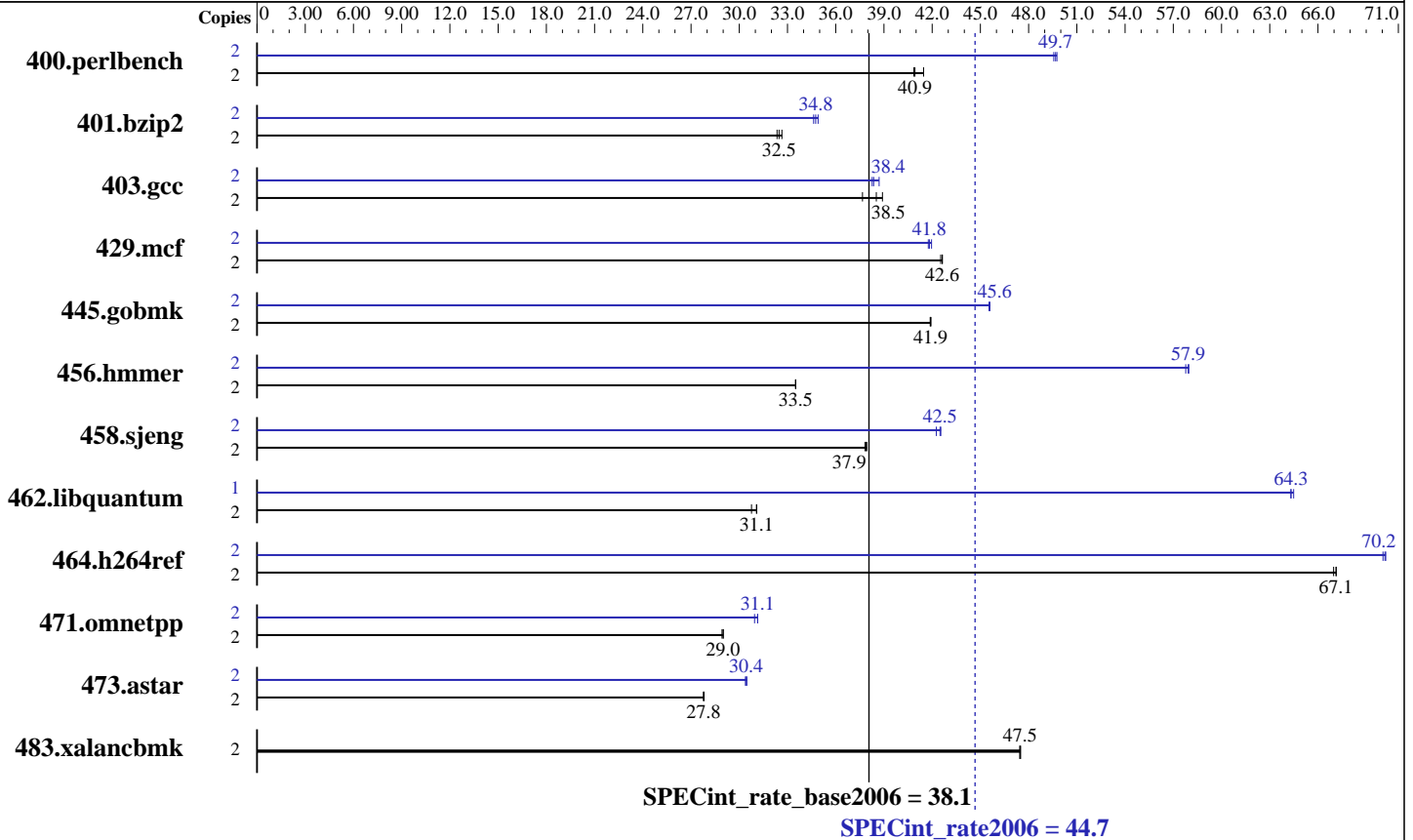
Test date: Jun-2008

Test sponsor: IBM Corporation

Hardware Availability: Sep-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E3120  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3166  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4 x 2 GB DDR2-6400E ECC)  
 Disk Subsystem: 1 x 160 GB SATA, 7200RPM  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (Full multiuser with network)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	478	40.9	<b>477</b>	<b>40.9</b>	471	41.5	2	394	49.6	393	49.8	<b>393</b>	<b>49.7</b>
401.bzip2	2	<b>594</b>	<b>32.5</b>	597	32.3	591	32.7	2	553	34.9	<b>555</b>	<b>34.8</b>	557	34.6
403.gcc	2	427	37.7	414	38.9	<b>418</b>	<b>38.5</b>	2	416	38.7	<b>420</b>	<b>38.4</b>	421	38.3
429.mcf	2	429	42.5	<b>428</b>	<b>42.6</b>	428	42.6	2	437	41.8	435	42.0	<b>436</b>	<b>41.8</b>
445.gobmk	2	501	41.9	<b>501</b>	<b>41.9</b>	500	41.9	2	461	45.5	460	45.6	<b>460</b>	<b>45.6</b>
456.hmmmer	2	557	33.5	<b>557</b>	<b>33.5</b>	557	33.5	2	323	57.8	<b>322</b>	<b>57.9</b>	322	58.0
458.sjeng	2	<b>639</b>	<b>37.9</b>	638	37.9	640	37.8	2	<b>570</b>	<b>42.5</b>	569	42.5	573	42.3
462.libquantum	2	<b>1334</b>	<b>31.1</b>	1333	31.1	1347	30.8	1	321	64.5	322	64.3	<b>322</b>	<b>64.3</b>
464.h264ref	2	<b>659</b>	<b>67.1</b>	659	67.1	661	67.0	2	632	70.0	<b>631</b>	<b>70.2</b>	630	70.2
471.omnetpp	2	<b>431</b>	<b>29.0</b>	432	28.9	431	29.0	2	404	31.0	<b>401</b>	<b>31.1</b>	401	31.1
473.astar	2	505	27.8	505	27.8	<b>505</b>	<b>27.8</b>	2	462	30.4	461	30.5	<b>461</b>	<b>30.4</b>
483.xalancbmk	2	291	47.5	291	47.5	<b>291</b>	<b>47.5</b>	2	291	47.5	291	47.5	<b>291</b>	<b>47.5</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer, for peak, are compiled in 64-bit mode  
Hardware Sector Prefetch Enabled and Adjacent Sector Prefetch Enabled  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to physical,0  
KMP\_STACKSIZE set to null  
taskset utility used to bind CPU(s) to processes

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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