



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

SPECint®2006 = 21.5

PRIMERGY RX300 S4, Intel Xeon L5410, 2.33 GHz

SPECint_base2006 = 18.7

CPU2006 license: 22

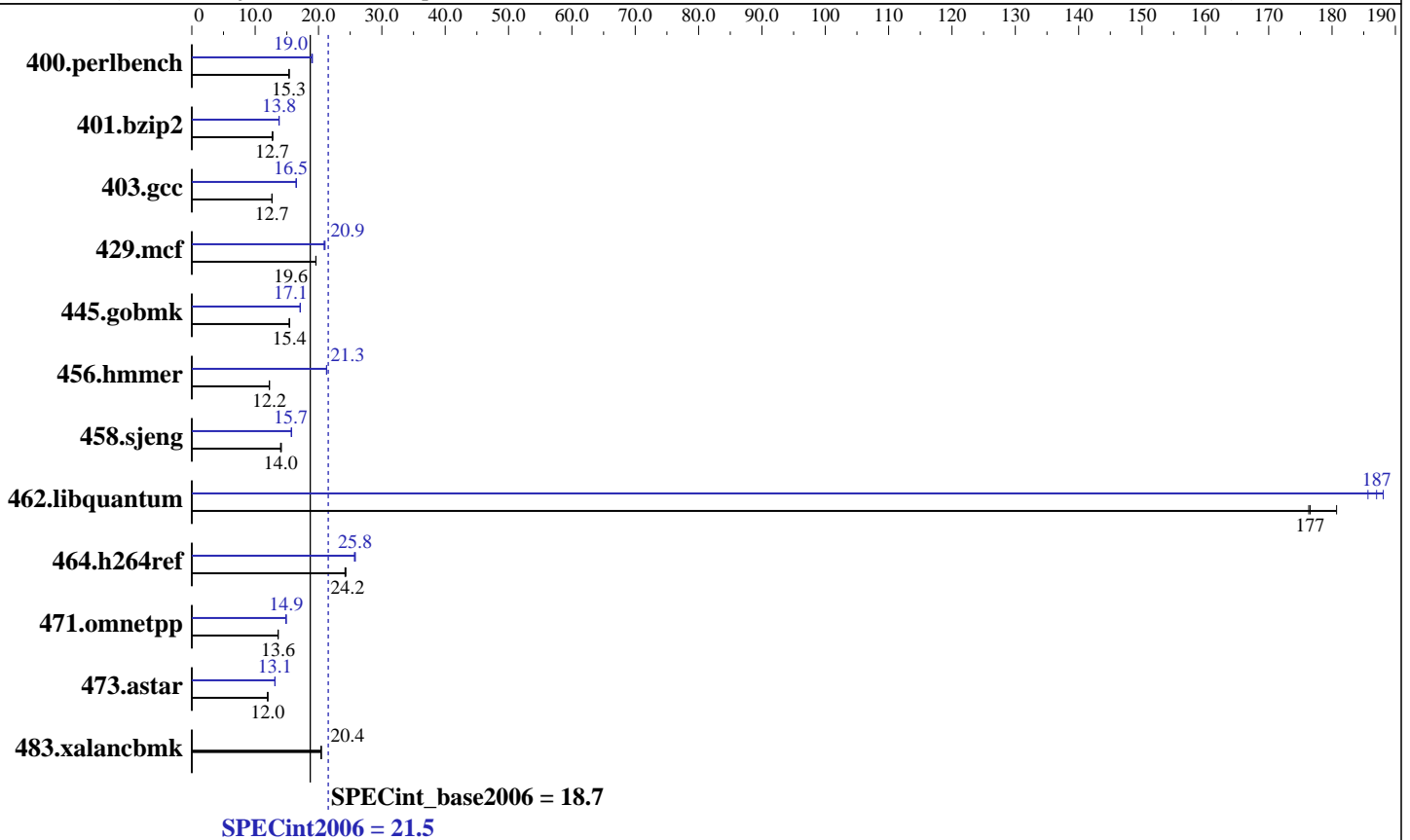
Test date: Aug-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Mar-2008

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon L5410
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)
 Disk Subsystem: 1x SAS, 73 GB, 15000 rpm
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64, Version 10.1, Build 20070913
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library, Version 8.1 binutils-2.17.50.0.5-0.1.x86_64



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	637	15.3	635	15.4	<u>637</u>	<u>15.3</u>	516	18.9	513	19.0	<u>515</u>	<u>19.0</u>
401.bzip2	760	12.7	<u>760</u>	<u>12.7</u>	754	12.8	701	13.8	<u>701</u>	<u>13.8</u>	700	13.8
403.gcc	637	12.6	635	12.7	<u>636</u>	<u>12.7</u>	488	16.5	489	16.5	<u>488</u>	<u>16.5</u>
429.mcf	465	19.6	<u>466</u>	<u>19.6</u>	466	19.6	436	20.9	434	21.0	<u>435</u>	<u>20.9</u>
445.gobmk	681	15.4	681	15.4	<u>681</u>	<u>15.4</u>	<u>613</u>	<u>17.1</u>	613	17.1	613	17.1
456.hmmer	762	12.3	<u>762</u>	<u>12.2</u>	762	12.2	<u>438</u>	<u>21.3</u>	439	21.3	438	21.3
458.sjeng	863	14.0	856	14.1	<u>862</u>	<u>14.0</u>	<u>769</u>	<u>15.7</u>	771	15.7	768	15.8
462.libquantum	<u>117</u>	<u>177</u>	115	181	118	176	<u>111</u>	<u>187</u>	110	188	112	186
464.h264ref	<u>914</u>	<u>24.2</u>	915	24.2	910	24.3	857	25.8	862	25.7	<u>859</u>	<u>25.8</u>
471.omnetpp	459	13.6	<u>459</u>	<u>13.6</u>	458	13.6	419	14.9	<u>420</u>	<u>14.9</u>	421	14.8
473.astar	588	11.9	585	12.0	<u>585</u>	<u>12.0</u>	<u>535</u>	<u>13.1</u>	535	13.1	535	13.1
483.xalancbmk	338	20.4	<u>338</u>	<u>20.4</u>	338	20.4	338	20.4	<u>338</u>	<u>20.4</u>	338	20.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)

Platform Notes

Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmcr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.01.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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