



SPEC® CINT2006 Result

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Bull SAS

NovaScale T860 E1
(Intel Xeon E5430, 2.66 GHz)

SPECint®_rate2006 = 65.5

SPECint_rate_base2006 = 55.4

CPU2006 license: 20

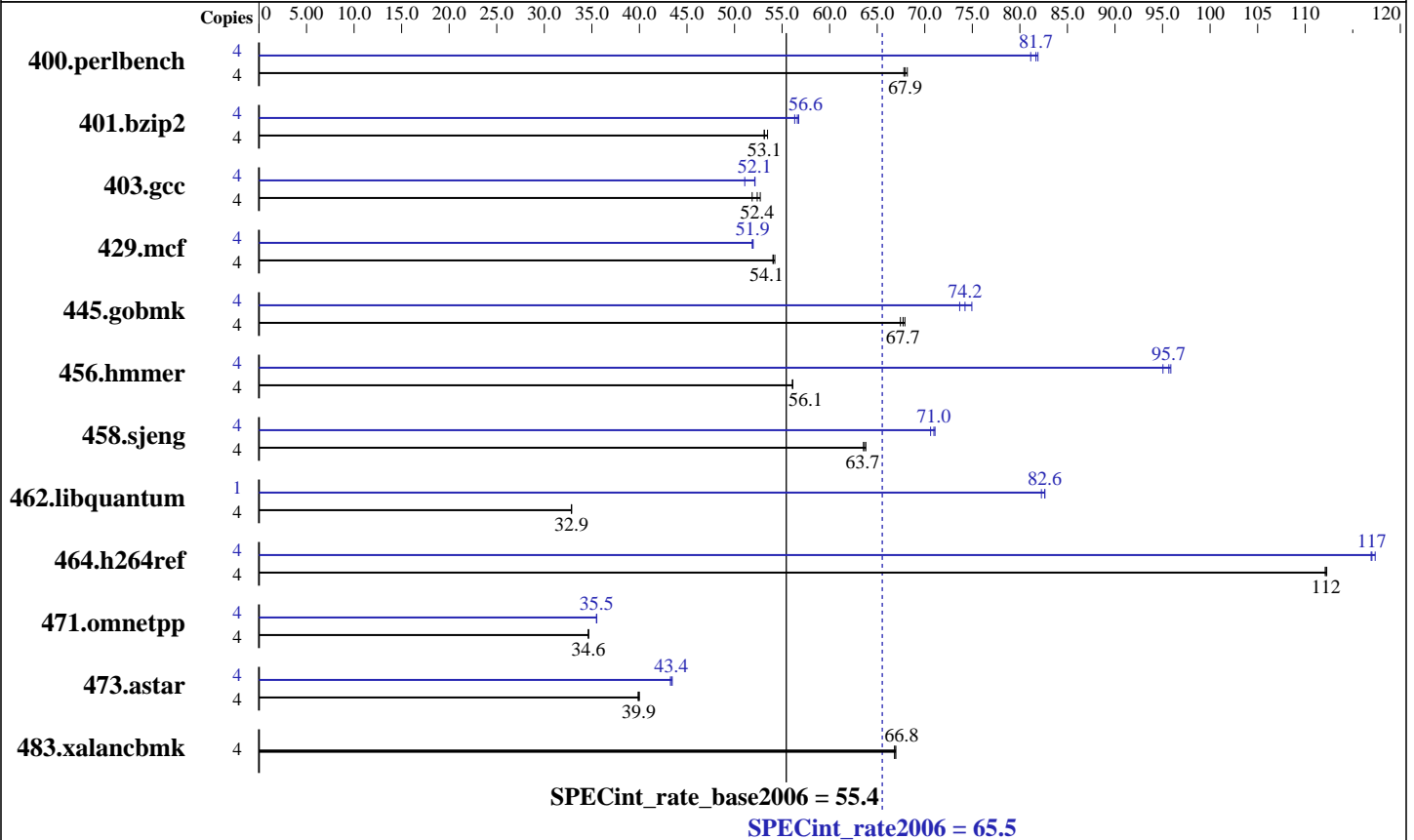
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Jul-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5430
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
 Disk Subsystem: 1x73 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 SP1
 Kernel 2.6.16.46-0.12-smp for x86_64
 Compiler: Intel C++ Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.17.50.0.15
 SmartHeap library V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	573	68.2	576	67.8	<u>575</u>	<u>67.9</u>	4	<u>478</u>	<u>81.7</u>	482	81.1	477	81.9
401.bzip2	4	722	53.5	727	53.1	<u>726</u>	<u>53.1</u>	4	685	56.3	680	56.7	<u>682</u>	<u>56.6</u>
403.gcc	4	621	51.9	611	52.7	<u>615</u>	<u>52.4</u>	4	<u>618</u>	<u>52.1</u>	630	51.1	617	52.2
429.mcf	4	<u>675</u>	<u>54.1</u>	675	54.0	673	54.2	4	703	51.9	702	52.0	<u>703</u>	<u>51.9</u>
445.gobmk	4	<u>620</u>	<u>67.7</u>	618	67.9	622	67.4	4	560	74.9	<u>565</u>	<u>74.2</u>	570	73.7
456.hammer	4	665	56.1	665	56.1	<u>665</u>	<u>56.1</u>	4	389	95.9	<u>390</u>	<u>95.7</u>	393	95.0
458.sjeng	4	759	63.8	762	63.5	<u>760</u>	<u>63.7</u>	4	681	71.1	685	70.6	<u>682</u>	<u>71.0</u>
462.libquantum	4	2521	32.9	2524	32.8	<u>2522</u>	<u>32.9</u>	1	<u>251</u>	<u>82.6</u>	251	82.6	252	82.3
464.h264ref	4	789	112	<u>789</u>	<u>112</u>	790	112	4	754	117	757	117	<u>757</u>	<u>117</u>
471.omnetpp	4	722	34.6	<u>722</u>	<u>34.6</u>	722	34.6	4	705	35.5	704	35.5	<u>704</u>	<u>35.5</u>
473.astar	4	<u>703</u>	<u>39.9</u>	702	40.0	705	39.9	4	646	43.4	649	43.3	<u>647</u>	<u>43.4</u>
483.xalancbmk	4	412	67.0	<u>413</u>	<u>66.8</u>	413	66.8	4	412	67.0	<u>413</u>	<u>66.8</u>	413	66.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```

'/usr/bin/taskset' used to bind processes to CPUs
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

```

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hammer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
               -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
           -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
                -opt-streaming-stores always -vec-guard-write
                -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
             -no-prec-div -ansi-alias -opt-ra-region-strategy=block
             -Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
           -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
           -Wl,-z,muldefs -L/spec/cpu2006/lib -lsmartheap

483.xalancbmk: basepeak = yes

```

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.00.html



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You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/EM64T_Intel101_int_flags.20090713.00.xml

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