SPEC® CINT2006 Result

PC Factory S.A.
PC2 MX 216035G PRO

$SPECint®\_rate2006 =$ 20.1

$SPECint\_rate\_base2006 =$ 19.0

Benchmark	Copies	Rate (blue)	Rate (base)
400.perlbench	2 / 2	28.6	24.8
401.bzip2	2 / 2	12.8	12.5
403.gcc	2 / 2	11.2	10.9
429.mcf	2		19.6
445.gobmk	2 / 2	25.3	22.8
456.hmmer	2 / 2	18.8	18.6
458.sjeng	2 / 2	23.5	21.2
462.libquantum	2 / 2	24.9	22.3
464.h264ref	2 / 2	36.8	35.3
471.omnetpp	2 / 2	16.3	15.2
473.astar	2 / 2	13.7	12.7
483.xalancbmk	2 / 2	24.3	24.6

SPECint_rate_base2006 = 19.0

SPECint_rate2006 = 20.1

Hardware

CPU Name:	Intel Pentium Dual Core E2160
CPU Characteristics:	800 MHz Bus Speed
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (1 GB 667MHz CL5 DDR2)
Disk Subsystem:	Hitachi 250GB, 7200RPM, SATA
Other Hardware:	None

Software

Operating System:	Microsoft Windows XP Professional
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20080312 Package ID: W_CC_P_10.1.021 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1

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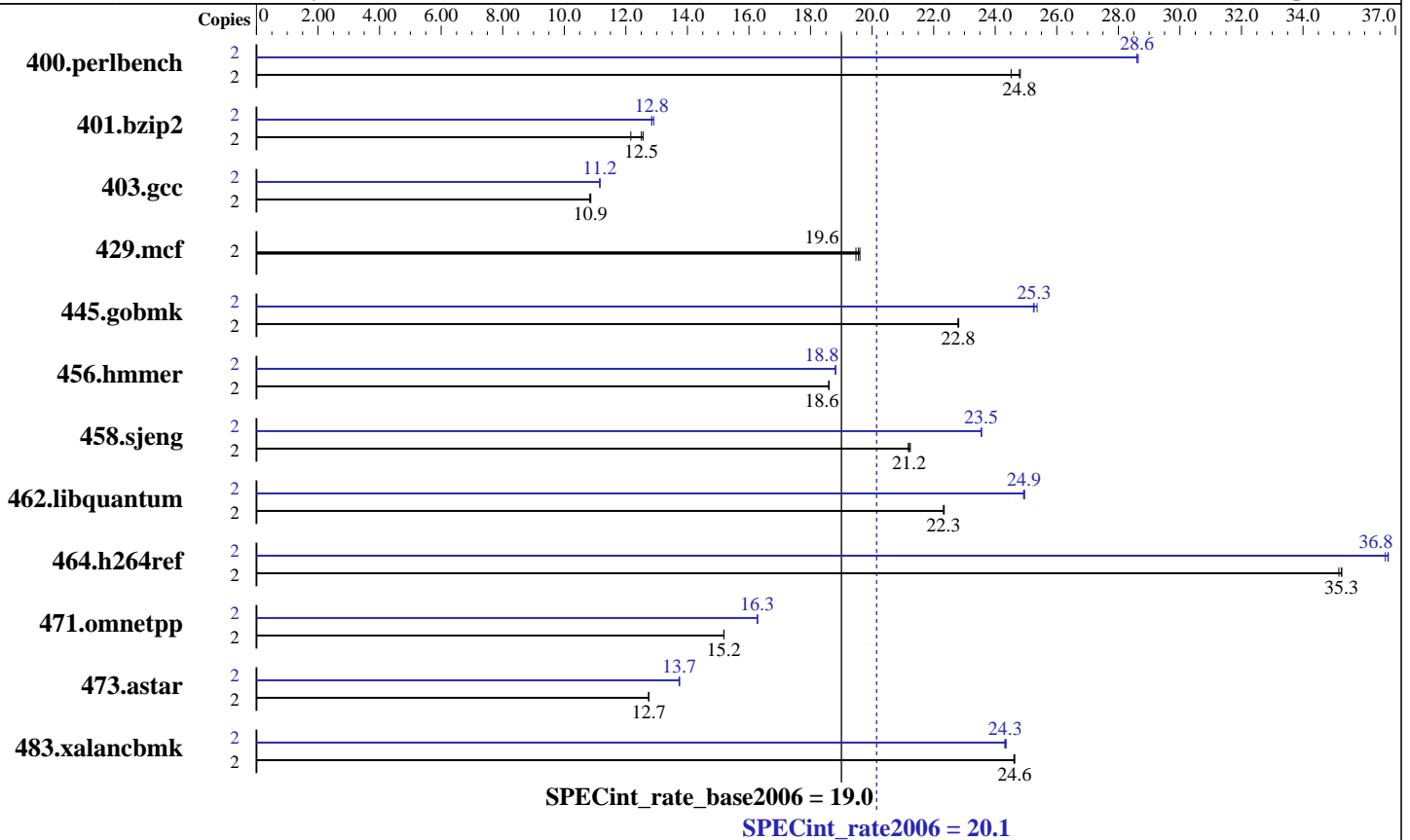
SPECint_rate_base2006 = 19.0

CPU2006 license: 3635
Test sponsor: PC Factory S.A.
Tested by: PC Factory S.A.

Test date: May-2008
Hardware Availability: Apr-2008
Software Availability: Apr-2008

Results Table

Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
			Base								Peak			
400.perlbench	2	797	24.5	**788**	**24.8**	787	24.8	2	**683**	**28.6**	682	28.6	683	28.6
401.bzip2	2	1587	12.2	1536	12.6	**1543**	**12.5**	2	1495	12.9	1503	12.8	**1503**	**12.8**
403.gcc	2	**1484**	**10.9**	1482	10.9	1487	10.8	2	1443	11.2	1443	11.2	**1443**	**11.2**
429.mcf	2	930	19.6	**933**	**19.6**	937	19.5	2	930	19.6	**933**	**19.6**	937	19.5
445.gobmk	2	920	22.8	920	22.8	**920**	**22.8**	2	827	25.4	**830**	**25.3**	831	25.2
456.hmmer	2	1004	18.6	1003	18.6	**1003**	**18.6**	2	**992**	**18.8**	992	18.8	992	18.8
458.sjeng	2	**1141**	**21.2**	1139	21.2	1143	21.2	2	1028	23.6	1028	23.5	**1028**	**23.5**
462.libquantum	2	**1856**	**22.3**	1857	22.3	1855	22.3	2	**1662**	**24.9**	1661	25.0	1663	24.9
464.h264ref	2	1255	35.3	**1256**	**35.3**	1259	35.2	2	1207	36.7	**1204**	**36.8**	1204	36.8
471.omnetpp	2	**823**	**15.2**	823	15.2	823	15.2	2	**768**	**16.3**	768	16.3	768	16.3
473.astar	2	1102	12.7	1101	12.7	**1102**	**12.7**	2	1023	13.7	**1022**	**13.7**	1021	13.7
483.xalancbmk	2	560	24.6	**560**	**24.6**	561	24.6	2	568	24.3	567	24.4	**567**	**24.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
          -link /FORCE:MULTIPLE
```

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CPU2006 license: 3635
Test sponsor: PC Factory S.A.
Tested by: PC Factory S.A.

Test date: May-2008
Hardware Availability: Apr-2008
Software Availability: Apr-2008

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias`
`-Qprefetch /F512000000 shlw32m.lib`
`-link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000`
`shlw32m.lib          -link /FORCE:MULTIPLE`

403.gcc: `Same as 401.bzip2`

429.mcf: `basepeak = yes`

445.gobmk: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo`
`-Qprec_div- -Qansi-alias /F512000000 shlw32m.lib`
`-link /FORCE:MULTIPLE`

456.hmmer: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2`
`-Qansi-alias /F512000000 shlw32m.lib`
`-link /FORCE:MULTIPLE`

458.sjeng: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4`
`/F512000000 shlw32m.lib          -link /FORCE:MULTIPLE`

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Peak Optimization Flags (Continued)

```
462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
                -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
                shlw32m.lib              -link /FORCE:MULTIPLE

   464.h264ref: Same as 456.hmmer

C++ benchmarks:
    -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
    -Qcxx_features /F512000000 shlw32m.lib
              -link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

```
   403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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