



SPEC® CFP2006 Result

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Itaotec

SPECfp®_rate2006 = 43.5

Servidor Itaotec LX201 (Intel Xeon X5460)

SPECfp_rate_base2006 = 39.6

CPU2006 license: 9001

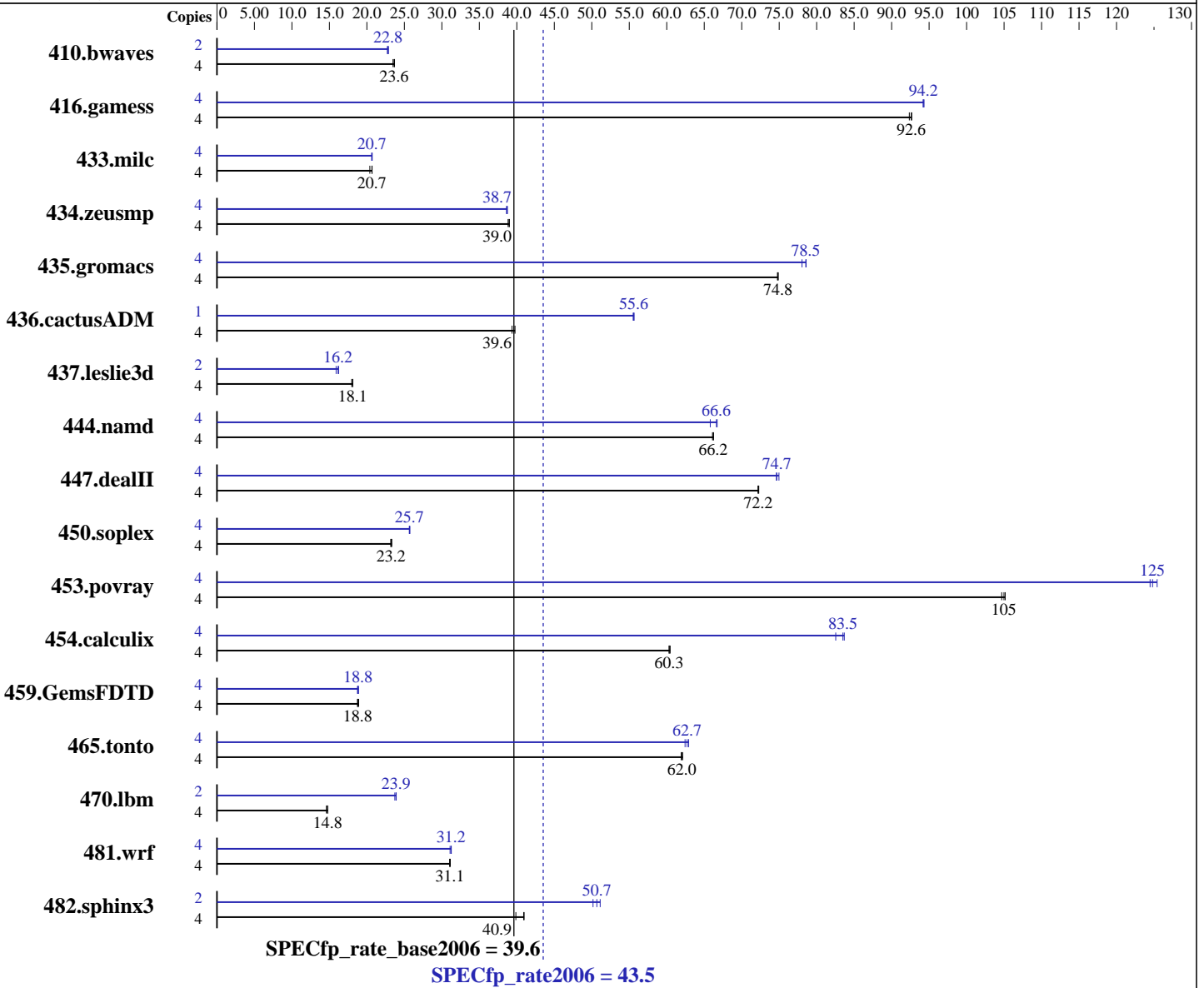
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Mar-2008

Hardware Availability: Dec-2007

Software Availability: Jan-2008



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics:
 CPU MHz: 3160
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20080112 Package ID: l_cc_p_10.1.012, l_fc_p_10.1.012
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 * 2 GB PC2-5300 FBDIMM, CL-5-5-5, ECC)
Disk Subsystem: 1 x SATA-2, 160GB, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.10.50

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	2314	23.5	2295	23.7	2300	23.6	2	1196	22.7	1191	22.8	1188	22.9		
416.gamess	4	845	92.7	848	92.4	846	92.6	4	830	94.3	831	94.2	831	94.2		
433.milc	4	1800	20.4	1774	20.7	1776	20.7	4	1779	20.6	1773	20.7	1777	20.7		
434.zeusmp	4	934	39.0	933	39.0	937	38.8	4	943	38.6	940	38.7	940	38.7		
435.gromacs	4	382	74.8	382	74.8	381	74.9	4	366	78.0	363	78.6	364	78.5		
436.cactusADM	4	1214	39.4	1202	39.8	1207	39.6	1	215	55.6	215	55.5	215	55.6		
437.leslie3d	4	2092	18.0	2074	18.1	2078	18.1	2	1159	16.2	1183	15.9	1164	16.2		
444.namd	4	485	66.2	485	66.1	484	66.2	4	481	66.7	488	65.8	482	66.6		
447.dealII	4	633	72.3	634	72.2	634	72.2	4	610	75.0	613	74.7	613	74.6		
450.soplex	4	1439	23.2	1436	23.2	1430	23.3	4	1300	25.7	1296	25.7	1299	25.7		
453.povray	4	203	105	203	105	202	105	4	171	125	170	125	171	124		
454.calculix	4	546	60.5	547	60.3	547	60.3	4	394	83.7	395	83.5	400	82.5		
459.GemsFDTD	4	2245	18.9	2259	18.8	2264	18.7	4	2247	18.9	2257	18.8	2264	18.7		
465.tonto	4	636	61.9	634	62.1	635	62.0	4	630	62.4	625	62.9	627	62.7		
470.lbm	4	3766	14.6	3723	14.8	3724	14.8	2	1149	23.9	1151	23.9	1158	23.7		
481.wrf	4	1440	31.0	1436	31.1	1438	31.1	4	1437	31.1	1430	31.2	1429	31.3		
482.sphinx3	4	1902	41.0	1905	40.9	1955	39.9	2	777	50.2	769	50.7	762	51.1		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores
KMP_STACK_SIZE set to 64M
KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
'/usr/bin/taskset' used to bind benchmark copies to processors.



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Platform Notes

BIOS configuration:
Hardware Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

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Base Optimization Flags (Continued)

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.012/bin/icc -L/opt/intel/cc/10.1.012/lib  
-I/opt/intel/cc/10.1.012/include
```

433.milc: icc

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/cc/10.1.012/bin/icpc -L/opt/intel/cc/10.1.012/lib  
-I/opt/intel/cc/10.1.012/include
```

Fortran benchmarks (except as noted below):

ifort

```
437.leslie3d: /opt/intel/fc/10.1.012/bin/ifort -L/opt/intel/fc/10.1.012/lib  
-I/opt/intel/fc/10.1.012/include
```

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```



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Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

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Peak Optimization Flags (Continued)

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic10.1-FP-intel64-linux-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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