SPEC® CFP2006 Result
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IBM Corporation

$SPECfp®\_rate2006 =$ Not Run

IBM System x3550 (Intel Xeon X5450)

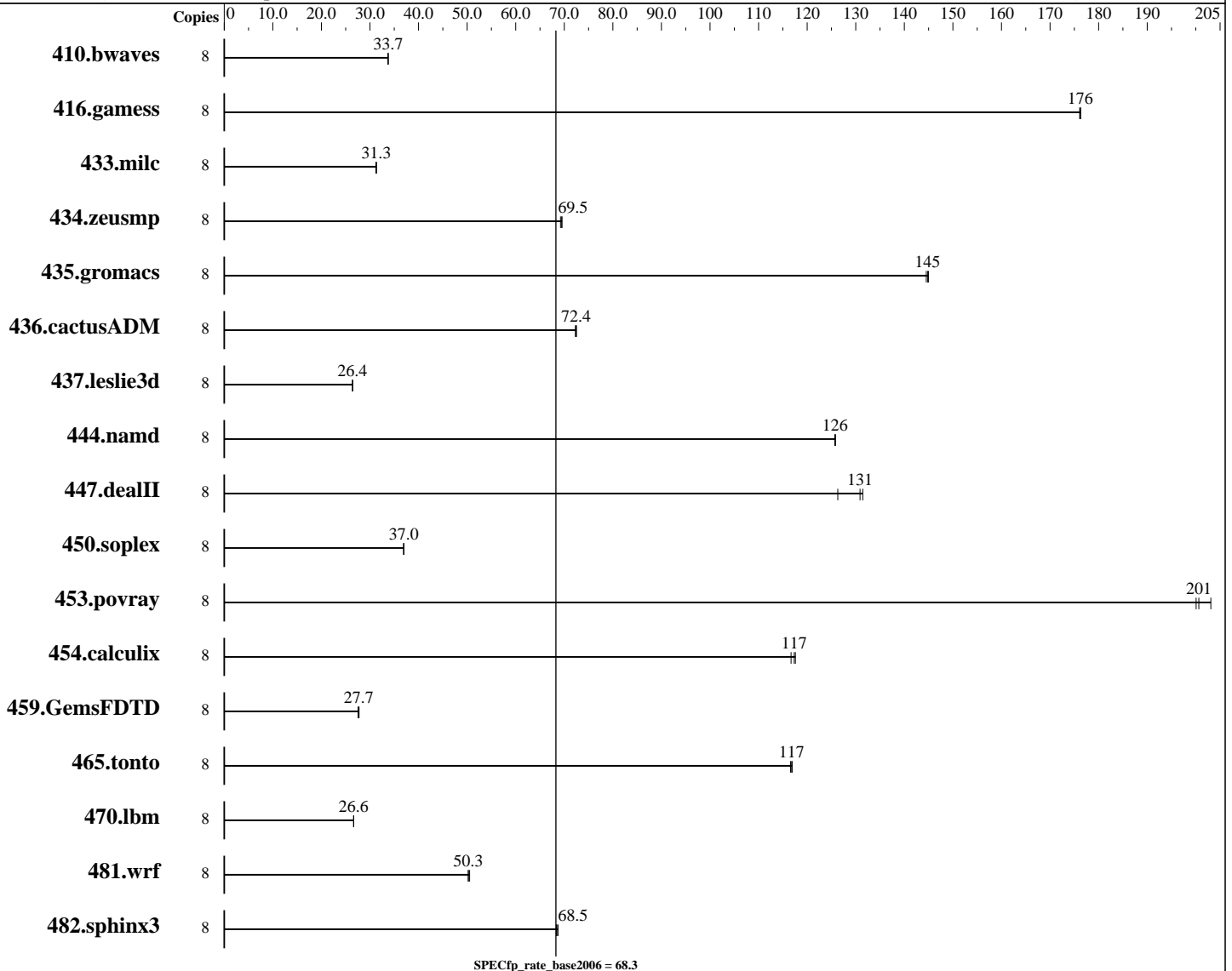$SPECfp\_rate\_base2006 =$ 68.3

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Benchmark	Copies	Result
410.bwaves	8	33.7
416.gamess	8	176
433.milc	8	31.3
434.zeusmp	8	69.5
435.gromacs	8	145
436.cactusADM	8	72.4
437.leslie3d	8	26.4
444.namd	8	126
447.dealII	8	131
450.soplex	8	37.0
453.povray	8	201
454.calculix	8	117
459.GemsFDTD	8	27.7
465.tonto	8	117
470.lbm	8	26.6
481.wrf	8	50.3
482.sphinx3	8	68.5

SPECfp_rate_base2006 = 68.3

Hardware

CPU Name: Intel Xeon X5450
CPU Characteristics: 1333MHz system bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel: No
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 64-bit

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L3 Cache: None	Peak Pointers: Not Applicable
Other Cache: None	Other Software: Binutils 2.17.50.0.15
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)	
Disk Subsystem: 1 x 36 GB SAS, 15000 RPM	
Other Hardware: None	

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3225	33.7	**3222**	**33.7**	3222	33.7							
416.gamess	8	**889**	**176**	888	176	890	176							
433.milc	8	2343	31.3	2352	31.2	**2348**	**31.3**							
434.zeusmp	8	1052	69.2	1047	69.5	**1048**	**69.5**							
435.gromacs	8	395	145	**394**	**145**	394	145							
436.cactusADM	8	**1320**	**72.4**	1318	72.5	1323	72.3							
437.leslie3d	8	2847	26.4	**2846**	**26.4**	2845	26.4							
444.namd	8	**510**	**126**	510	126	510	126							
447.dealII	8	696	131	725	126	**699**	**131**							
450.soplex	8	1808	36.9	1803	37.0	**1805**	**37.0**							
453.povray	8	213	200	**212**	**201**	210	203							
454.calculix	8	561	118	566	117	**562**	**117**							
459.GemsFDTD	8	3059	27.8	3081	27.6	**3064**	**27.7**							
465.tonto	8	673	117	**674**	**117**	675	117							
470.lbm	8	4127	26.6	**4126**	**26.6**	4124	26.7							
481.wrf	8	1769	50.5	**1778**	**50.3**	1779	50.2							
482.sphinx3	8	2269	68.7	**2277**	**68.5**	2280	68.4							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
Powersaved dameon was disabled in OS
taskset utility used to bind CPU(s) to processes
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-fast
```

C++ benchmarks:
```
-fast
```

Fortran benchmarks:
```
-fast
```

Benchmarks using both Fortran and C:
```
-fast
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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