# SPEC® CINT2006 Result



## Fujitsu Siemens Computers

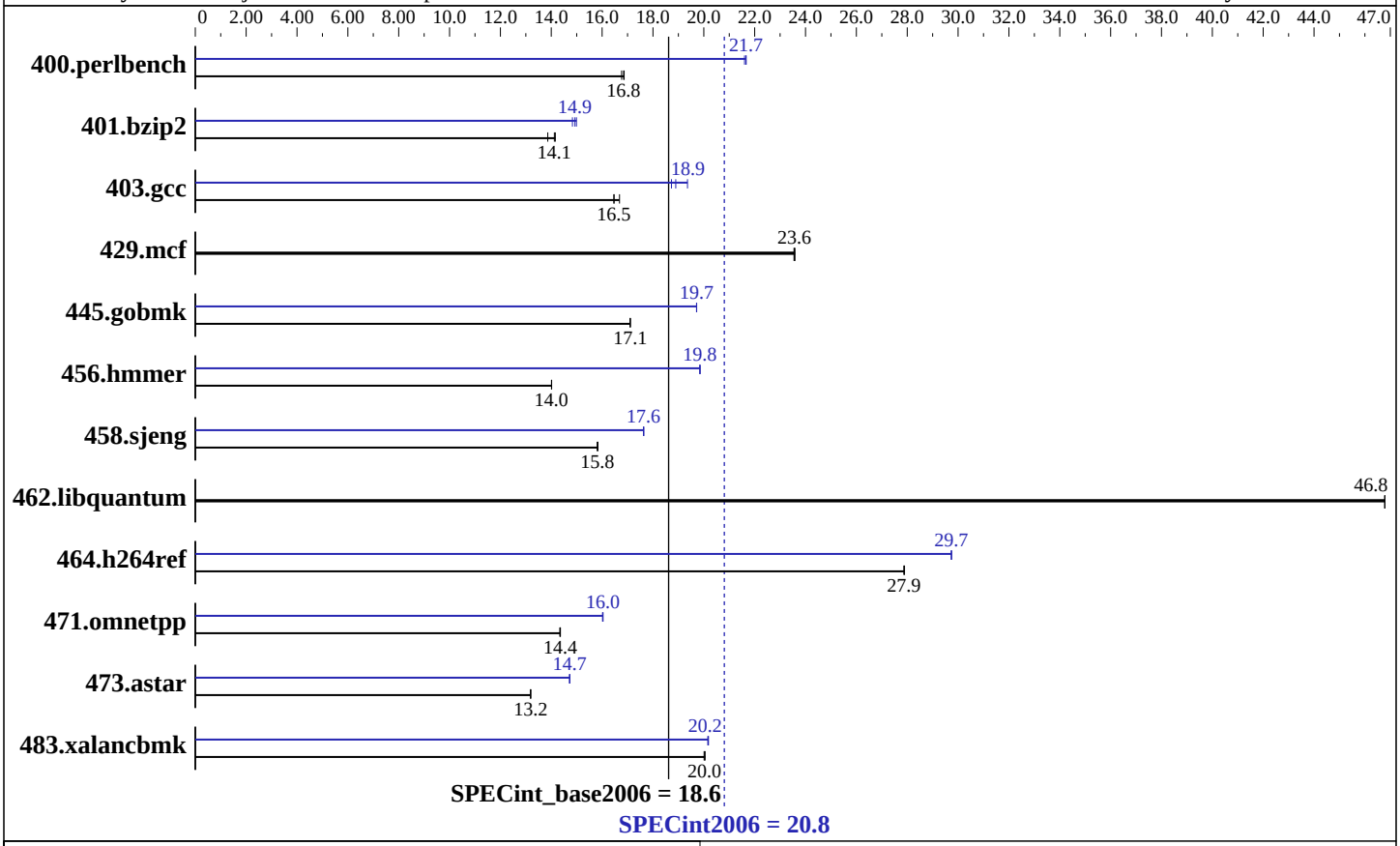
CELSIUS M460, Intel Core 2 Quad Q6700 processor

SPECint®2006 = 20.8

SPECint_base2006 = 18.6

**CPU2006 license:** 22
**Test sponsor:** Fujitsu Siemens Computers
**Tested by:** Fujitsu Siemens Computers

**Test date:** Dec-2007
**Hardware Availability:** Nov-2007
**Software Availability:** Nov-2007




### Hardware

| | |
|---|---|
| CPU Name: | Intel Core 2 Quad Q6700 |
| CPU Characteristics: | |
| CPU MHz: | 2667 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 8 MB I+D on chip per chip, 4 MB shared / 2 cores |
| L3 Cache: | None |
| Other Cache: | None |
| Memory: | 4 GB (4x1 GB PC2-6400 CL6 SDRAM) |
| Disk Subsystem: | 1 x 400 GB SATA II 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Microsoft Windows Vista Ultimate (x64) |
| Compiler: | Intel C++ Compilers for IA-32 and for Intel64, Version 10.1, Build 20070913 Microsoft Visual Studio 2005 with SP1 (for libraries) |
| Auto Parallel: | Yes |
| File System: | NTFS |
| System State: | Default |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | MicroQuill SmartHeap Library, Version 8.1 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 583 | 16.8 | 579 | 16.9 | **580** | **16.8** | **451** | **21.7** | 452 | 21.6 | 451 | 21.7 |
| 401.bzip2 | **683** | **14.1** | 696 | 13.9 | 681 | 14.2 | 651 | 14.8 | **646** | **14.9** | 644 | 15.0 |
| 403.gcc | **489** | **16.5** | 489 | 16.5 | 483 | 16.7 | 430 | 18.7 | 416 | 19.4 | **426** | **18.9** |
| 429.mcf | 387 | 23.6 | **387** | **23.6** | 387 | 23.5 | 387 | 23.6 | **387** | **23.6** | 387 | 23.5 |
| 445.gobmk | 613 | 17.1 | 613 | 17.1 | **613** | **17.1** | **532** | **19.7** | 532 | 19.7 | 532 | 19.7 |
| 456.hmmer | **666** | **14.0** | 666 | 14.0 | 666 | 14.0 | 470 | 19.9 | 470 | 19.8 | **470** | **19.8** |
| 458.sjeng | 764 | 15.8 | **765** | **15.8** | 765 | 15.8 | 686 | 17.6 | **686** | **17.6** | 687 | 17.6 |
| 462.libquantum | 443 | 46.8 | **443** | **46.8** | 443 | 46.8 | 443 | 46.8 | **443** | **46.8** | 443 | 46.8 |
| 464.h264ref | 793 | 27.9 | 794 | 27.9 | **794** | **27.9** | **744** | **29.7** | 744 | 29.7 | 744 | 29.7 |
| 471.omnetpp | 435 | 14.4 | **435** | **14.4** | 436 | 14.3 | **390** | **16.0** | 390 | 16.0 | 390 | 16.0 |
| 473.astar | 532 | 13.2 | **532** | **13.2** | 532 | 13.2 | **477** | **14.7** | 477 | 14.7 | 476 | 14.7 |
| 483.xalancbmk | 344 | 20.0 | 345 | 20.0 | **344** | **20.0** | 342 | 20.2 | 342 | 20.2 | **342** | **20.2** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
OMP_NUM_THREADS set to number of cores (default).
```

## Platform Notes

```
BIOS default settings have been used.
```

## General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.


For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

## Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

## Base Optimization Flags

C benchmarks:
`-fast -Qparallel -Qvec-guard-write -Qpar-runtime-control -F512000000 libguide40.lib`

C++ benchmarks:
`-fast -Qcxx-features -F512000000 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icl -Qvc8 -Qc99`

401.bzip2:
```
C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"
```

456.hmmer:
```
C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Bin\\icl.exe
-IC:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Include
-link -LIBPATH:C:\\DevelTools\\Intel\\Compiler\\C++\\10.1.011\\EM64T\\Lib
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib"
-link -LIBPATH:"C:\\Program Files\\Microsoft Visual Studio 8\\vc\\lib\\amd64"
```

C++ benchmarks:
`icl -Qvc8`

## Peak Portability Flags

401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_WIN32
456.hmmer: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel -Qpar-runtime-control -Qansi-alias -Qprefetch -F512000000 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch -F512000000 libguide40.lib

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F512000000 libguide40.lib

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -O2 -Qipo -QxT -Qprec-div- -Qansi-alias -F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive -F512000000 libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 -F512000000 libguide40.lib

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -Qansi-alias -Qopt-ra-region-strategy=block -F512000000 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -Qansi-alias -Qopt-ra-region-strategy=routine -F512000000 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE

Continued on next page

## Peak Optimization Flags (Continued)

483.xalancbmk: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx-features -Qansi-alias -F512000000 libguide40.lib shlW32M.lib -link -FORCE:MULTIPLE`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.01.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.01.xml`



For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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