



SPEC® CFP2006 Result

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Dell Inc.

SPECfp®_rate2006 = 57.1

Dell Precision T7400 (Intel Xeon X5272, 3.40 GHz)

SPECfp_rate_base2006 = 52.2

CPU2006 license: 55

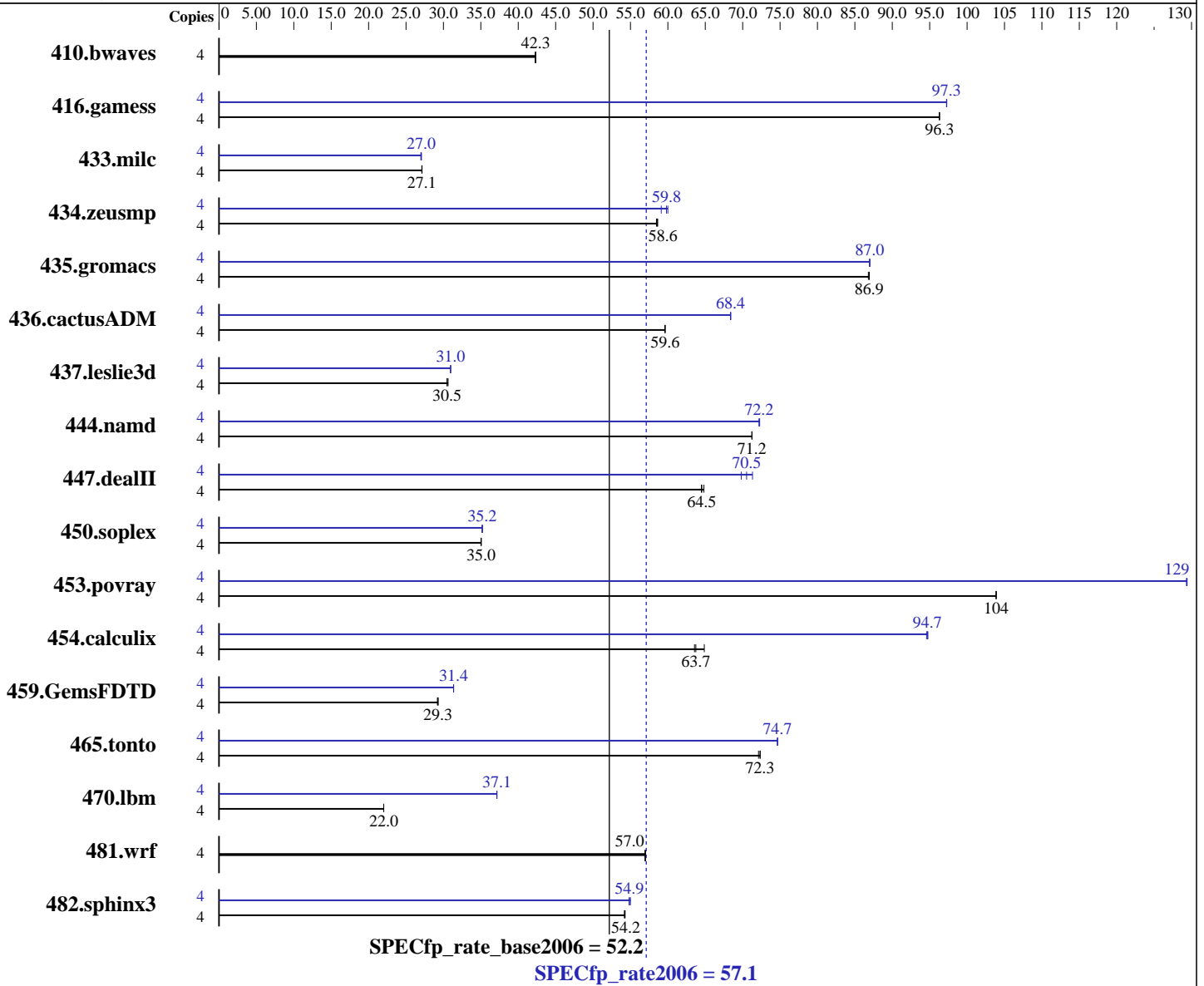
Test date: Dec-2007

Test sponsor: Dell Inc.

Hardware Availability: Jan-2008

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5272
 CPU Characteristics: 1600 MHz Bus Speed
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip

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Software

Operating System: Windows XP Professional x64 Edition SP2
 Compiler: Intel C++ Compiler for Intel 64, Version 10.1
 Build 20070809 Package ID: w_cc_p_10.1.011
 Intel Visual Fortran Compiler for Intel 64,
 Version 10.0
 Build 20070809 Package ID: w_fc_p_10.1.011
 Microsoft Visual Studio 2005 SP1

Auto Parallel: No
 File System: NTFS

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L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB 800 MHz FB-DIMM CL5)
 Disk Subsystem: 1 x 73 GB SAS 15K RPM
 Other Hardware: None

System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1 for x64

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1284	42.3	1284	42.3	1286	42.3	4	1284	42.3	1284	42.3	1286	42.3
416.gamess	4	813	96.3	813	96.3	813	96.3	4	805	97.3	805	97.3	805	97.3
433.milc	4	1354	27.1	1353	27.1	1354	27.1	4	1357	27.1	1358	27.0	1361	27.0
434.zeusmp	4	621	58.6	621	58.6	623	58.5	4	607	60.0	616	59.1	609	59.8
435.gromacs	4	329	86.9	329	86.8	329	86.9	4	328	87.0	328	87.0	328	87.0
436.cactusADM	4	802	59.6	802	59.6	802	59.6	4	699	68.4	699	68.4	699	68.4
437.leslie3d	4	1234	30.5	1228	30.6	1233	30.5	4	1214	31.0	1215	31.0	1215	30.9
444.namd	4	450	71.2	450	71.2	450	71.2	4	444	72.3	444	72.2	444	72.2
447.dealII	4	706	64.8	709	64.5	710	64.5	4	642	71.3	656	69.8	649	70.5
450.soplex	4	951	35.1	953	35.0	952	35.0	4	948	35.2	947	35.2	948	35.2
453.povray	4	205	104	205	104	205	104	4	165	129	164	129	165	129
454.calculix	4	518	63.7	519	63.6	509	64.9	4	348	94.7	349	94.6	348	94.8
459.GemsFDTD	4	1449	29.3	1454	29.2	1450	29.3	4	1353	31.4	1354	31.4	1353	31.4
465.tonto	4	546	72.1	544	72.3	544	72.3	4	527	74.6	527	74.7	527	74.7
470.lbm	4	2496	22.0	2496	22.0	2496	22.0	4	1480	37.1	1480	37.1	1480	37.1
481.wrf	4	785	57.0	784	57.0	784	57.0	4	785	57.0	784	57.0	784	57.0
482.sphinx3	4	1437	54.2	1439	54.2	1436	54.3	4	1421	54.9	1422	54.8	1417	55.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries were built on Windows Vista Ultimate (64-bit)

BIOS Settings

Adjacent Cache Line Prefetch set to ON

Base Compiler Invocation

C benchmarks:
icl -Qstd=c99

C++ benchmarks:
icl

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
 416.gamess: -DSPEC_CPU_P64
 433.milc: -DSPEC_CPU_P64
 434.zeusmp: -DSPEC_CPU_P64
 435.gromacs: -DSPEC_CPU_P64
 436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
 437.leslie3d: -DSPEC_CPU_P64
 444.namd: -DSPEC_CPU_P64 /TP
 447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 450.soplex: -DSPEC_CPU_P64
 453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
 459.GemsFDTD: -DSPEC_CPU_P64
 465.tonto: -DSPEC_CPU_P64
 470.lbm: -DSPEC_CPU_P64
 481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-fast -Qauto-ilp32 /F1000000000 shlw64m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qauto-ilp32 -Qcxx_features /F1000000000 shlw64m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-fast -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE



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Peak Compiler Invocation

C benchmarks:
icl -Qstd=c99

C++ benchmarks:
icl

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
          -Qunroll2 -Oa /F1000000000 shlw64m.lib
          -link /FORCE:MULTIPLE
```

```
470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
          -Qunroll2 -Qscalar-rep- -Qprefetch /F1000000000
          shlw64m.lib -link /FORCE:MULTIPLE
```

```
482.sphinx3: -fast -Qauto-ilp32 -Qunroll2 /F1000000000 shlw64m.lib
             -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
          -Oa -Qcxx_features /F1000000000 shlw64m.lib
          -link /FORCE:MULTIPLE
```

```
447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
            -Qunroll2 -Qprefetch -Qcxx_features /F1000000000
            shlw64m.lib -link /FORCE:MULTIPLE
```

```
450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
            -Qcxx_features /F1000000000 shlw64m.lib
            -link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

```
453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
           -Qunroll4 -Qansi-alias -Qcxx_features /F1000000000
           shlw64m.lib -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
410.bwaves: basepeak = yes
```

```
416.gamess: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
           -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000
           -link /FORCE:MULTIPLE
```

```
434.zeusmp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qprec-div-
           -Qunroll0 -Qscalar-rep- /F1000000000
           -link /FORCE:MULTIPLE
```

```
437.leslie3d: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
            -Qprefetch /F1000000000 -link /FORCE:MULTIPLE
```

```
459.GemsFDTD: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
            -Qunroll2 -Ob0 -Qprefetch /F1000000000
            -link /FORCE:MULTIPLE
```

```
465.tonto: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
           -Qunroll4 -Qauto /F1000000000
           -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:

```
435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
            -Oa -Qprefetch /F1000000000
            -link /FORCE:MULTIPLE
```

```
436.cactusADM: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qauto-ilp32
              -Qunroll2 -Qprefetch /F1000000000
              -link /FORCE:MULTIPLE
```

```
454.calculix: -fast -Qauto-ilp32 -Qunroll-aggressive /F1000000000
            -link /FORCE:MULTIPLE
```

```
481.wrf: basepeak = yes
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.xml>



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