



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY RX300 S4, Intel Xeon processor E5205, 1.86 GHz

SPECint®_rate2006 = 52.6

SPECint_rate_base2006 = 45.0

CPU2006 license: 22

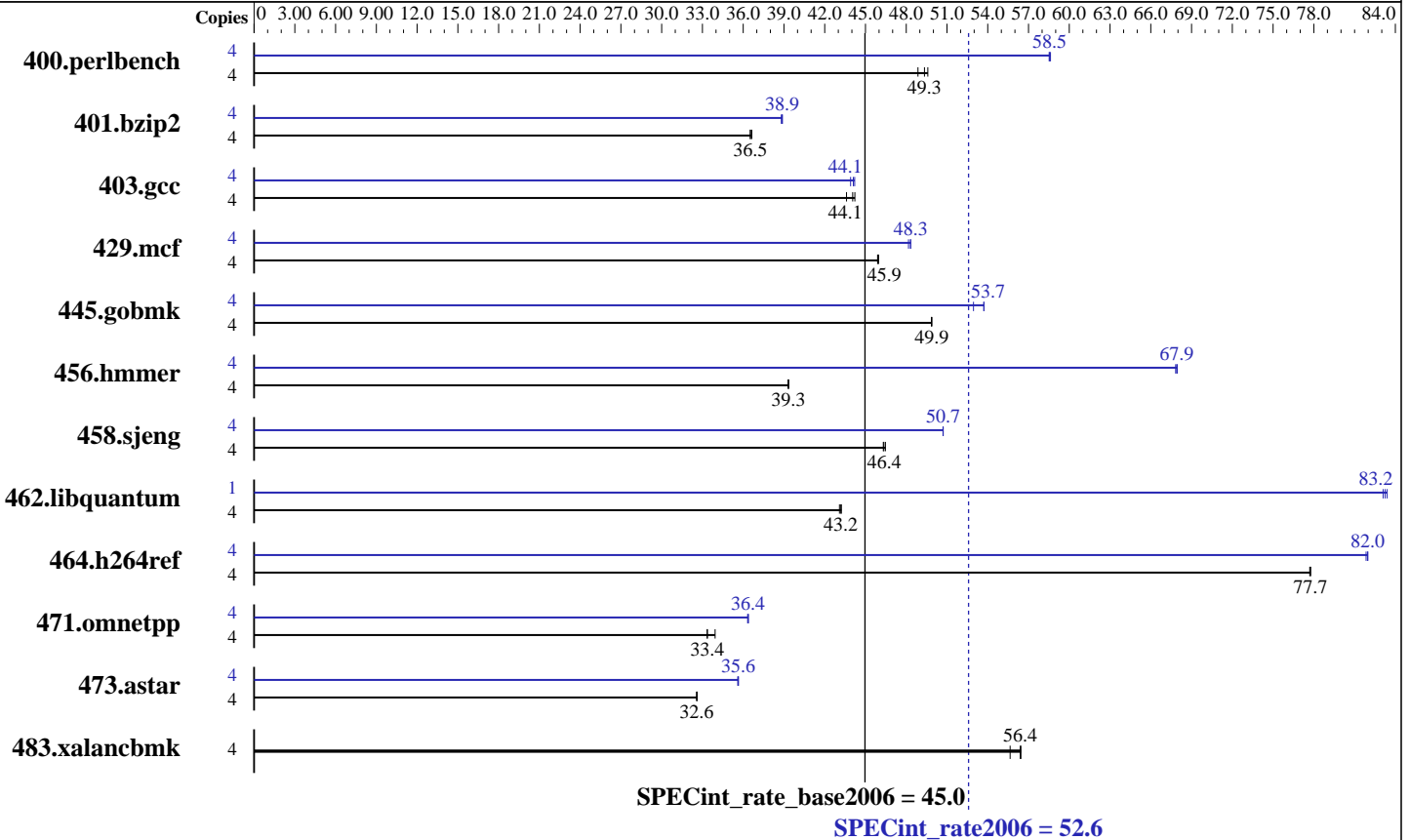
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5205
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
 Auto Parallel: Yes
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|-------------|-------------|-------------|-------------|------------|-------------|--------|------------|-------------|-------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | 788 | 49.6 | 800 | 48.9 | <u>792</u> | <u>49.3</u> | 4 | <u>667</u> | <u>58.5</u> | 667 | 58.6 | 668 | 58.5 |
| 401.bzip2 | 4 | <u>1056</u> | <u>36.5</u> | 1054 | 36.6 | 1057 | 36.5 | 4 | 995 | 38.8 | <u>993</u> | <u>38.9</u> | 993 | 38.9 |
| 403.gcc | 4 | <u>731</u> | <u>44.1</u> | 738 | 43.6 | 728 | 44.2 | 4 | <u>730</u> | <u>44.1</u> | 728 | 44.2 | 733 | 43.9 |
| 429.mcf | 4 | <u>794</u> | <u>45.9</u> | 795 | 45.9 | 794 | 46.0 | 4 | 755 | 48.3 | <u>755</u> | <u>48.3</u> | 757 | 48.2 |
| 445.gobmk | 4 | 841 | 49.9 | 842 | 49.9 | <u>841</u> | <u>49.9</u> | 4 | 781 | 53.7 | <u>781</u> | <u>53.7</u> | 792 | 53.0 |
| 456.hmmmer | 4 | 949 | 39.3 | 950 | 39.3 | <u>949</u> | <u>39.3</u> | 4 | <u>550</u> | <u>67.9</u> | 549 | 67.9 | 550 | 67.8 |
| 458.sjeng | 4 | 1045 | 46.3 | <u>1044</u> | <u>46.4</u> | 1042 | 46.5 | 4 | 955 | 50.7 | <u>954</u> | <u>50.7</u> | 954 | 50.7 |
| 462.libquantum | 4 | 1924 | 43.1 | <u>1919</u> | <u>43.2</u> | 1917 | 43.2 | 1 | 248 | 83.4 | <u>249</u> | <u>83.2</u> | 249 | 83.1 |
| 464.h264ref | 4 | 1138 | 77.8 | <u>1139</u> | <u>77.7</u> | 1139 | 77.7 | 4 | 1082 | 81.8 | <u>1080</u> | <u>82.0</u> | 1080 | 82.0 |
| 471.omnetpp | 4 | 737 | 33.9 | 750 | 33.4 | <u>749</u> | <u>33.4</u> | 4 | 687 | 36.4 | <u>687</u> | <u>36.4</u> | 688 | 36.3 |
| 473.astar | 4 | 861 | 32.6 | <u>862</u> | <u>32.6</u> | 862 | 32.6 | 4 | <u>788</u> | <u>35.6</u> | 788 | 35.7 | 789 | 35.6 |
| 483.xalancbmk | 4 | 496 | 55.6 | 489 | 56.4 | <u>489</u> | <u>56.4</u> | 4 | 496 | 55.6 | 489 | 56.4 | <u>489</u> | <u>56.4</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-alloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.06.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.06.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:34:36 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 December 2007.