



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 61.3

PowerEdge R200 (Intel Xeon X3230, 2.66 GHz)

SPECint_rate_base2006 = 53.6

CPU2006 license: 55

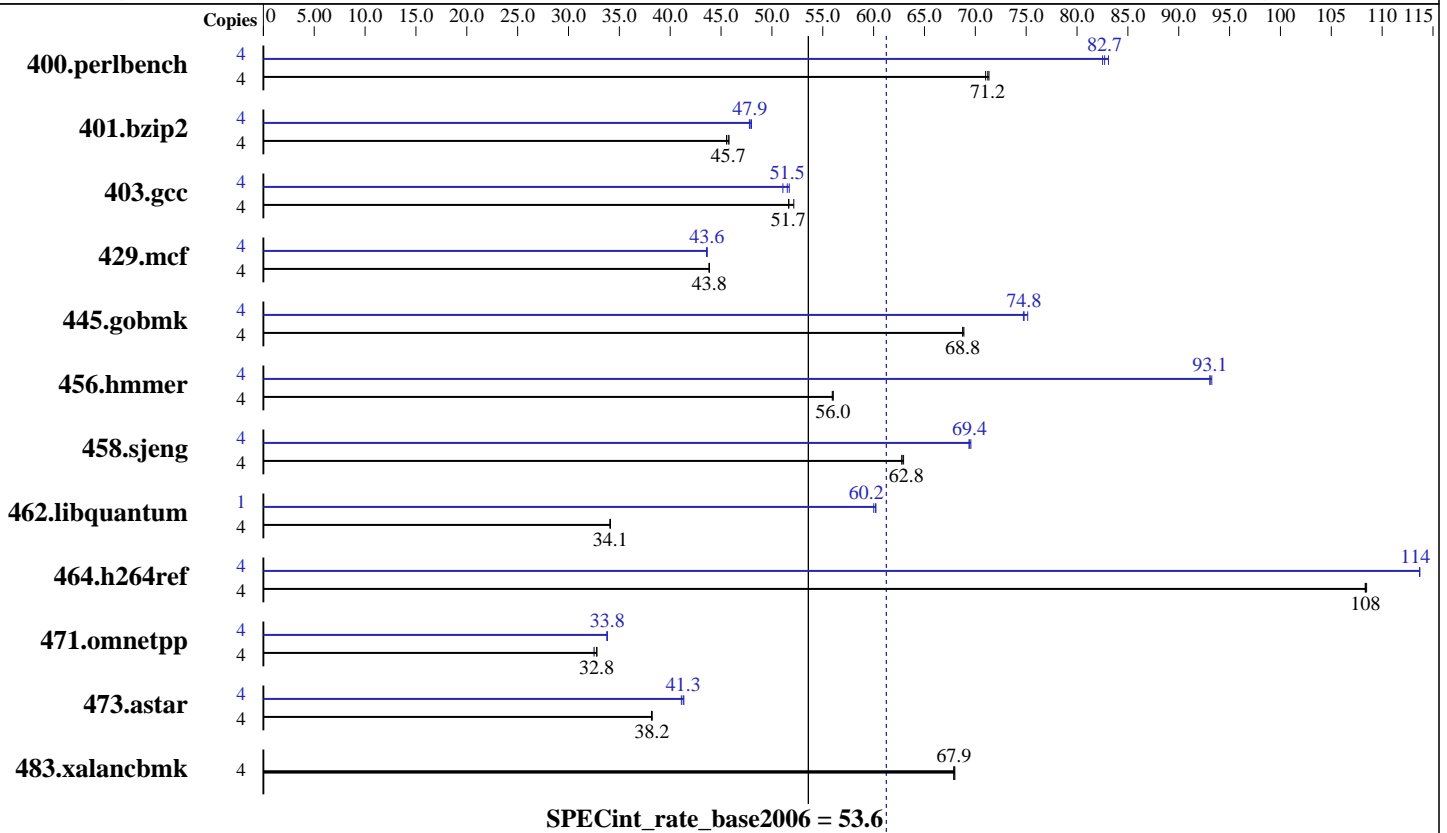
Test date: Nov-2007

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X3230
 CPU Characteristics: 1066 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2GB 800MHz DDR2)
 Disk Subsystem: 73GB SAS, 10K RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	550	71.0	548	71.3	<u>549</u>	<u>71.2</u>	4	<u>472</u>	<u>82.7</u>	474	82.5	470	83.1
401.bzip2	4	<u>845</u>	<u>45.7</u>	843	45.8	848	45.5	4	<u>806</u>	<u>47.9</u>	808	47.8	804	48.0
403.gcc	4	<u>623</u>	<u>51.7</u>	617	52.2	623	51.6	4	623	51.7	<u>625</u>	<u>51.5</u>	630	51.1
429.mcf	4	<u>832</u>	<u>43.8</u>	832	43.9	832	43.8	4	<u>836</u>	<u>43.6</u>	836	43.6	837	43.6
445.gobmk	4	610	68.8	609	68.9	<u>610</u>	<u>68.8</u>	4	562	74.7	558	75.2	<u>561</u>	<u>74.8</u>
456.hammer	4	666	56.0	<u>666</u>	<u>56.0</u>	667	55.9	4	<u>401</u>	<u>93.1</u>	401	93.1	400	93.2
458.sjeng	4	<u>771</u>	<u>62.8</u>	769	62.9	771	62.8	4	696	69.6	<u>697</u>	<u>69.4</u>	698	69.4
462.libquantum	4	2429	34.1	2432	34.1	<u>2430</u>	<u>34.1</u>	1	344	60.2	<u>344</u>	<u>60.2</u>	345	60.0
464.h264ref	4	817	108	816	108	<u>817</u>	<u>108</u>	4	779	114	<u>779</u>	<u>114</u>	779	114
471.omnetpp	4	769	32.5	762	32.8	<u>763</u>	<u>32.8</u>	4	<u>739</u>	<u>33.8</u>	739	33.8	740	33.8
473.astar	4	<u>735</u>	<u>38.2</u>	736	38.2	735	38.2	4	683	41.1	679	41.3	<u>680</u>	<u>41.3</u>
483.xalancbmk	4	<u>406</u>	<u>67.9</u>	406	68.0	407	67.9	4	<u>406</u>	<u>67.9</u>	406	68.0	407	67.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

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Peak Optimization Flags (Continued)

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.xml>



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