



SPEC® CINT2006 Result

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Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor E5450,
3.00 GHz)

SPECint®2006 = 26.4

SPECint_base2006 = 23.0

CPU2006 license: 13

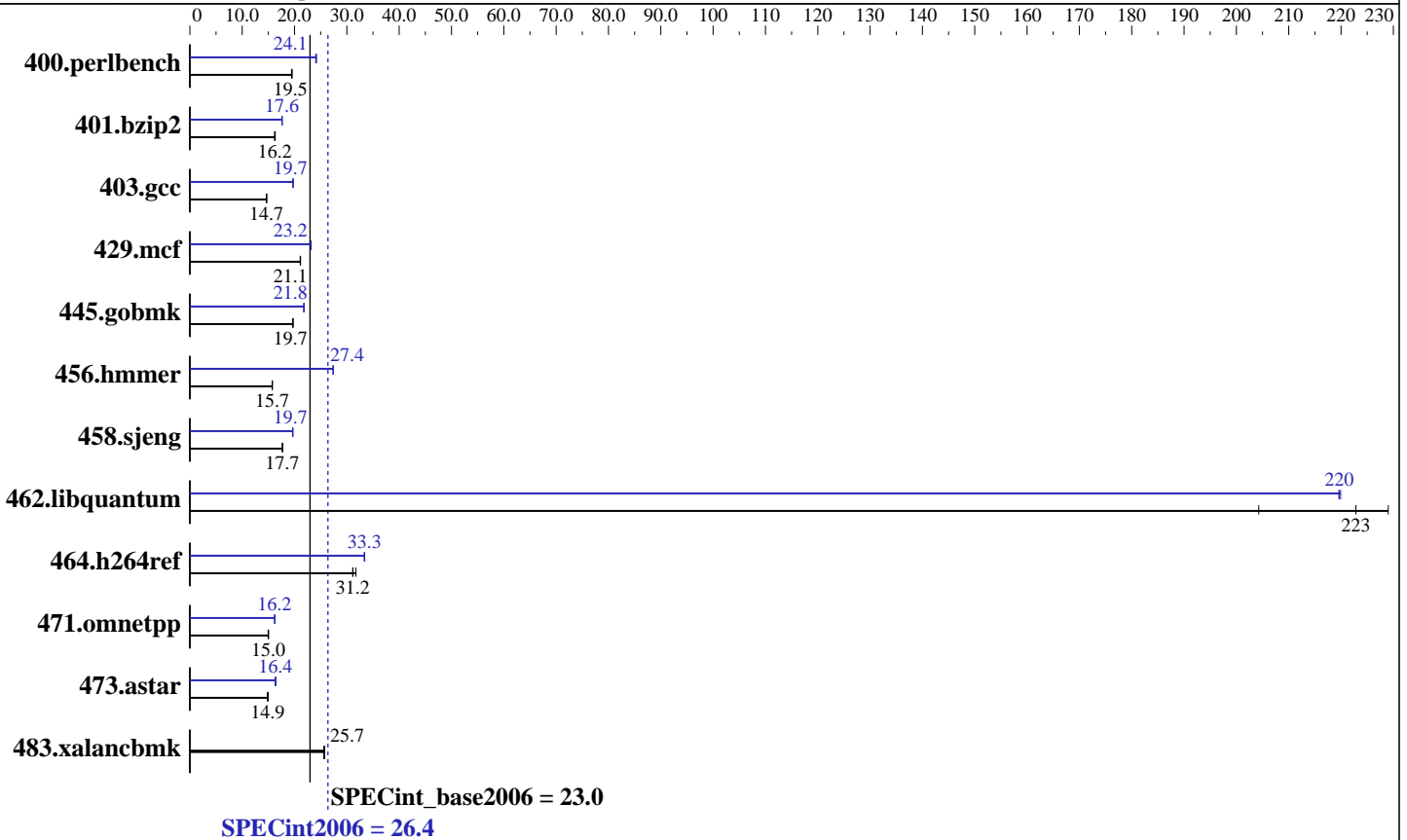
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5450
 CPU Characteristics: Quad Core, 3.00 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73GB Seagate ST37330LC SCSI 10K RPM
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10 SP1 RC1, Kernel linux-cbgn 2.6.16.43-0.5-smp for x86_64
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>501</u>	<u>19.5</u>	503	19.4	500	19.5	406	24.1	403	24.2	<u>406</u>	<u>24.1</u>
401.bzip2	596	16.2	<u>594</u>	<u>16.2</u>	591	16.3	548	17.6	547	17.6	<u>547</u>	<u>17.6</u>
403.gcc	551	14.6	<u>549</u>	<u>14.7</u>	547	14.7	409	19.7	<u>408</u>	<u>19.7</u>	408	19.8
429.mcf	<u>432</u>	<u>21.1</u>	433	21.1	431	21.1	394	23.2	394	23.1	<u>394</u>	<u>23.2</u>
445.gobmk	532	19.7	<u>533</u>	<u>19.7</u>	533	19.7	481	21.8	<u>481</u>	<u>21.8</u>	481	21.8
456.hmmer	<u>592</u>	<u>15.7</u>	592	15.8	593	15.7	340	27.4	341	27.4	<u>341</u>	<u>27.4</u>
458.sjeng	681	17.8	685	17.7	<u>685</u>	<u>17.7</u>	617	19.6	<u>615</u>	<u>19.7</u>	614	19.7
462.libquantum	<u>93.0</u>	<u>223</u>	101	204	90.5	229	94.2	220	94.4	220	<u>94.3</u>	<u>220</u>
464.h264ref	697	31.7	711	31.1	<u>710</u>	<u>31.2</u>	<u>664</u>	<u>33.3</u>	662	33.4	664	33.3
471.omnetpp	416	15.0	418	15.0	<u>416</u>	<u>15.0</u>	385	16.2	<u>386</u>	<u>16.2</u>	386	16.2
473.astar	474	14.8	469	15.0	<u>471</u>	<u>14.9</u>	431	16.3	427	16.4	<u>428</u>	<u>16.4</u>
483.xalanbmk	268	25.7	269	25.6	<u>269</u>	<u>25.7</u>	268	25.7	269	25.6	<u>269</u>	<u>25.7</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:

Hardware Prefetcher: Enabled

Adjacent Sector Prefetch: Enabled

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll14 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.36.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.36.xml>

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For other inquiries, please contact webmaster@spec.org.

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