



SPEC® CINT2006 Result

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Supermicro Motherboard X7DWA-N

SPECint®_rate2006 = 147

SPECint_rate_base2006 = 121

CPU2006 license: 001176

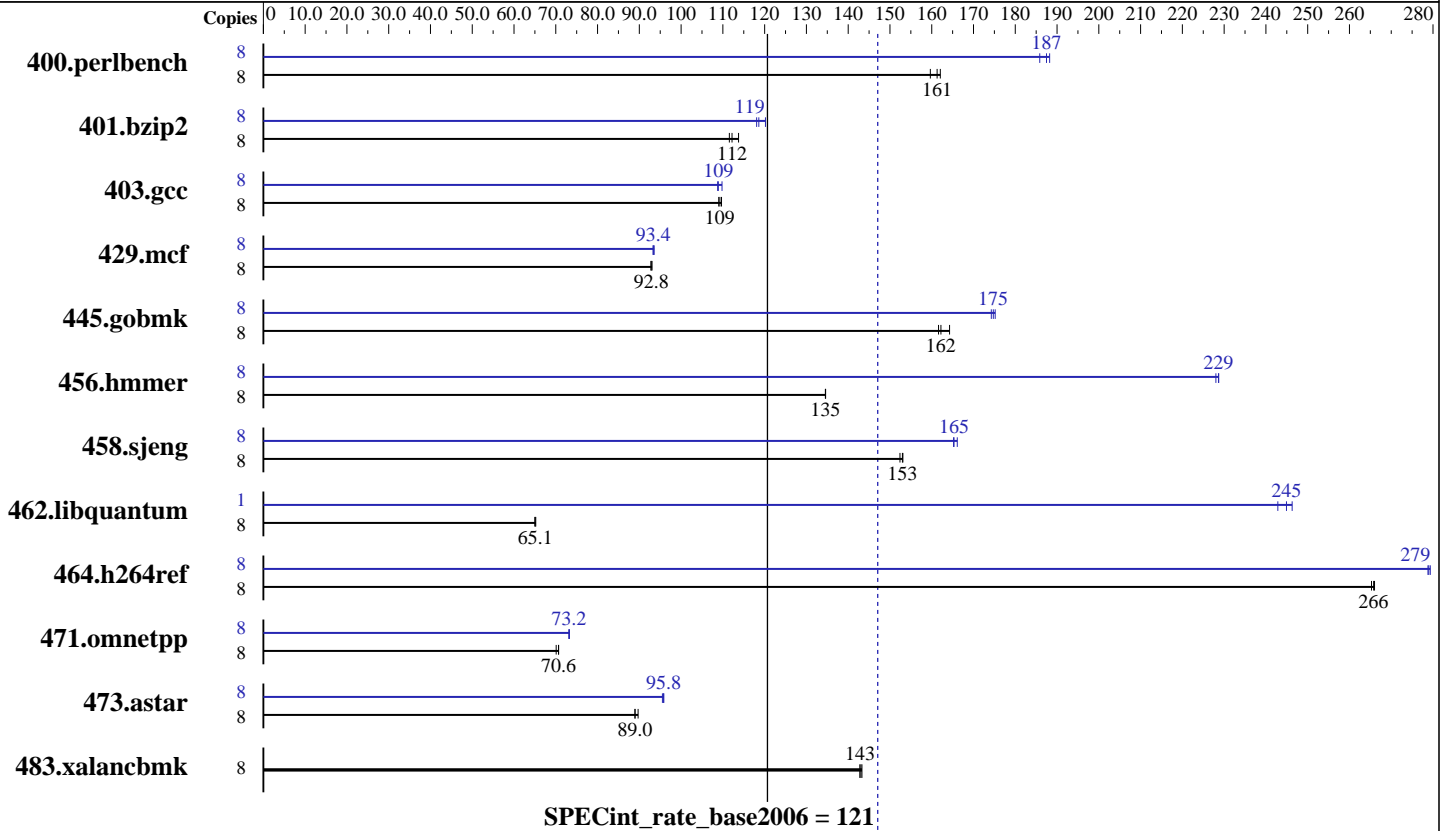
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Oct-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5482
 CPU Characteristics: Quad Core, 3.20GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2 GB PC2-6400 FBDIMM, CL-5-5-5, ECC)
 Disk Subsystem: WD WD5000YS Caviar RE2, 500GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: 64-Bit Suse Linux Enterprise Server 10 w/ SP1
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	482	162	490	160	485	161	8	421	186	415	188	417	187
401.bzip2	8	688	112	692	112	679	114	8	654	118	651	119	642	120
403.gcc	8	587	110	591	109	589	109	8	592	109	591	109	587	110
429.mcf	8	786	92.8	784	93.0	786	92.8	8	781	93.4	782	93.3	780	93.6
445.gobmk	8	511	164	519	162	517	162	8	480	175	482	174	479	175
456.hammer	8	555	135	555	135	555	135	8	326	229	326	229	327	228
458.sjeng	8	632	153	633	153	635	152	8	585	165	583	166	586	165
462.libquantum	8	2550	65.0	2548	65.1	2542	65.2	1	84.6	245	84.1	246	85.3	243
464.h264ref	8	667	265	666	266	666	266	8	635	279	635	279	634	279
471.omnetpp	8	708	70.6	707	70.7	713	70.1	8	684	73.2	683	73.3	683	73.2
473.astar	8	626	89.7	631	89.0	631	89.0	8	588	95.5	586	95.8	586	95.9
483.xalancbmk	8	387	143	386	143	385	143	8	387	143	386	143	385	143

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with CSE-825TQ-R700LPV case,
To ensure system stability, a 500W (minimum)
ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
Product description located as of
<http://www.supermicro.com/products/motherboard/Xeon1333/5400/X7WA-N.cfm>
The system bus runs at 1600 MHz
BIOS Setting: Default

Base Compiler Invocation

C benchmarks:
icc
C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3
C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.xml>

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For other inquiries, please contact webmaster@spec.org.

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