



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY RX300 S4, Intel Xeon processor E5405, 2.0 GHz

### SPECint®\_rate2006 = Not Run

### SPECint\_rate\_base2006 = 85.0

CPU2006 license: 22

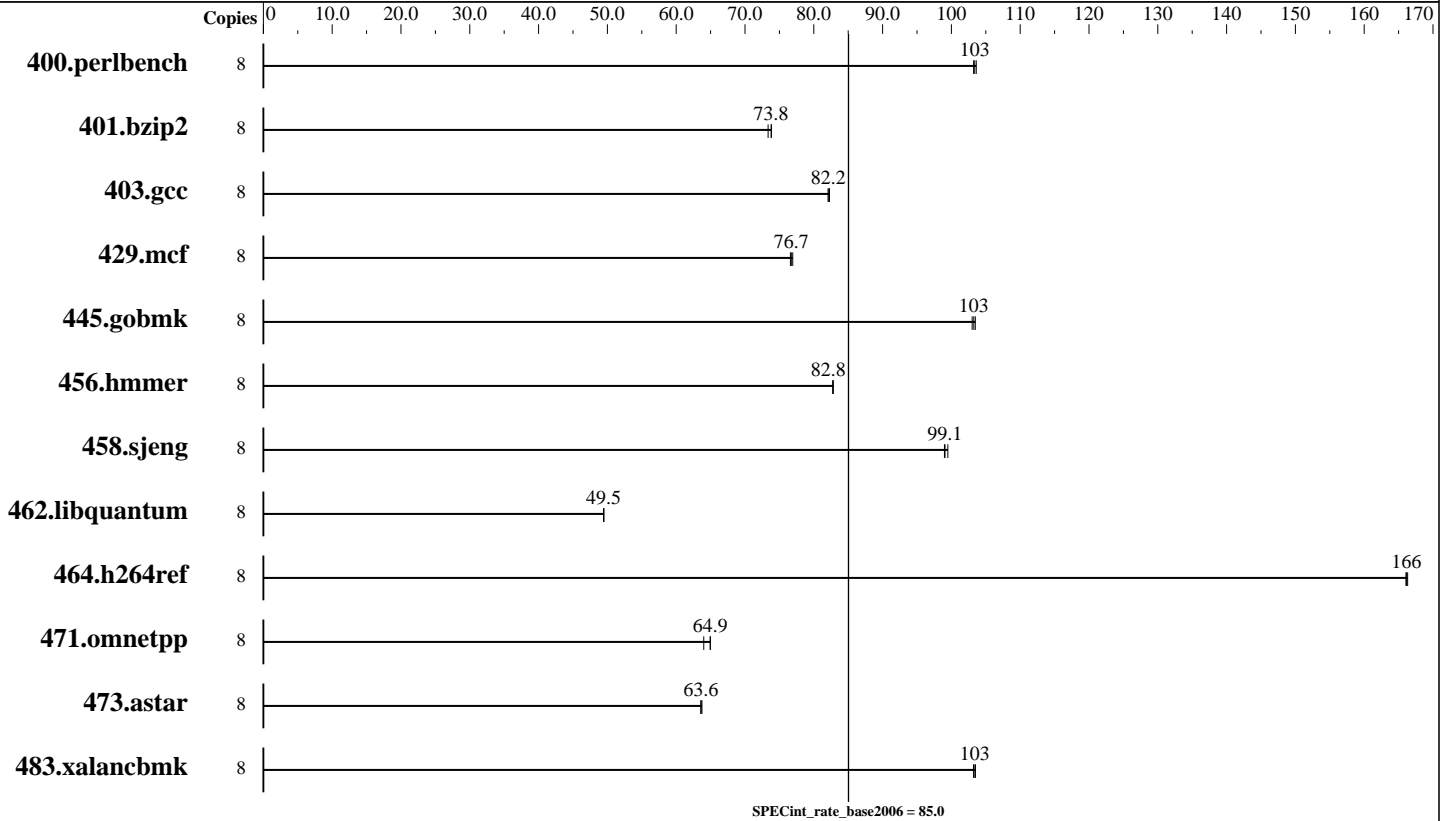
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Oct-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5405  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Smart Heap Library, Version 8.1  
 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<b><u>756</u></b>	<b><u>103</u></b>	757	103	754	104							
401.bzip2	8	<b><u>1046</u></b>	<b><u>73.8</u></b>	1052	73.4	1046	73.8							
403.gcc	8	<b><u>784</u></b>	<b><u>82.2</u></b>	785	82.1	783	82.3							
429.mcf	8	948	76.9	952	76.6	<b><u>951</u></b>	<b><u>76.7</u></b>							
445.gobmk	8	815	103	<b><u>813</u></b>	<b><u>103</u></b>	811	103							
456.hammer	8	901	82.8	<b><u>902</u></b>	<b><u>82.8</u></b>	902	82.8							
458.sjeng	8	<b><u>977</u></b>	<b><u>99.1</u></b>	973	99.5	978	99.0							
462.libquantum	8	<b><u>3351</u></b>	<b><u>49.5</u></b>	3351	49.5	3350	49.5							
464.h264ref	8	1064	166	<b><u>1065</u></b>	<b><u>166</u></b>	1066	166							
471.omnetpp	8	770	65.0	<b><u>770</u></b>	<b><u>64.9</u></b>	781	64.0							
473.astar	8	883	63.6	881	63.7	<b><u>883</u></b>	<b><u>63.6</u></b>							
483.xalancbmk	8	<b><u>534</u></b>	<b><u>103</u></b>	535	103	533	103							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:

<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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