



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DWN+ (Intel Xeon processor E5472,
3.00 GHz)

SPECint®_rate2006 = 143

SPECint_rate_base2006 = 118

CPU2006 license: 13

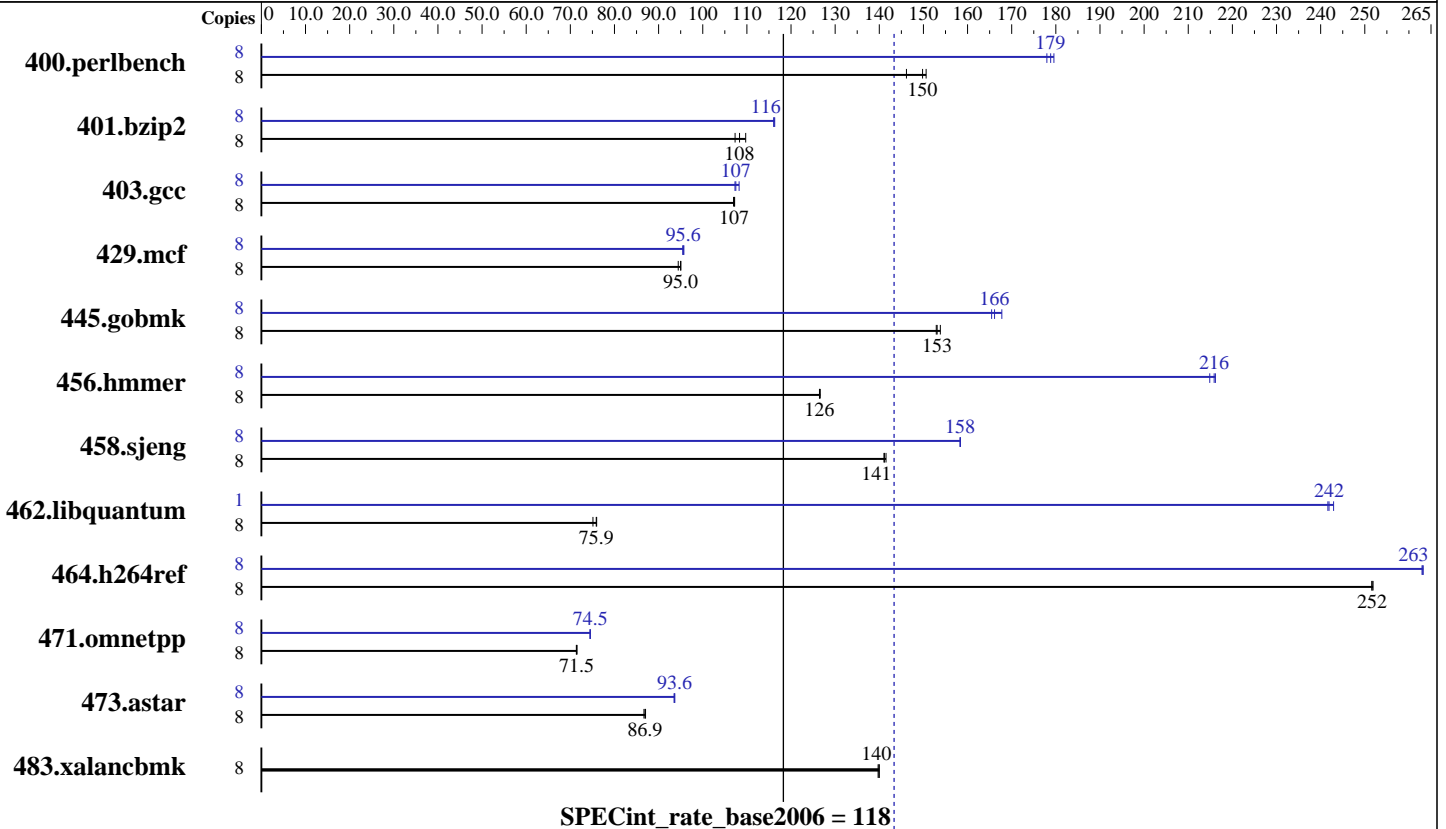
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5472
 CPU Characteristics: Quad Core, 3.0 GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 32 GB (8 * 4GB Hynix DDR2 6400F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: Western Digital Raptor (WD740GD-00FLA1) Serial ATA 74.8GB
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10 SP1 RC1, Kernel
 linux-cbgn 2.6.16.43-0.5-smp for x86_64
 Compiler: Intel C++ Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1
 Binutils 2.17.50.0.15



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DWN+ (Intel Xeon processor E5472,
3.00 GHz)

SPECint_rate2006 = 143

SPECint_rate_base2006 = 118

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	535	146	519	151	<u>522</u>	<u>150</u>	8	439	178	<u>437</u>	<u>179</u>	435	180
401.bzip2	8	719	107	<u>713</u>	<u>108</u>	704	110	8	664	116	<u>665</u>	<u>116</u>	665	116
403.gcc	8	<u>601</u>	<u>107</u>	601	107	602	107	8	595	108	600	107	<u>599</u>	<u>107</u>
429.mcf	8	768	95.1	773	94.4	<u>768</u>	<u>95.0</u>	8	762	95.7	<u>763</u>	<u>95.6</u>	764	95.5
445.gobmk	8	549	153	<u>548</u>	<u>153</u>	546	154	8	507	165	500	168	<u>505</u>	<u>166</u>
456.hammer	8	<u>590</u>	<u>126</u>	589	127	590	126	8	347	215	<u>346</u>	<u>216</u>	345	216
458.sjeng	8	686	141	684	142	<u>686</u>	<u>141</u>	8	612	158	611	158	<u>611</u>	<u>158</u>
462.libquantum	8	2206	75.1	2182	76.0	<u>2185</u>	<u>75.9</u>	1	<u>85.7</u>	<u>242</u>	85.3	243	85.8	242
464.h264ref	8	704	252	703	252	<u>703</u>	<u>252</u>	8	673	263	673	263	<u>673</u>	<u>263</u>
471.omnetpp	8	<u>700</u>	<u>71.5</u>	701	71.4	699	71.5	8	671	74.5	<u>671</u>	<u>74.5</u>	671	74.5
473.astar	8	645	87.0	648	86.7	<u>646</u>	<u>86.9</u>	8	600	93.7	<u>600</u>	<u>93.6</u>	601	93.5
483.xalancbmk	8	395	140	394	140	<u>395</u>	<u>140</u>	8	395	140	394	140	<u>395</u>	<u>140</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:

Hardware Prefetcher: Enabled

Adjacent Sector Prefetch: Disabled

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hammer, for peak, are compiled in 64-bit mode

The taskset utility was used to bind processes to cores

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DWN+ (Intel Xeon processor E5472,
3.00
GHz)

SPECint_rate2006 = 143

SPECint_rate_base2006 = 118

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007

Base Optimization Flags

C benchmarks:

-fast -inline-alloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DWN+ (Intel Xeon processor E5472,
3.00
GHz)

SPECint_rate2006 = 143

SPECint_rate_base2006 = 118

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.33.html>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DWN+ (Intel Xeon processor E5472,
3.00
GHz)

SPECint_rate2006 = 143

SPECint_rate_base2006 = 118

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.33.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:20:03 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 November 2007.