



# SPEC® CINT2006 Result

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## IBM Corporation

## SPECint®\_rate2006 = Not Run

## IBM System x3650 (Intel Xeon X5460)

## SPECint\_rate\_base2006 = 113

CPU2006 license: 11

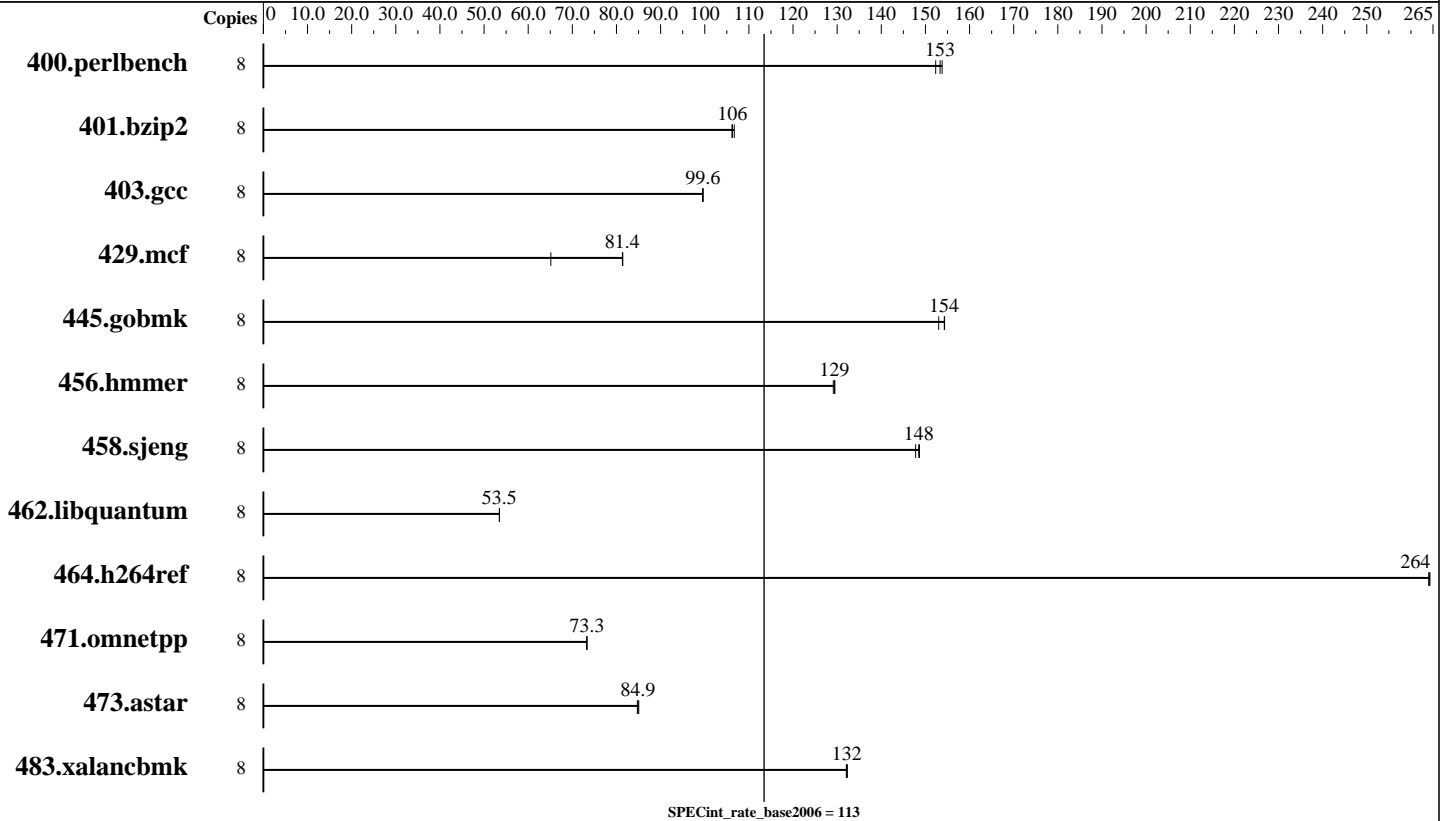
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Nov-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5460  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3158  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 73 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SLES10 (x86\_64), 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux  
 Build 20070913 Package ID: 1\_cc\_p\_10.1.008  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap 8.1  
 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	508	154	<u>510</u>	<u>153</u>	513	152							
401.bzip2	8	<u>727</u>	<u>106</u>	727	106	724	107							
403.gcc	8	646	99.6	647	99.5	<u>647</u>	<u>99.6</u>							
429.mcf	8	896	81.4	<u>897</u>	<u>81.4</u>	1120	65.1							
445.gobmk	8	544	154	548	153	<u>544</u>	<u>154</u>							
456.hammer	8	578	129	<u>577</u>	<u>129</u>	576	129							
458.sjeng	8	655	148	651	149	<u>652</u>	<u>148</u>							
462.libquantum	8	3100	53.5	<u>3099</u>	<u>53.5</u>	3098	53.5							
464.h264ref	8	<u>670</u>	<u>264</u>	671	264	670	264							
471.omnetpp	8	682	73.3	682	73.3	<u>682</u>	<u>73.3</u>							
473.astar	8	<u>662</u>	<u>84.9</u>	660	85.0	663	84.7							
483.xalancbmk	8	417	132	<u>418</u>	<u>132</u>	418	132							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hammer, for peak, are compiled in 64-bit mode  
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to physical,0  
KMP\_STACKSIZE set to 64M  
Powersaved dameon was disabled in OS  
taskset utility used to bind CPU(s) to processes

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.31.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.31.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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