



# SPEC® CINT2006 Result

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## NEC Corporation

Express5800/120Rg-1  
(Intel Xeon processor 5130)

SPECint®2006 = 15.5

SPECint\_base2006 = 14.1

CPU2006 license: 9006

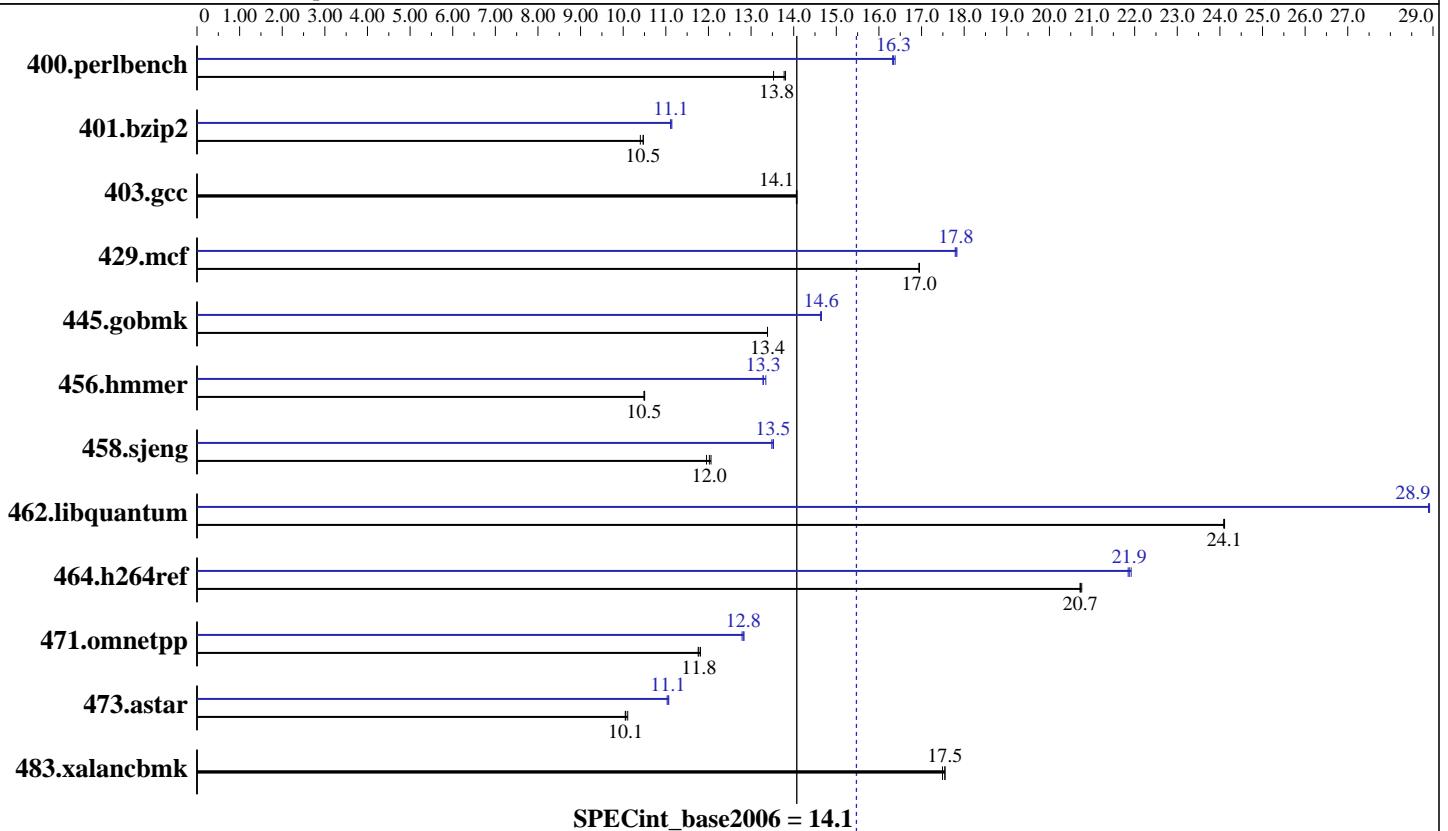
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Oct-2007

Hardware Availability: May-2007

Software Availability: Jun-2007



### Hardware

CPU Name: Intel Xeon 5130  
 CPU Characteristics: 2.00 GHz, 4 MB L2, 1333 MHz bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: 1x73.2 GB SAS, 15000RPM  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070426 Package ID: l\_cc\_p\_10.0.023  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	722	13.5	<b><u>709</u></b>	<b><u>13.8</u></b>	708	13.8	<b><u>598</u></b>	<b><u>16.3</u></b>	596	16.4	599	16.3
401.bzip2	921	10.5	928	10.4	<b><u>922</u></b>	<b><u>10.5</u></b>	<b><u>868</u></b>	<b><u>11.1</u></b>	866	11.1	868	11.1
403.gcc	572	14.1	572	14.1	<b><u>572</u></b>	<b><u>14.1</u></b>	572	14.1	572	14.1	<b><u>572</u></b>	<b><u>14.1</u></b>
429.mcf	538	16.9	<b><u>538</u></b>	<b><u>17.0</u></b>	538	17.0	513	17.8	511	17.8	<b><u>512</u></b>	<b><u>17.8</u></b>
445.gobmk	<b><u>784</u></b>	<b><u>13.4</u></b>	784	13.4	784	13.4	717	14.6	<b><u>716</u></b>	<b><u>14.6</u></b>	716	14.7
456.hmmer	<b><u>889</u></b>	<b><u>10.5</u></b>	888	10.5	889	10.5	<b><u>702</u></b>	<b><u>13.3</u></b>	702	13.3	699	13.3
458.sjeng	<b><u>1006</u></b>	<b><u>12.0</u></b>	1003	12.1	1012	12.0	895	13.5	<b><u>895</u></b>	<b><u>13.5</u></b>	897	13.5
462.libquantum	859	24.1	<b><u>860</u></b>	<b><u>24.1</u></b>	860	24.1	<b><u>717</u></b>	<b><u>28.9</u></b>	717	28.9	716	28.9
464.h264ref	1066	20.8	1068	20.7	<b><u>1068</u></b>	<b><u>20.7</u></b>	1010	21.9	<b><u>1012</u></b>	<b><u>21.9</u></b>	1013	21.8
471.omnetpp	531	11.8	529	11.8	<b><u>531</u></b>	<b><u>11.8</u></b>	<b><u>488</u></b>	<b><u>12.8</u></b>	487	12.8	489	12.8
473.astar	695	10.1	699	10.0	<b><u>698</u></b>	<b><u>10.1</u></b>	<b><u>635</u></b>	<b><u>11.1</u></b>	636	11.0	634	11.1
483.xalancbmk	393	17.5	<b><u>393</u></b>	<b><u>17.5</u></b>	394	17.5	393	17.5	<b><u>393</u></b>	<b><u>17.5</u></b>	394	17.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

The Express5800/120Rg-1(Intel Xeon Processor 5130) and the Express5800/120Ri-2(Intel Xeon Processor 5130) models are electronically equivalent. The results have been measured on a Express5800/120Ri-2(Intel Xeon Processor 5130) model.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0  
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-ic10-INT-ia32-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-ic10-INT-ia32-intel64-linux-flags.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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