SPEC® CINT2006 Result
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Sun Microsystems

Sun Blade X6250 (3.0GHz, Xeon X5365)

SPECint®_rate2006 = 108

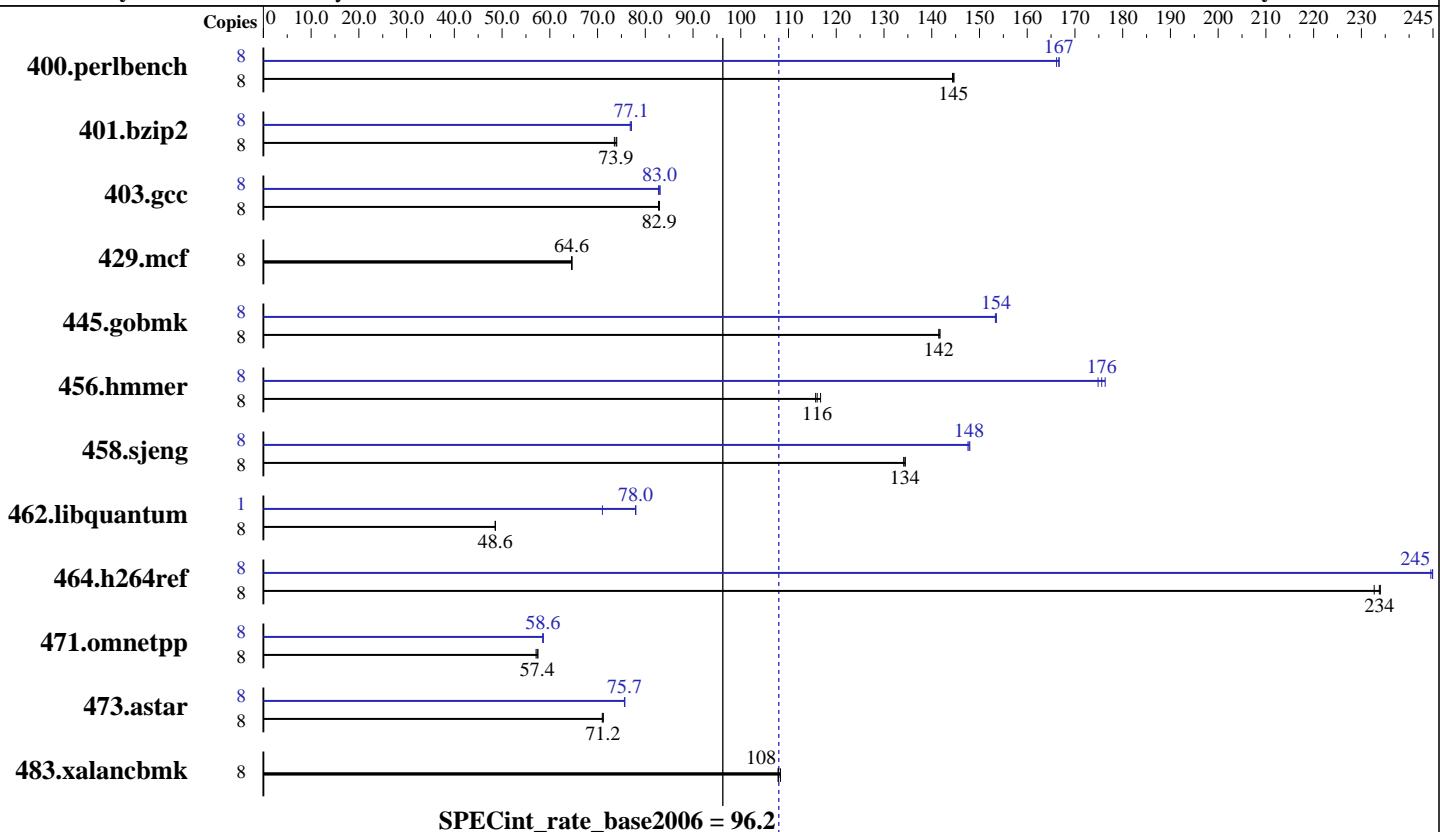SPECint_rate_base2006 = 96.2

CPU2006 license: 6
Test sponsor: Sun Microsystems
Tested by: Sun Microsystems

Test date: Oct-2007
Hardware Availability: Oct-2007
Software Availability: Nov-2007

Benchmark	Copies	Value
400.perlbench	8	167
	8	145
401.bzip2	8	77.1
	8	73.9
403.gcc	8	83.0
	8	82.9
429.mcf	8	64.6
445.gobmk	8	154
	8	142
456.hmmer	8	176
	8	116
458.sjeng	8	148
	8	134
462.libquantum	1	78.0
	8	48.6
464.h264ref	8	245
	8	234
471.omnetpp	8	58.6
	8	57.4
473.astar	8	75.7
	8	71.2
483.xalancbmk	8	108

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Hardware

CPU Name:	Intel Xeon X5365
CPU Characteristics:	Quad Core, 3.0 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 (order by number of chips)
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	32 GB (16x2GB DDR2 PC2-5300F 2rank CAS 5-5-5 with ECC)
Disk Subsystem:	SAS, 72 GB, 10K RPM
Other Hardware:	None

Software

Operating System:	SUSE LINUX Enterprise Server 10 SP1 for x86_64
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**541**	**145**	541	144	540	145	8	469	167	**469**	**167**	470	166
401.bzip2	8	1050	73.5	1043	74.0	**1045**	**73.9**	8	1001	77.1	**1002**	**77.1**	1005	76.8
403.gcc	8	**777**	**82.9**	778	82.8	776	83.0	8	778	82.8	775	83.1	**776**	**83.0**
429.mcf	8	1129	64.7	**1130**	**64.6**	1130	64.6	8	1129	64.7	**1130**	**64.6**	1130	64.6
445.gobmk	8	**593**	**142**	593	142	592	142	8	547	154	547	153	**547**	**154**
456.hmmer	8	639	117	**643**	**116**	645	116	8	427	175	**425**	**176**	423	176
458.sjeng	8	720	134	**720**	**134**	722	134	8	654	148	**655**	**148**	656	148
462.libquantum	8	3412	48.6	**3412**	**48.6**	3411	48.6	1	266	78.0	292	71.0	**266**	**78.0**
464.h264ref	8	757	234	**757**	**234**	761	233	8	723	245	724	245	**723**	**245**
471.omnetpp	8	869	57.5	875	57.1	**872**	**57.4**	8	**853**	**58.6**	854	58.5	853	58.6
473.astar	8	791	71.0	**789**	**71.2**	789	71.2	8	743	75.6	**742**	**75.7**	742	75.7
483.xalancbmk	8	510	108	**511**	**108**	512	108	8	510	108	**511**	**108**	512	108

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited
OMP_NUM_THREADS set to 8
KMP_STACKSIZE set to 64M
KMP_AFFINITY set to physical,0
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch = Disable; Adjacent Sector Prefetch = Disable
```

General Notes

```
All benchmarks were compiled in 32-bit mode except 401.bzip2
and 456.hmmer for peak were compiled in 64-bit mode
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
    -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc
```

```
    401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
              -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
              -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
    456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
              -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
              -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
    icpc
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
```

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Test sponsor: Sun Microsystems		**Hardware Availability:**	Oct-2007
Tested by: Sun Microsystems		**Software Availability:**	Nov-2007

Peak Portability Flags (Continued)

   483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

   400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias`
`-prefetch`

   401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

   403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

   429.mcf: basepeak = yes

   445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo`
`-no-prec-div -ansi-alias`

   456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

   458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

   462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
`-opt-streaming-stores always -vec-guard-write`
`-opt-malloc-options=3 -parallel -par-runtime-control`

   464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2`
`-ansi-alias`

C++ benchmarks:

   471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
`-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

   473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
`-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

   483.xalancbmk: basepeak = yes

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.html`

You can also download the XML flags source by saving the following link:

`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.02.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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