



# SPEC<sup>®</sup> CINT2006 Result

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Dell Inc.

SPECint<sup>®</sup>\_rate2006 = 37.3

PowerEdge 2900 (Intel Xeon E5310, 1.60 GHz)

SPECint\_rate\_base2006 = 34.6

CPU2006 license: 55

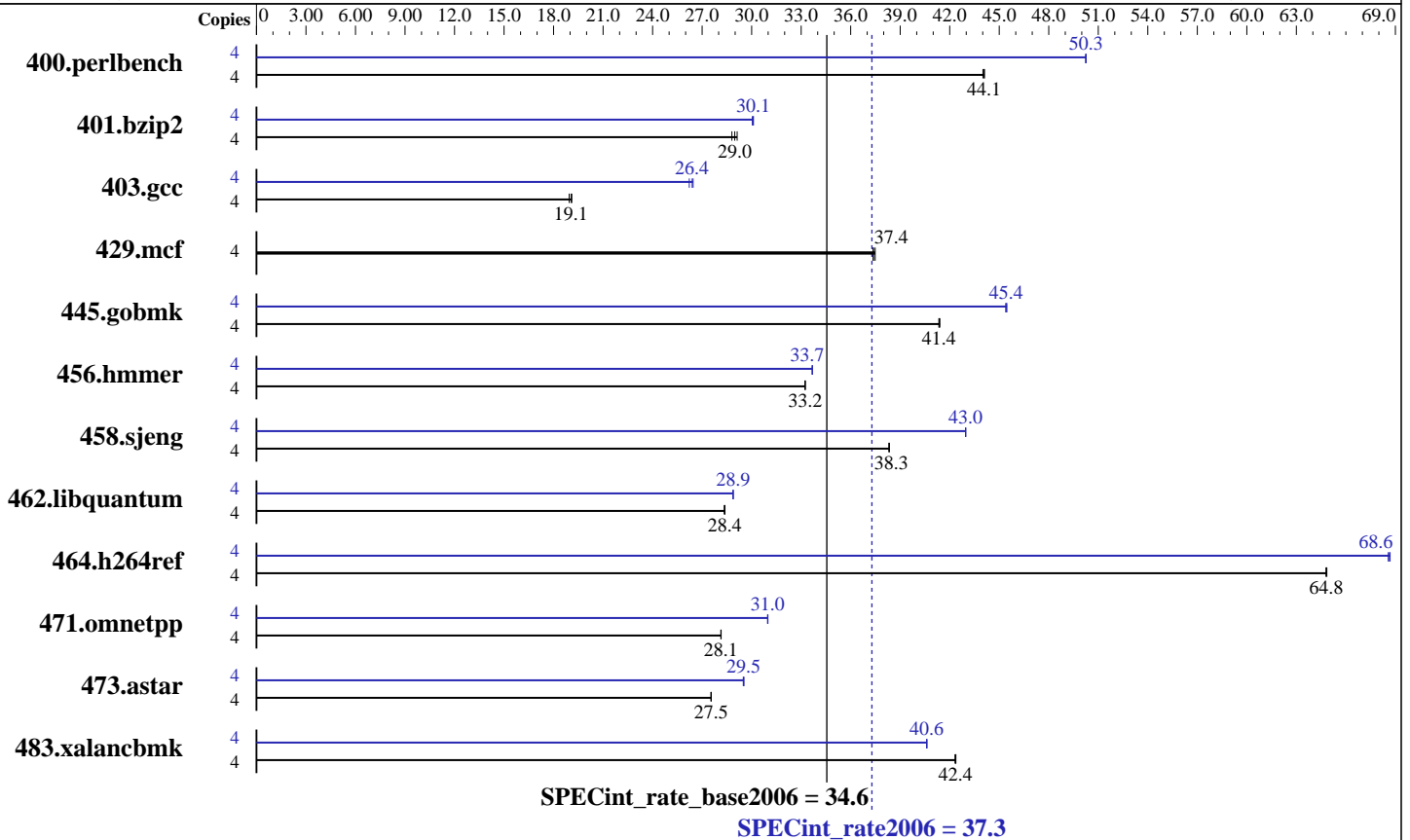
Test date: Aug-2007

Test sponsor: Dell Inc.

Hardware Availability: Dec-2006

Tested by: Dell Inc.

Software Availability: Jun-2007



## Hardware

CPU Name: Intel Xeon E5310  
 CPU Characteristics: 1066 MHz Bus Speed  
 CPU MHz: 1600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8 x 1 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)  
 Disk Subsystem: 4 x 146 GB SAS, 15000RPM  
 Other Hardware: 1 X Gigabit Ethernet card Dual Port Integrated

## Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition  
 Compiler: Intel C++ Compiler for IA32 version 10.0  
 Build 20070426 Package ID: W\_CC\_P\_10.0.025  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: MicroQuill SmartHeap Library Version 8.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	886	44.1	888	44.0	<b>886</b>	<b>44.1</b>	4	778	50.2	778	50.3	<b>778</b>	<b>50.3</b>
401.bzip2	4	<b>1332</b>	<b>29.0</b>	1325	29.1	1340	28.8	4	1285	30.0	1282	30.1	<b>1282</b>	<b>30.1</b>
403.gcc	4	1700	18.9	1686	19.1	<b>1689</b>	<b>19.1</b>	4	1228	26.2	1217	26.5	<b>1220</b>	<b>26.4</b>
429.mcf	4	973	37.5	<b>976</b>	<b>37.4</b>	976	37.4	4	973	37.5	<b>976</b>	<b>37.4</b>	976	37.4
445.gobmk	4	1013	41.4	1015	41.3	<b>1015</b>	<b>41.4</b>	4	923	45.5	<b>924</b>	<b>45.4</b>	924	45.4
456.hammer	4	<b>1123</b>	<b>33.2</b>	1123	33.2	1123	33.2	4	1108	33.7	1108	33.7	<b>1108</b>	<b>33.7</b>
458.sjeng	4	1262	38.3	<b>1263</b>	<b>38.3</b>	1263	38.3	4	1126	43.0	<b>1127</b>	<b>43.0</b>	1127	43.0
462.libquantum	4	<b>2920</b>	<b>28.4</b>	2920	28.4	2925	28.3	4	<b>2870</b>	<b>28.9</b>	2869	28.9	2870	28.9
464.h264ref	4	1365	64.8	1366	64.8	<b>1366</b>	<b>64.8</b>	4	1291	68.6	<b>1290</b>	<b>68.6</b>	1289	68.7
471.omnetpp	4	<b>888</b>	<b>28.1</b>	888	28.2	889	28.1	4	807	31.0	807	31.0	<b>807</b>	<b>31.0</b>
473.astar	4	<b>1020</b>	<b>27.5</b>	1020	27.5	1019	27.6	4	<b>951</b>	<b>29.5</b>	952	29.5	951	29.5
483.xalancbmk	4	652	42.3	651	42.4	<b>652</b>	<b>42.4</b>	4	679	40.6	680	40.6	<b>680</b>	<b>40.6</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

"start /b /wait /affinity" used to bind processes to cores

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32



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## Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qprefetch /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

401.bzip2: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE

403.gcc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
-link /FORCE:MULTIPLE

429.mcf: basepeak = yes

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## Peak Optimization Flags (Continued)

445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxT -O2 -Qipo  
-Qprec\_div- -Qansi-alias /F512000000  
-link /FORCE:MULTIPLE

456.hmmer: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
-Qansi-alias /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

458.sjeng: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll4  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll4  
-Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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