SPEC® CFP2006 Result

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Dell Inc.

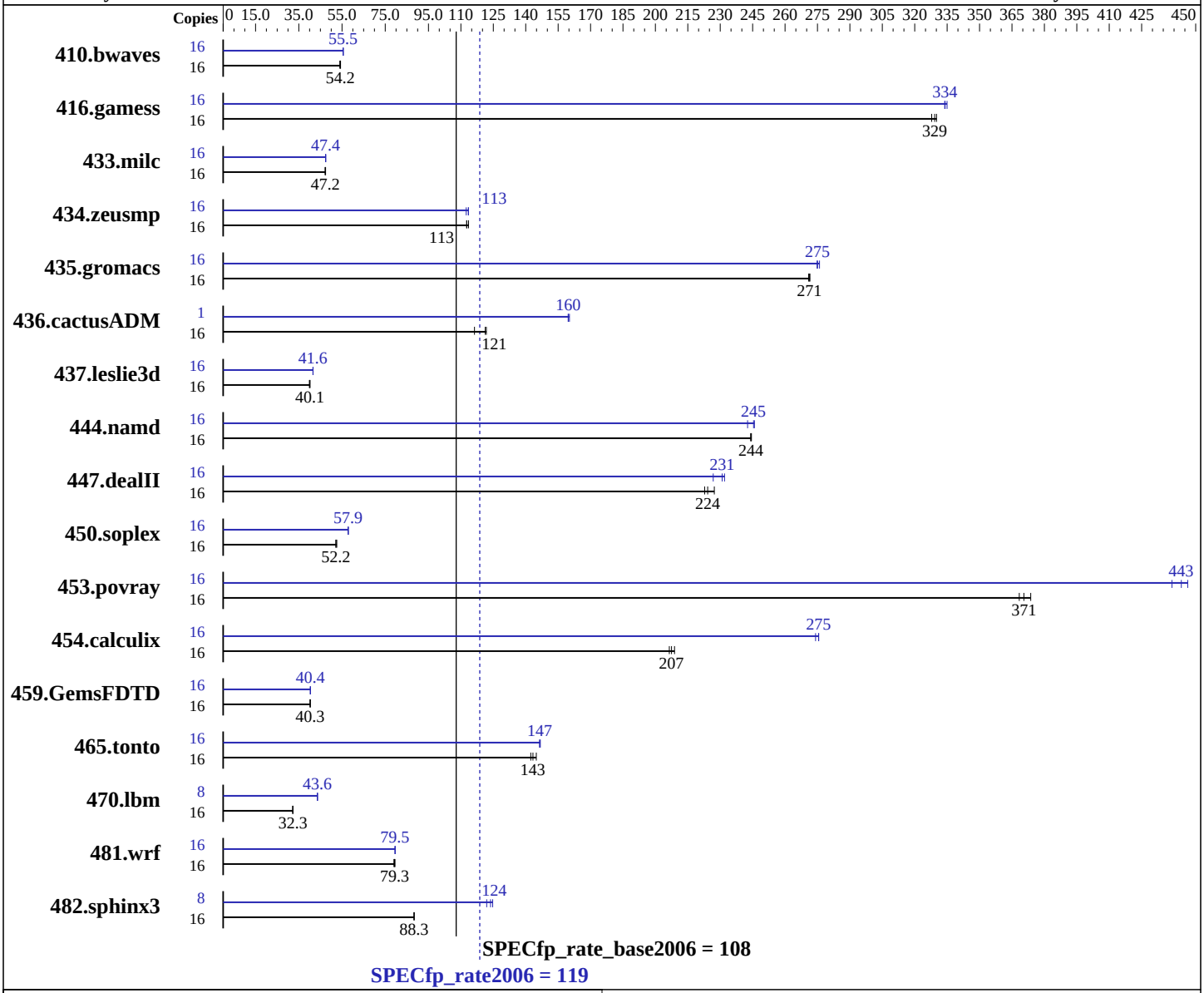
PowerEdge R900 (Intel Xeon X7350, 2.93 GHz)

SPECfp®_rate2006 = 119

SPECfp_rate_base2006 = 108

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Oct-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X7350
CPU Characteristics:	1066 MHz system bus
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 SP1
Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (16 x 1GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem: 1 x 73 GB SAS 15K RPM
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	4029	54.0	**4009**	**54.2**	4003	54.3	16	3917	55.5	3913	55.6	**3914**	**55.5**
416.gamess	16	949	330	956	328	**952**	**329**	16	935	335	**938**	**334**	938	334
433.milc	16	3112	47.2	**3109**	**47.2**	3099	47.4	16	3091	47.5	**3100**	**47.4**	3100	47.4
434.zeusmp	16	1292	113	**1291**	**113**	1282	114	16	1283	113	**1283**	**113**	1295	112
435.gromacs	16	421	272	**421**	**271**	422	271	16	414	276	416	275	**415**	**275**
436.cactusADM	16	1644	116	1570	122	**1577**	**121**	1	**74.8**	**160**	74.6	160	74.9	160
437.leslie3d	16	3770	39.9	3745	40.2	**3747**	**40.1**	16	3614	41.6	**3617**	**41.6**	3621	41.5
444.namd	16	525	244	526	244	**525**	**244**	16	**523**	**245**	529	243	522	246
447.dealII	16	805	227	**816**	**224**	822	223	16	807	227	**793**	**231**	789	232
450.soplex	16	2538	52.6	**2556**	**52.2**	2558	52.2	16	2308	57.8	**2306**	**57.9**	2304	57.9
453.povray	16	231	368	**230**	**371**	228	374	16	**192**	**443**	191	446	194	439
454.calculix	16	632	209	639	206	**636**	**207**	16	479	276	482	274	**479**	**275**
459.GemsFDTD	16	4218	40.2	4210	40.3	**4217**	**40.3**	16	4203	40.4	**4205**	**40.4**	4208	40.3
465.tonto	16	1087	145	1106	142	**1098**	**143**	16	1073	147	1075	146	**1075**	**147**
470.lbm	16	6831	32.2	**6814**	**32.3**	6813	32.3	8	2518	43.7	2521	43.6	**2520**	**43.6**
481.wrf	16	2262	79.0	**2253**	**79.3**	2248	79.5	16	**2247**	**79.5**	2247	79.5	2245	79.6
482.sphinx3	16	3530	88.3	**3530**	**88.3**	3529	88.4	8	1279	122	**1260**	**124**	1250	125

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

Platform Notes

```
BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (default Enabled)
Hardware Prefetcher = Disabled (default Enabled)
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):
```
/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include
```

433.milc: `icc`

C++ benchmarks (except as noted below):
```
icpc
```

450.soplex: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icpc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include`

Fortran benchmarks (except as noted below):
```
ifort
```

437.leslie3d: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/ifort -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include`

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.22.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.22.xml

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For other inquiries, please contact webmaster@spec.org.

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