



# SPEC<sup>®</sup> CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

SPECint<sup>®</sup>\_rate2006 = 25.1

ProLiant DL380 G5  
(1.86 GHz, Intel Xeon processor 5120)

SPECint\_rate\_base2006 = 22.1

CPU2006 license: 3

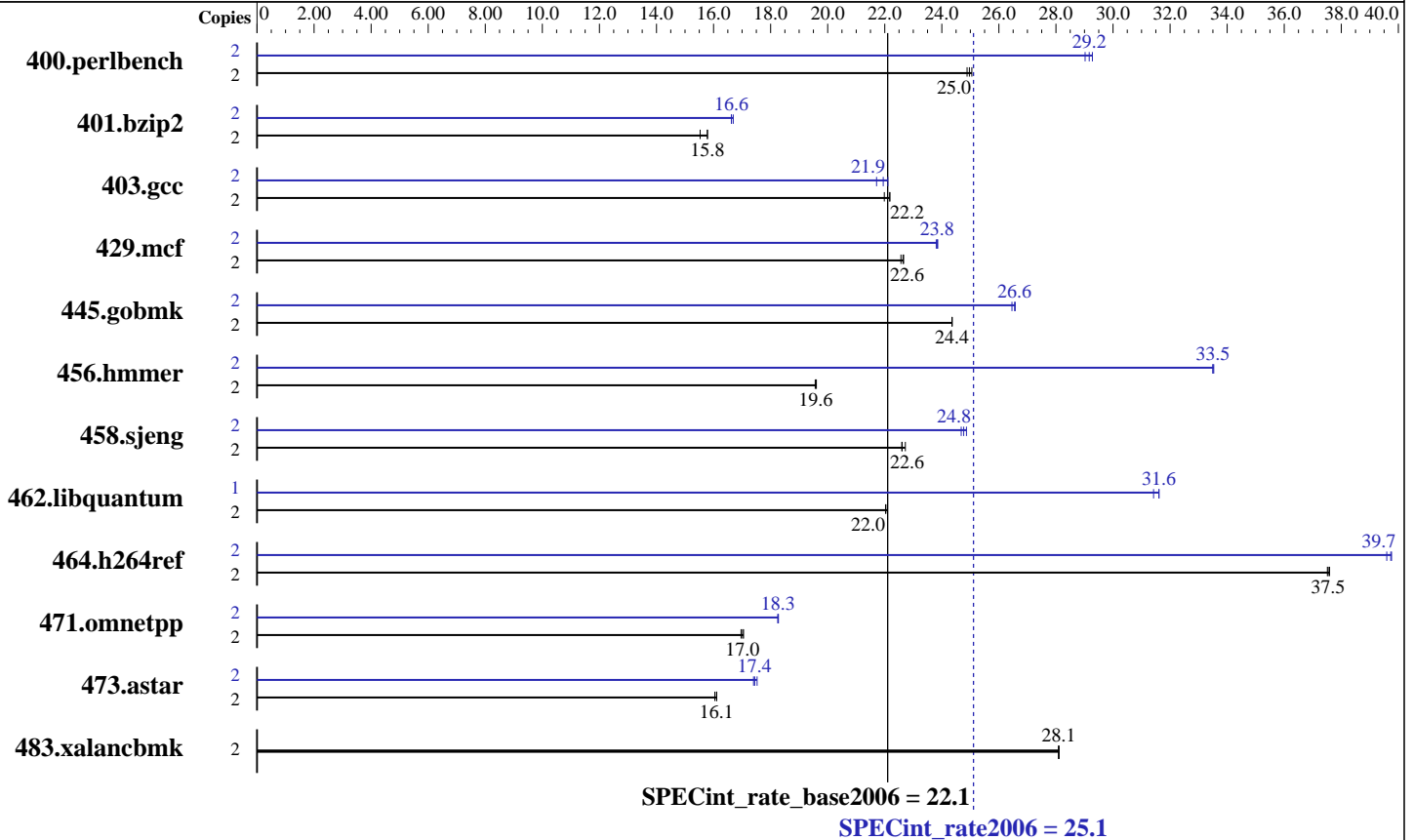
Test date: Oct-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon 5120  
 CPU Characteristics: 1.86 GHz, 4 MB L2 shared, 1066 MHz system bus  
 CPU MHz: 1866  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 or 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB PC2-5300F CL5)  
 Disk Subsystem: 1x72 GB 10 K SAS  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64) SP1  
 kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64  
 version 10.1, Build 20070725  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Multi-user run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library 8.1  
 binutils-2.17.50



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

SPECint\_rate2006 = 25.1

ProLiant DL380 G5  
(1.86 GHz, Intel Xeon processor 5120)

SPECint\_rate\_base2006 = 22.1

CPU2006 license: 3

Test date: Oct-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	785	24.9	<b>783</b>	<b>25.0</b>	780	25.1	2	667	29.3	<b>670</b>	<b>29.2</b>	673	29.0
401.bzip2	2	1243	15.5	1223	15.8	<b>1223</b>	<b>15.8</b>	2	1156	16.7	1161	16.6	<b>1161</b>	<b>16.6</b>
403.gcc	2	726	22.2	732	22.0	<b>726</b>	<b>22.2</b>	2	728	22.1	<b>734</b>	<b>21.9</b>	741	21.7
429.mcf	2	805	22.7	<b>806</b>	<b>22.6</b>	808	22.6	2	765	23.9	766	23.8	<b>765</b>	<b>23.8</b>
445.gobmk	2	<b>861</b>	<b>24.4</b>	861	24.4	861	24.4	2	790	26.6	793	26.5	<b>790</b>	<b>26.6</b>
456.hammer	2	<b>953</b>	<b>19.6</b>	954	19.6	952	19.6	2	557	33.5	<b>557</b>	<b>33.5</b>	557	33.5
458.sjeng	2	1071	22.6	1065	22.7	<b>1070</b>	<b>22.6</b>	2	981	24.7	<b>977</b>	<b>24.8</b>	973	24.9
462.libquantum	2	1876	22.1	1881	22.0	<b>1881</b>	<b>22.0</b>	1	659	31.4	655	31.6	<b>656</b>	<b>31.6</b>
464.h264ref	2	<b>1179</b>	<b>37.5</b>	1177	37.6	1180	37.5	2	1113	39.8	<b>1114</b>	<b>39.7</b>	1118	39.6
471.omnetpp	2	733	17.1	<b>735</b>	<b>17.0</b>	737	17.0	2	684	18.3	<b>685</b>	<b>18.3</b>	685	18.3
473.astar	2	875	16.0	871	16.1	<b>872</b>	<b>16.1</b>	2	807	17.4	<b>805</b>	<b>17.4</b>	801	17.5
483.xalancbmk	2	491	28.1	<b>491</b>	<b>28.1</b>	491	28.1	2	491	28.1	<b>491</b>	<b>28.1</b>	491	28.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to physical,0  
KMP\_STACKSIZE set to 64M

## Platform Notes

BIOS configuration:  
Power Regulator set to Static High Performance Mode  
Adjacent Sector Prefetch Disabled

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

**SPECint\_rate2006 = 25.1**

ProLiant DL380 G5  
(1.86 GHz, Intel Xeon processor 5120)

**SPECint\_rate\_base2006 = 22.1**

**CPU2006 license:** 3

**Test date:** Oct-2007

**Test sponsor:** Hewlett-Packard Company

**Hardware Availability:** Jun-2006

**Tested by:** Hewlett-Packard Company

**Software Availability:** Nov-2007

## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

**SPECint\_rate2006 = 25.1**

ProLiant DL380 G5  
(1.86 GHz, Intel Xeon processor 5120)

**SPECint\_rate\_base2006 = 22.1**

**CPU2006 license:** 3

**Test date:** Oct-2007

**Test sponsor:** Hewlett-Packard Company

**Hardware Availability:** Jun-2006

**Tested by:** Hewlett-Packard Company

**Software Availability:** Nov-2007

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
               -prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
           -no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
                -opt-streaming-stores always -vec-guard-write
                -opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
             -no-prec-div -ansi-alias -opt-ra-region-strategy=block
             -Wl,-z,muldefs
             -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
           -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
           -Wl,-z,muldefs
           -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

```

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

ProLiant DL380 G5  
(1.86 GHz, Intel Xeon processor 5120)

**SPECint\_rate2006 = 25.1**

**SPECint\_rate\_base2006 = 22.1**

**CPU2006 license:** 3

**Test sponsor:** Hewlett-Packard Company

**Tested by:** Hewlett-Packard Company

**Test date:** Oct-2007

**Hardware Availability:** Jun-2006

**Software Availability:** Nov-2007

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.20090714.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 14:11:57 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 30 October 2007.