



SPEC® CINT2006 Result

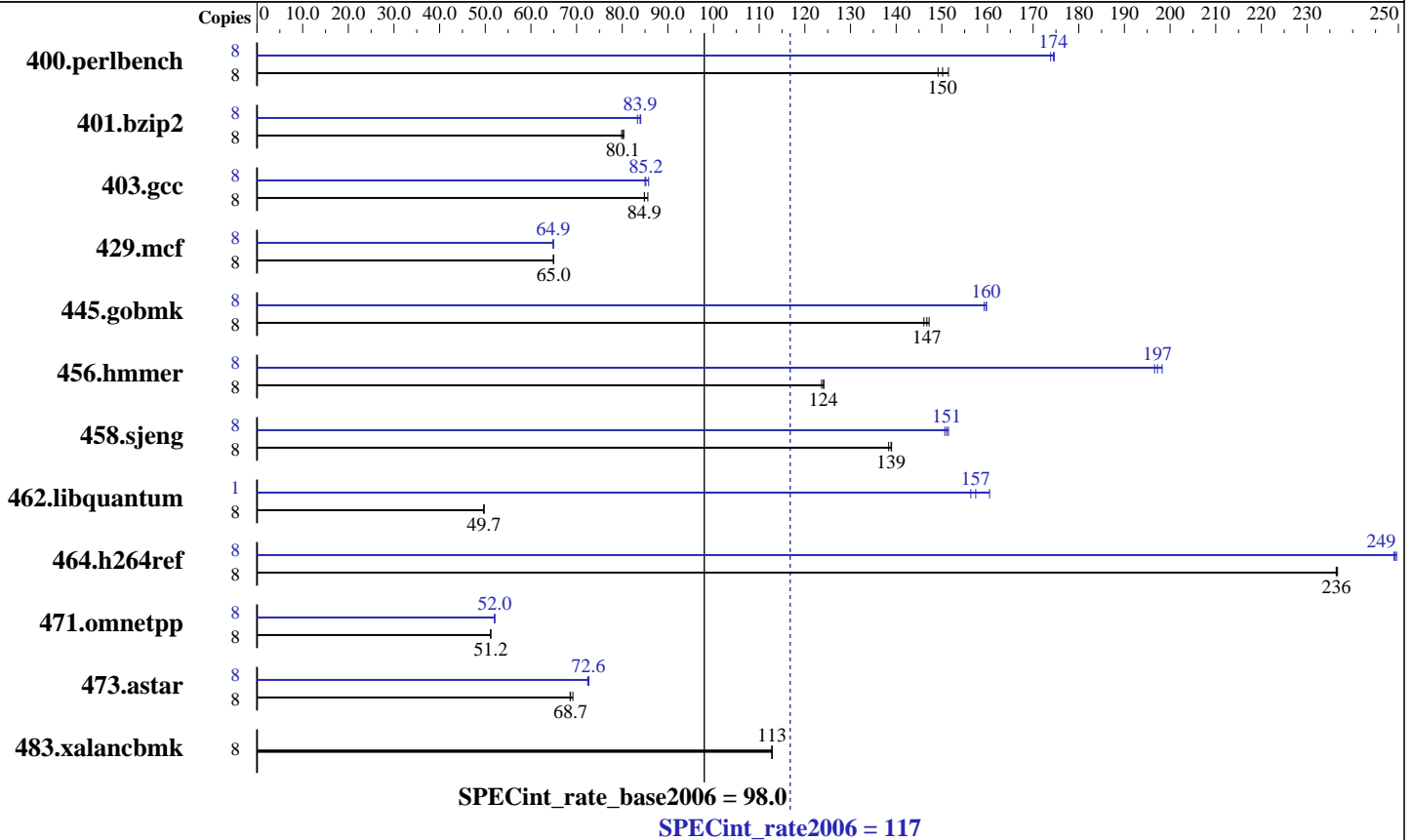
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Supermicro Motherboard X7DB3

SPECint®_rate2006 = 117
SPECint_rate_base2006 = 98.0

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Sep-2007
Hardware Availability: Sep-2007
Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5365
 CPU Characteristics: Quad Core, 3.00GHz
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 32 GB (8 * 4 GB DDR2 5300, CL-5-5-5, ECC)
 Disk Subsystem: 500 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: 64-Bit Suse Linux Enterprise Server 10 w/ SP1
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: None



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	520	150	516	151	524	149	8	450	174	448	174	448	175
401.bzip2	8	963	80.1	961	80.3	967	79.9	8	926	83.3	918	84.1	920	83.9
403.gcc	8	752	85.6	759	84.9	759	84.8	8	758	85.0	756	85.2	751	85.8
429.mcf	8	1123	65.0	1124	64.9	1123	65.0	8	1124	64.9	1124	64.9	1124	64.9
445.gobmk	8	574	146	570	147	572	147	8	527	159	525	160	525	160
456.hmmr	8	601	124	604	124	601	124	8	380	197	376	198	378	197
458.sjeng	8	697	139	700	138	696	139	8	641	151	639	151	643	151
462.libquantum	8	3334	49.7	3336	49.7	3335	49.7	1	133	156	129	160	132	157
464.h264ref	8	748	237	749	236	749	236	8	711	249	710	249	709	250
471.omnetpp	8	977	51.2	977	51.2	977	51.2	8	961	52.0	960	52.1	961	52.0
473.astar	8	819	68.6	818	68.7	812	69.2	8	773	72.7	775	72.4	774	72.6
483.xalancbmk	8	489	113	489	113	489	113	8	489	113	489	113	489	113

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with CSE-825TQ-R700LPV case,
 To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]
 Product description located as of <http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DB3.cfm>
 The system bus runs at 1333 MHz
 Bios settings:
 Hardware Prefetcher: Enabled
 Adjacent Sector Prefetch: Disabled
 All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmr,
 for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
 icc
 C++ benchmarks:
 icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmr: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.29.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.29.xml>

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