



# SPEC® CINT2006 Result

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## Supermicro Motherboard C2SBA+

SPECint®\_rate2006 = 37.2

SPECint\_rate\_base2006 = 33.4

CPU2006 license: 001176

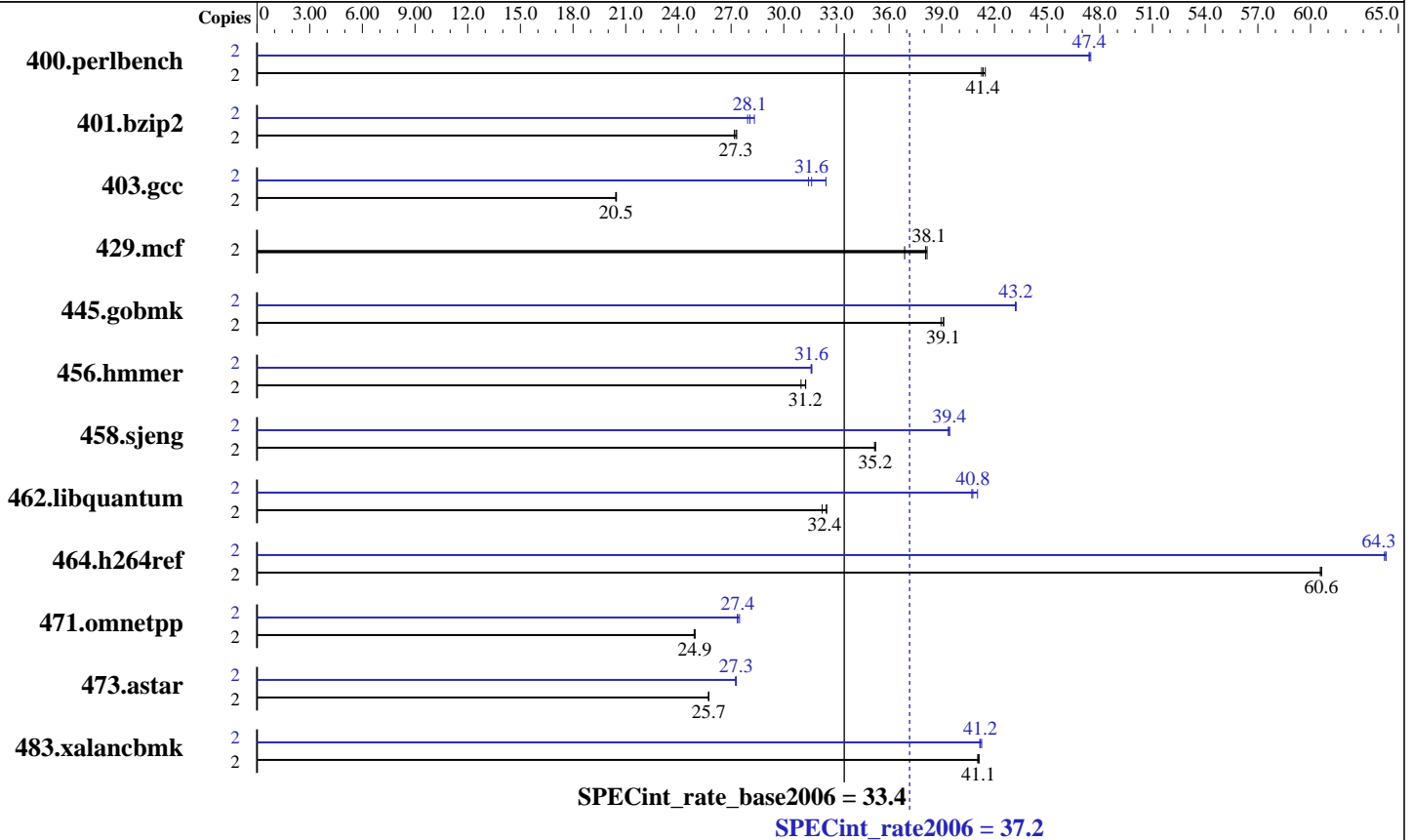
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Sep-2007

Hardware Availability: Jul-2007

Software Availability: May-2007



### Hardware

CPU Name: Intel Core 2 Duo E6850  
 CPU Characteristics: 3.00GHz, 1333MHz Bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4 X 2GB PC2-6400, CL5)  
 Disk Subsystem: 74GB SATA, 7200RPM  
 Other Hardware: None

### Software

Operating System: Windows Vista Ultimate (32-bit)  
 Compiler: Intel C++ Compiler for IA32 version 10.0  
 Build 20070426 Package ID: W\_CC\_P\_10.0.025  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: SmartHeap Library Version 8.0 from  
<http://www.microquill.com/>



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	<b>473</b>	<b>41.4</b>	471	41.5	473	41.3	2	<b>412</b>	<b>47.4</b>	412	47.5	412	47.4
401.bzip2	2	<b>708</b>	<b>27.3</b>	706	27.3	710	27.2	2	<b>688</b>	<b>28.1</b>	681	28.3	691	27.9
403.gcc	2	787	20.5	<b>787</b>	<b>20.5</b>	788	20.4	2	513	31.4	497	32.4	<b>510</b>	<b>31.6</b>
429.mcf	2	478	38.2	<b>479</b>	<b>38.1</b>	495	36.9	2	478	38.2	<b>479</b>	<b>38.1</b>	495	36.9
445.gobmk	2	536	39.1	<b>537</b>	<b>39.1</b>	539	39.0	2	486	43.2	485	43.2	<b>486</b>	<b>43.2</b>
456.hammer	2	<b>597</b>	<b>31.2</b>	597	31.2	602	31.0	2	<b>591</b>	<b>31.6</b>	591	31.6	591	31.6
458.sjeng	2	687	35.2	<b>687</b>	<b>35.2</b>	688	35.2	2	614	39.4	<b>614</b>	<b>39.4</b>	615	39.4
462.libquantum	2	1287	32.2	<b>1278</b>	<b>32.4</b>	1277	32.4	2	<b>1017</b>	<b>40.8</b>	1018	40.7	1010	41.0
464.h264ref	2	<b>730</b>	<b>60.6</b>	730	60.6	731	60.6	2	689	64.2	<b>688</b>	<b>64.3</b>	688	64.3
471.omnetpp	2	501	24.9	502	24.9	<b>501</b>	<b>24.9</b>	2	455	27.5	<b>457</b>	<b>27.4</b>	457	27.4
473.astar	2	546	25.7	<b>546</b>	<b>25.7</b>	546	25.7	2	515	27.3	514	27.3	<b>515</b>	<b>27.3</b>
483.xalancbmk	2	<b>336</b>	<b>41.1</b>	336	41.0	336	41.1	2	335	41.2	334	41.3	<b>335</b>	<b>41.2</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Tested systems can be used with case CSE-733i-450  
To ensure system stability, a 420W (minimum) ATX power supply [4-pin +12V AND (20 or 24-pin)] is required.  
Product description located at:  
<http://www.supermicro.com/products/motherboard/Core2Duo/G33/C2SBA+II.cfm>  
The system bus runs at 1333 MHz  
BIOS Setting : Default

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32



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## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qprefetch /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

401.bzip2: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE

403.gcc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
-link /FORCE:MULTIPLE

429.mcf: basepeak = yes

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## Peak Optimization Flags (Continued)

445.gobmk: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxT -O2 -Qipo  
-Qprec\_div- -Qansi-alias /F512000000  
-link /FORCE:MULTIPLE

456.hmmer: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll2  
-Qansi-alias /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

458.sjeng: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll4  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qunroll4  
-Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

### C++ benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Peak Other Flags

### C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 13:56:08 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 2 October 2007.