



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint[®]_rate2006 = 36.2

ProLiant DL380 G5
(3.0 GHz, Intel Xeon processor 5160)

SPECint_rate_base2006 = 31.9

CPU2006 license: 3

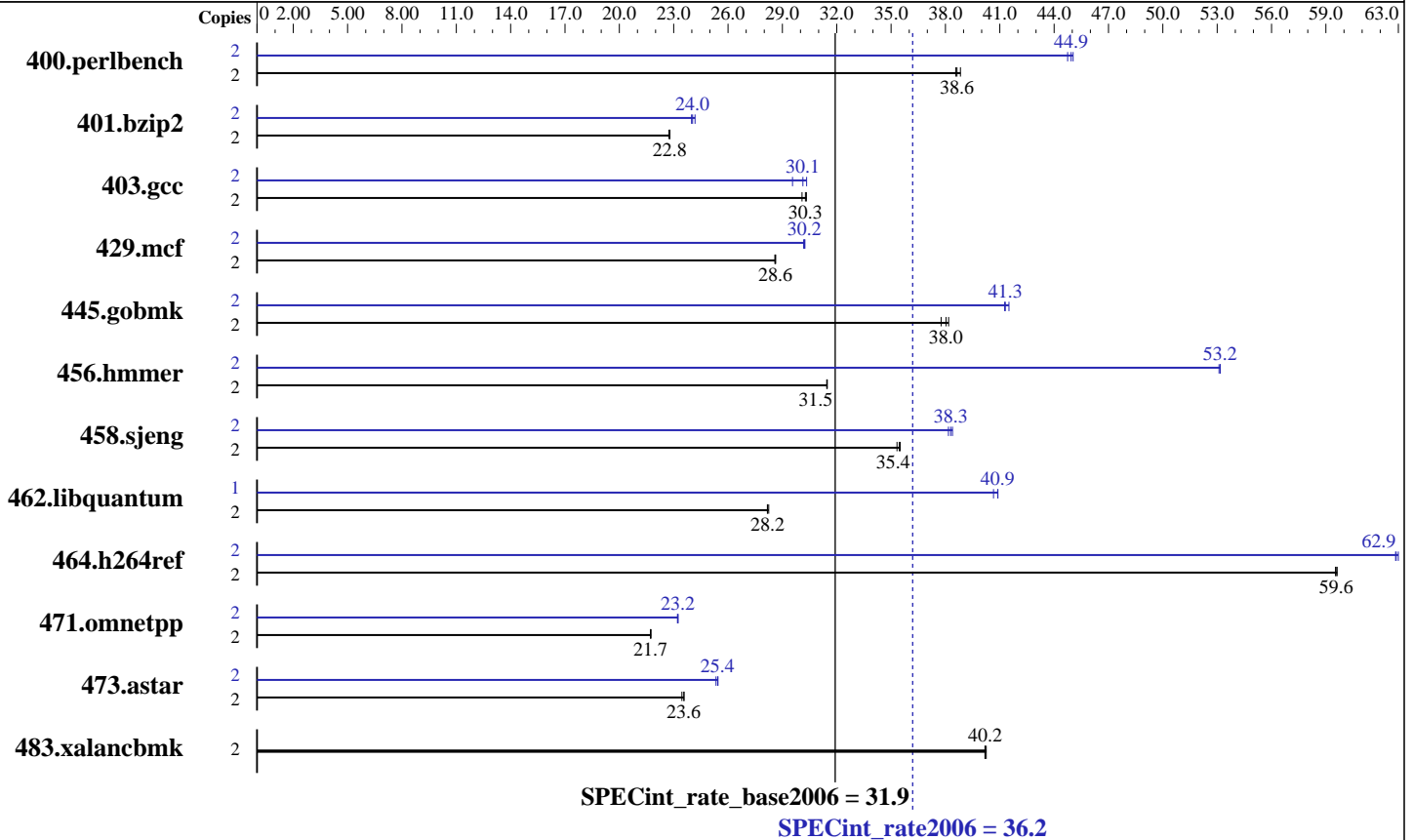
Test date: Aug-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: 3.0 GHz, 2x4 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 or 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 10 K SAS
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1
 kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64
 version 10.1
 Build 20070725
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1
 binutils-2.17.50



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 36.2

ProLiant DL380 G5
(3.0 GHz, Intel Xeon processor 5160)

SPECint_rate_base2006 = 31.9

CPU2006 license: 3

Test date: Aug-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	507	38.6	<u>506</u>	<u>38.6</u>	503	38.8	2	<u>435</u>	<u>44.9</u>	434	45.0	437	44.7
401.bzip2	2	847	22.8	849	22.7	<u>847</u>	<u>22.8</u>	2	798	24.2	<u>803</u>	<u>24.0</u>	805	24.0
403.gcc	2	<u>532</u>	<u>30.3</u>	535	30.1	531	30.3	2	<u>534</u>	<u>30.1</u>	545	29.6	531	30.3
429.mcf	2	637	28.6	638	28.6	<u>638</u>	<u>28.6</u>	2	604	30.2	603	30.2	<u>604</u>	<u>30.2</u>
445.gobmk	2	550	38.2	<u>552</u>	<u>38.0</u>	556	37.8	2	<u>508</u>	<u>41.3</u>	508	41.3	505	41.5
456.hammer	2	593	31.5	<u>593</u>	<u>31.5</u>	593	31.4	2	351	53.1	351	53.2	<u>351</u>	<u>53.2</u>
458.sjeng	2	<u>683</u>	<u>35.4</u>	682	35.5	685	35.3	2	634	38.2	630	38.4	<u>632</u>	<u>38.3</u>
462.libquantum	2	1471	28.2	1467	28.2	<u>1470</u>	<u>28.2</u>	1	510	40.7	507	40.9	<u>507</u>	<u>40.9</u>
464.h264ref	2	<u>743</u>	<u>59.6</u>	743	59.5	742	59.6	2	703	63.0	704	62.8	<u>704</u>	<u>62.9</u>
471.omnetpp	2	<u>575</u>	<u>21.7</u>	575	21.7	576	21.7	2	<u>538</u>	<u>23.2</u>	538	23.2	539	23.2
473.astar	2	599	23.5	596	23.6	<u>596</u>	<u>23.6</u>	2	552	25.4	<u>552</u>	<u>25.4</u>	554	25.3
483.xalancbmk	2	343	40.3	<u>343</u>	<u>40.2</u>	343	40.2	2	343	40.3	<u>343</u>	<u>40.2</u>	343	40.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 36.2

ProLiant DL380 G5
(3.0 GHz, Intel Xeon processor 5160)

SPECint_rate_base2006 = 31.9

CPU2006 license: 3

Test date: Aug-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007

Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 36.2

ProLiant DL380 G5
(3.0 GHz, Intel Xeon processor 5160)

SPECint_rate_base2006 = 31.9

CPU2006 license: 3

Test date: Aug-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL380 G5
(3.0 GHz, Intel Xeon processor 5160)

SPECint_rate2006 = 36.2

SPECint_rate_base2006 = 31.9

CPU2006 license: 3

Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Aug-2007

Hardware Availability: Jun-2006

Software Availability: Nov-2007

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-flags.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 14:00:11 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 October 2007.