



# SPEC® CINT2006 Result

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## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor E5335, 2.00 GHz)

**SPECint®\_rate2006 = 92.2**

**SPECint\_rate\_base2006 = 78.1**

CPU2006 license: 13

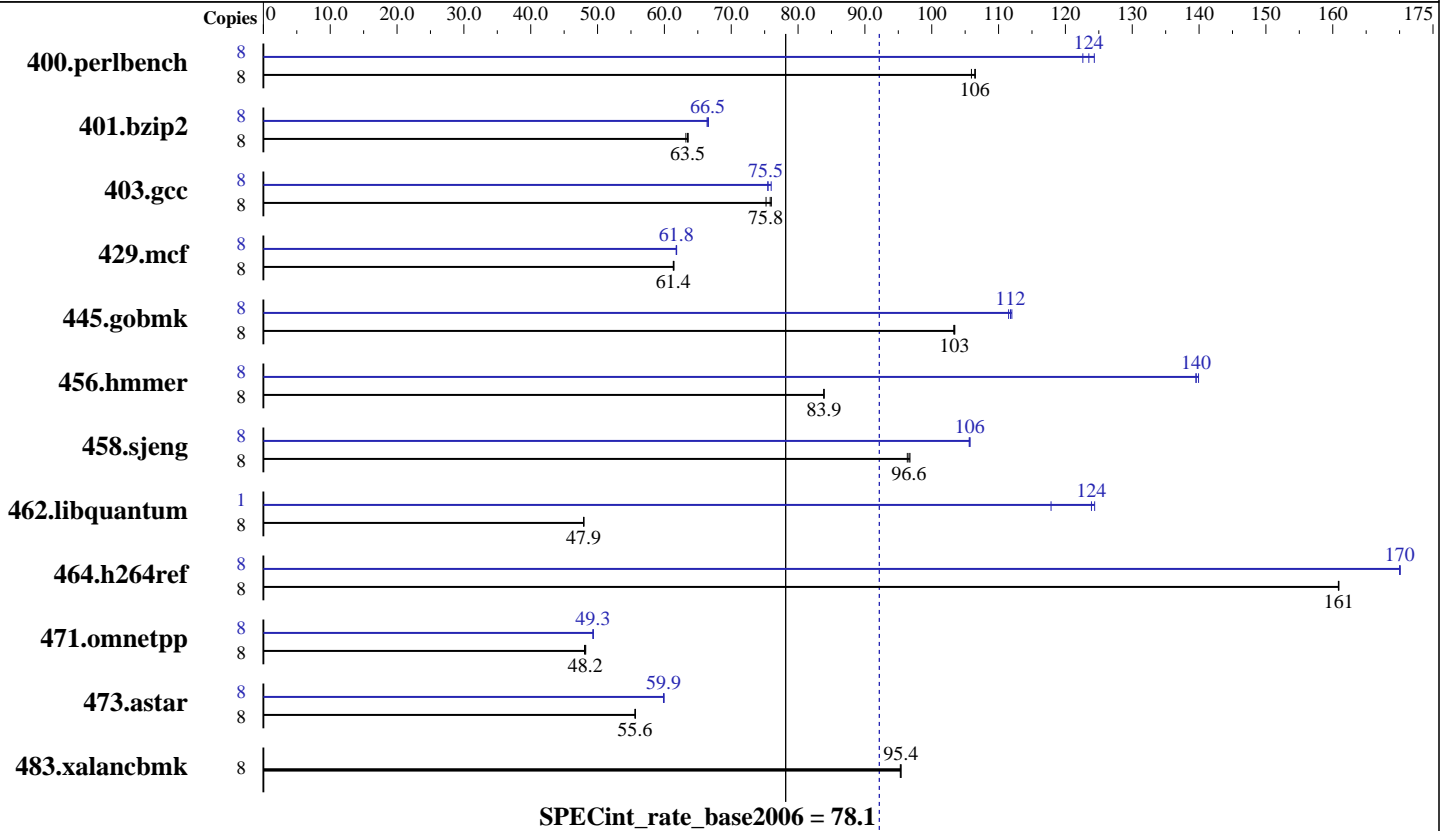
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2007

Hardware Availability: Jul-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5335  
 CPU Characteristics: Quad Core, 2.00 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10 SP1, Kernel linux-cbmg 2.6.16.43-0.5-smp for x86\_64  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	738	106	<b>734</b>	<b>106</b>	734	107	8	629	124	637	123	<b>633</b>	<b>124</b>
401.bzip2	8	1215	63.6	<b>1216</b>	<b>63.5</b>	1221	63.2	8	<b>1161</b>	<b>66.5</b>	1163	66.4	1159	66.6
403.gcc	8	856	75.2	<b>849</b>	<b>75.8</b>	847	76.0	8	<b>853</b>	<b>75.5</b>	853	75.5	848	76.0
429.mcf	8	1190	61.3	1188	61.4	<b>1189</b>	<b>61.4</b>	8	<b>1181</b>	<b>61.8</b>	1181	61.8	1180	61.8
445.gobmk	8	812	103	811	103	<b>812</b>	<b>103</b>	8	<b>751</b>	<b>112</b>	749	112	753	111
456.hmmer	8	<b>890</b>	<b>83.9</b>	890	83.9	890	83.8	8	533	140	535	140	<b>535</b>	<b>140</b>
458.sjeng	8	1001	96.7	1005	96.3	<b>1002</b>	<b>96.6</b>	8	<b>916</b>	<b>106</b>	917	106	916	106
462.libquantum	8	3459	47.9	3457	47.9	<b>3458</b>	<b>47.9</b>	1	167	124	176	118	<b>167</b>	<b>124</b>
464.h264ref	8	1100	161	1101	161	<b>1100</b>	<b>161</b>	8	1041	170	<b>1041</b>	<b>170</b>	1041	170
471.omnetpp	8	1037	48.2	1041	48.0	<b>1038</b>	<b>48.2</b>	8	<b>1014</b>	<b>49.3</b>	1014	49.3	1013	49.4
473.astar	8	1009	55.7	<b>1010</b>	<b>55.6</b>	1010	55.6	8	<b>937</b>	<b>59.9</b>	938	59.9	937	59.9
483.xalanbmk	8	579	95.4	<b>579</b>	<b>95.4</b>	579	95.3	8	579	95.4	<b>579</b>	<b>95.4</b>	579	95.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios settings:

Hardware Prefetcher: Enabled

Adjacent Sector Prefetch: Disabled

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.46.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.46.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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