



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint[®]2006 = 11.0

PowerEdge 6850 (Intel Xeon 7130M, 3.20 GHz)

SPECint_base2006 = 10.3

CPU2006 license: 55

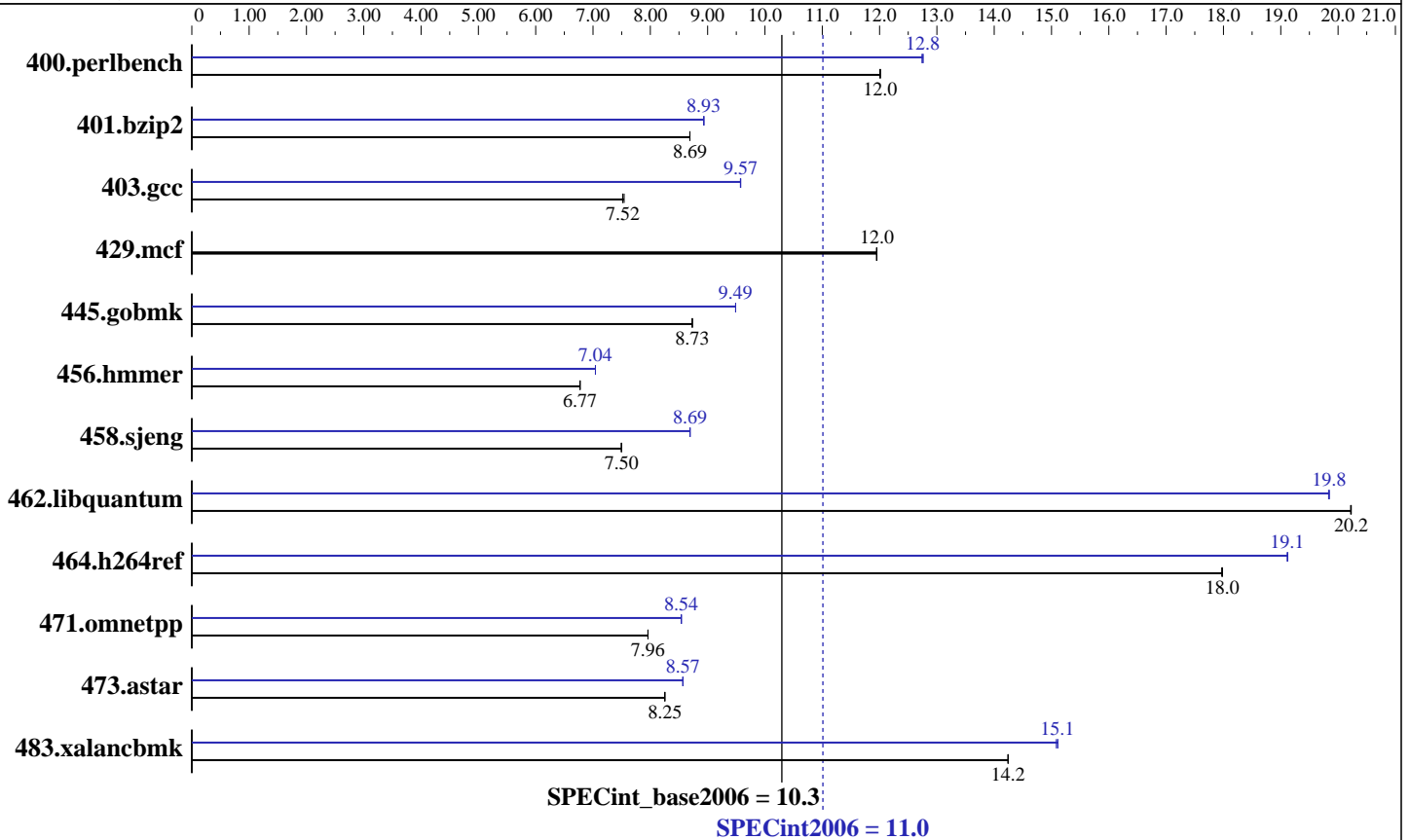
Test date: Aug-2007

Test sponsor: Dell Inc.

Hardware Availability: Aug-2006

Tested by: Dell Inc.

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon 7130M
 CPU Characteristics: 800 MHz Bus Speed
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 12 K micro-ops I + 16 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (8 x 1 GB ECC 400MHz DDR2 SDRAM)
 Disk Subsystem: 1 x 18 GB SCSI 15K RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition
 Compiler: Intel C++ Compiler for IA32 version 10.0
 Build 20070426 Package ID: W_CC_P_10.0.025
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library Version 8.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 11.0

PowerEdge 6850 (Intel Xeon 7130M, 3.20 GHz)

SPECint_base2006 = 10.3

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2007
Hardware Availability: Aug-2006
Software Availability: Jun-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	813	12.0	814	12.0	813	12.0	766	12.8	766	12.8	767	12.7
401.bzip2	1111	8.69	1110	8.69	1110	8.69	1080	8.93	1080	8.93	1080	8.94
403.gcc	1067	7.54	1070	7.52	1071	7.52	841	9.57	841	9.57	841	9.58
429.mcf	763	12.0	764	11.9	763	12.0	763	12.0	764	11.9	763	12.0
445.gobmk	1201	8.73	1201	8.73	1201	8.73	1105	9.49	1106	9.49	1106	9.49
456.hammer	1377	6.78	1377	6.77	1378	6.77	1325	7.04	1325	7.04	1325	7.04
458.sjeng	1614	7.50	1614	7.50	1615	7.49	1392	8.69	1392	8.69	1392	8.69
462.libquantum	1024	20.2	1024	20.2	1025	20.2	1045	19.8	1044	19.8	1044	19.8
464.h264ref	1232	18.0	1231	18.0	1231	18.0	1158	19.1	1157	19.1	1158	19.1
471.omnetpp	785	7.96	785	7.96	785	7.96	732	8.54	732	8.54	732	8.54
473.astar	851	8.25	850	8.25	850	8.25	819	8.57	819	8.57	819	8.57
483.xalancbmk	484	14.2	485	14.2	485	14.2	457	15.1	457	15.1	457	15.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-QxP -Qipo -O3 -Qprec-div- /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
C++ benchmarks:
-QxP -Qipo -O3 -Qprec-div- -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 11.0

PowerEdge 6850 (Intel Xeon 7130M, 3.20 GHz)

SPECint_base2006 = 10.3

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2007
Hardware Availability: Aug-2006
Software Availability: Jun-2007

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qansi-alias -Qprefetch /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- /F512000000 -link /FORCE:MULTIPLE

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000
-link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll2 -Qansi-alias /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll14 /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint2006 = 11.0

PowerEdge 6850 (Intel Xeon 7130M, 3.20 GHz)

SPECint_base2006 = 10.3

CPU2006 license: 55

Test date: Aug-2007

Test sponsor: Dell Inc.

Hardware Availability: Aug-2006

Tested by: Dell Inc.

Software Availability: Jun-2007

Peak Optimization Flags (Continued)

```
462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
                -Qprec-div- -Qunroll4 -Ob0 -Qprefetch
                -Qopt-streaming-stores:always /F512000000 shlw32m.lib
                -link /FORCE:MULTIPLE
```

464.h264ref: Same as 456.hmmer

C++ benchmarks:

```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3 -Qprec-div-
-Qansi-alias -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32-intel64-win.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Dell-Intel-ic10-ia32-intel64-win.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 13:06:23 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 4 September 2007.