



SPEC® CINT2006 Result

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Lenovo Group Limited

(Test Sponsor: Intel Corporation)

SPECint®2006 = Not Run

Lenovo ThinkPad T43(Intel Pentium M 750)

SPECint_base2006 = 9.04

CPU2006 license: 13

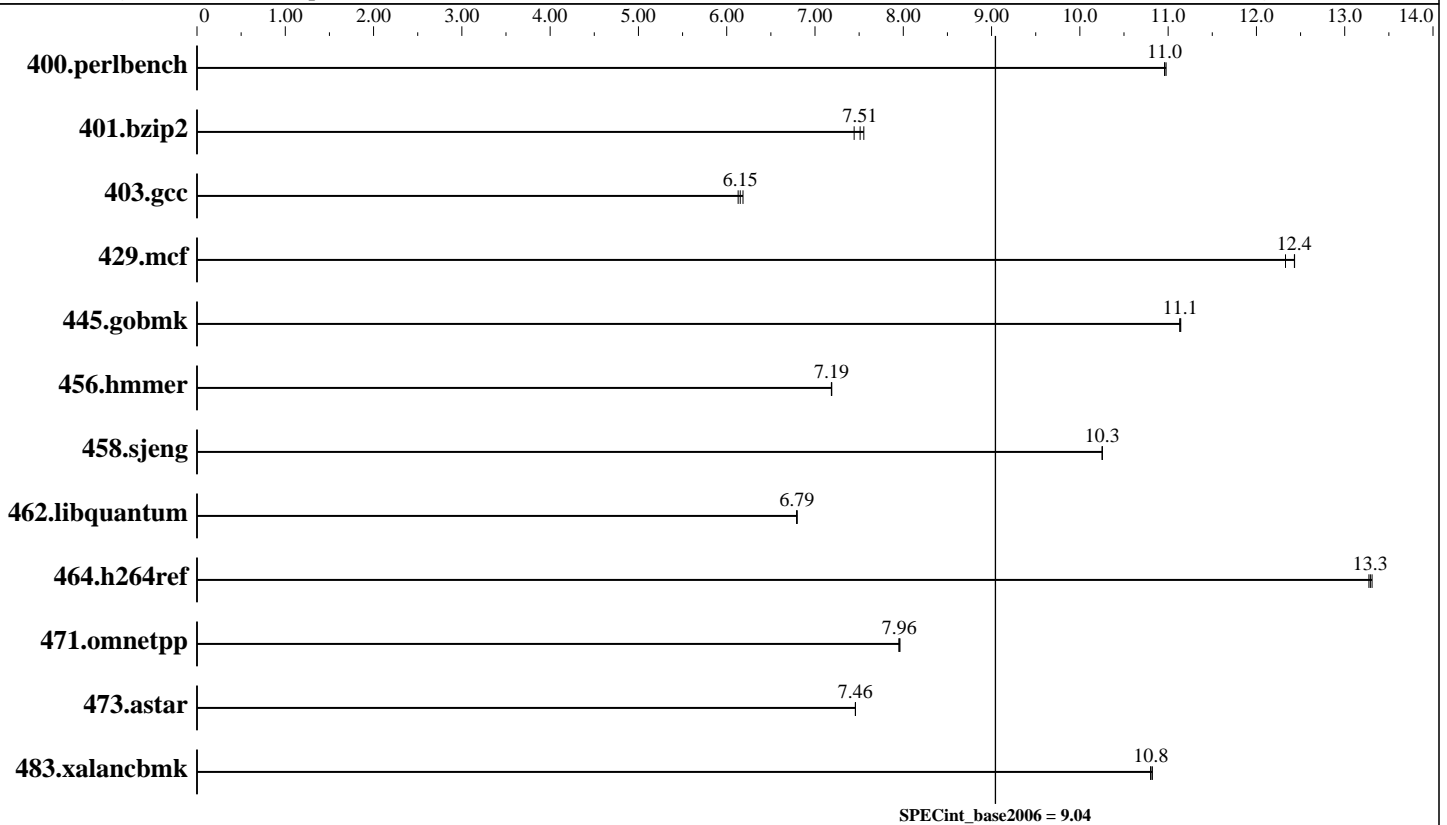
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: Feb-2007



Hardware

CPU Name: Intel Pentium M 750
 CPU Characteristics: 1.86 GHz 2MB L2
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per chip
 Secondary Cache: 2 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 1 GB (2x512 MB Samsung* PC2-4200s DDR2-533 4-4-4-12)
 Disk Subsystem: Hitachi* HTS721010G9 SATA 100GB 7200RPM
 Other Hardware: None

Software

Operating System: Windows Vista32 Business
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: SmartHeap library V7.4 from Microquill



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	890	11.0	891	11.0	891	11.0						
401.bzip2	1285	7.51	1296	7.44	1278	7.55						
403.gcc	1308	6.15	1302	6.18	1313	6.13						
429.mcf	740	12.3	734	12.4	734	12.4						
445.gobmk	942	11.1	942	11.1	941	11.1						
456.hammer	1298	7.19	1298	7.19	1298	7.19						
458.sjeng	1180	10.3	1180	10.3	1180	10.3						
462.libquantum	3048	6.80	3050	6.79	3051	6.79						
464.h264ref	1667	13.3	1663	13.3	1665	13.3						
471.omnetpp	786	7.96	785	7.96	786	7.95						
473.astar	941	7.46	941	7.46	941	7.46						
483.xalancbmk	638	10.8	639	10.8	639	10.8						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-QxN -Qipo -O3 -Qprec-div- /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-QxN -Qipo -O3 -Qprec-div- -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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