



# SPEC® CINT2006 Result

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Lenovo Group Limited

(Test Sponsor: Intel Corporation)

SPECint®2006 = Not Run

Lenovo ThinkPad T61(Intel Core 2 Duo T7100)

SPECint\_base2006 = 11.4

CPU2006 license: 13

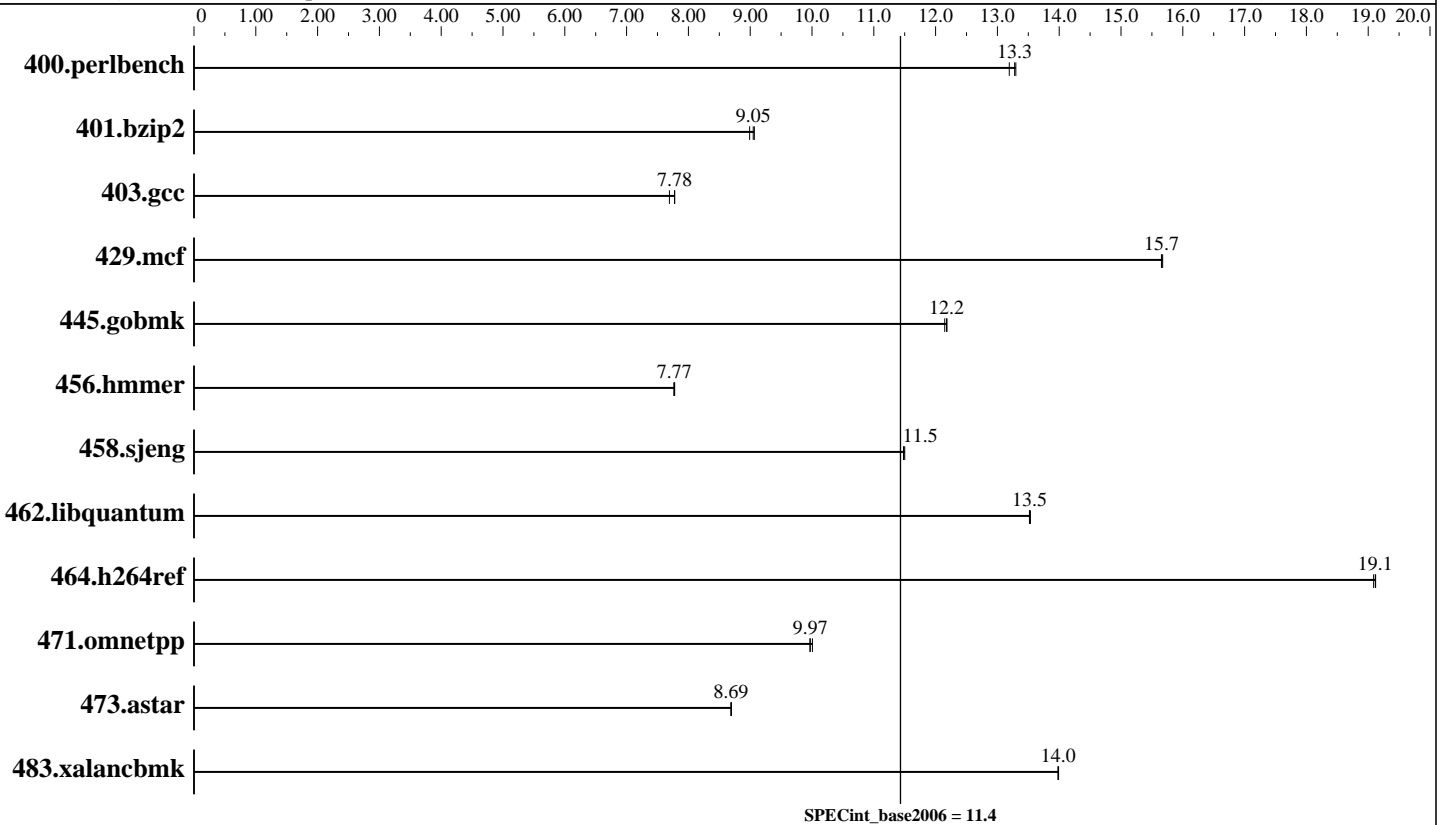
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jun-2007

Hardware Availability: Jun-2007

Software Availability: Feb-2007



## Hardware

CPU Name: Intel Core 2 Duo T7100  
 CPU Characteristics: 1.80 GHz, 2MB L2, 800 MHz Bus  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 2 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2x1GB Qimonda\* PC-5300 DDR2-667 5-5-5-12)  
 Disk Subsystem: Hitachi\* HTS721010G9 SATA 100GB 7200RPM  
 Other Hardware: None

## Software

Operating System: Windows Vista32 Business  
 Compiler: Intel C++ Compiler for IA32 version 9.1  
 Build no 20060816  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: SmartHeap library V7.4 from Microquill



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	741	13.2	<u>736</u>	<u>13.3</u>	735	13.3						
401.bzip2	1073	8.99	<u>1066</u>	<u>9.05</u>	1064	9.07						
403.gcc	1035	7.78	<u>1035</u>	<u>7.78</u>	1046	7.69						
429.mcf	582	15.7	<u>582</u>	<u>15.7</u>	583	15.7						
445.gobmk	864	12.1	861	12.2	<u>861</u>	<u>12.2</u>						
456.hammer	1200	7.78	<u>1201</u>	<u>7.77</u>	1201	7.77						
458.sjeng	<u>1053</u>	<u>11.5</u>	1054	11.5	1052	11.5						
462.libquantum	1531	13.5	<u>1533</u>	<u>13.5</u>	1533	13.5						
464.h264ref	1157	19.1	<u>1158</u>	<u>19.1</u>	1159	19.1						
471.omnetpp	<u>627</u>	<u>9.97</u>	625	10.0	627	9.97						
473.astar	807	8.70	808	8.69	<u>808</u>	<u>8.69</u>						
483.xalancbmk	<u>493</u>	<u>14.0</u>	493	14.0	494	14.0						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Binaries built on Windows XP Professional w/ SP2  
Vista Power Profile set to High Performance

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 12:24:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 21 August 2007.