



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX200 S3, Intel Xeon processor 5140,
2.33 GHz

SPECint®_rate2006 = 50.0

SPECint_rate_base2006 = 45.8

CPU2006 license: 22

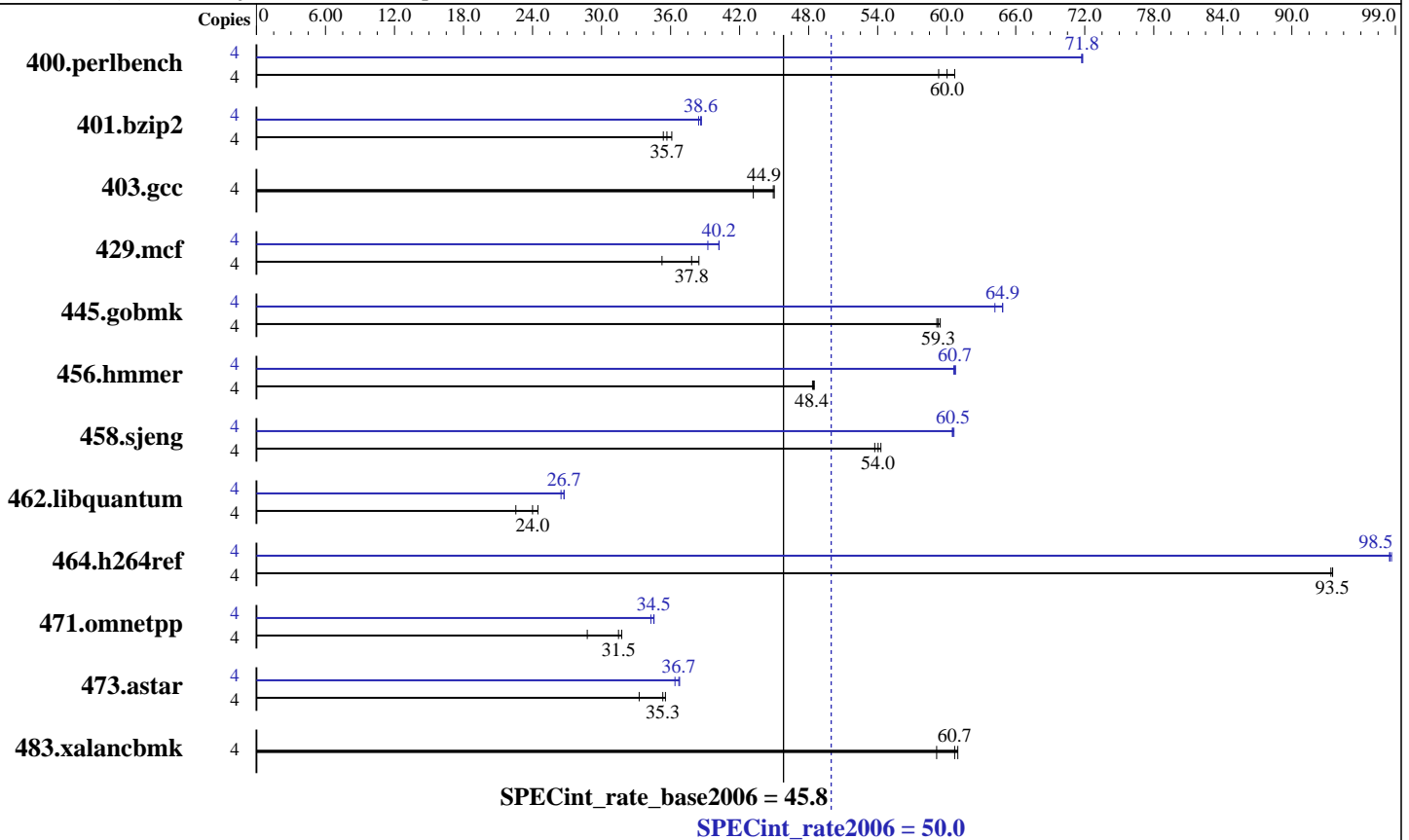
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jul-2007

Hardware Availability: Jul-2006

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon 5140
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: Seagate ST336754SS (SAS, 36GB, 15000rpm)
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: I_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	4	659	59.3	<u>651</u>	<u>60.0</u>	644	60.7	4	<u>545</u>	<u>71.8</u>	545	71.7	544	71.8		
401.bzip2	4	1091	35.4	<u>1082</u>	<u>35.7</u>	1069	36.1	4	<u>1000</u>	<u>38.6</u>	1004	38.4	998	38.7		
403.gcc	4	746	43.2	<u>717</u>	<u>44.9</u>	715	45.0	4	746	43.2	<u>717</u>	<u>44.9</u>	715	45.0		
429.mcf	4	1035	35.3	<u>964</u>	<u>37.8</u>	949	38.5	4	930	39.2	907	40.2	<u>908</u>	<u>40.2</u>		
445.gobmk	4	709	59.1	<u>708</u>	<u>59.3</u>	706	59.4	4	654	64.2	<u>647</u>	<u>64.9</u>	647	64.9		
456.hmmmer	4	772	48.3	770	48.5	<u>771</u>	<u>48.4</u>	4	<u>615</u>	<u>60.7</u>	614	60.8	616	60.6		
458.sjeng	4	901	53.7	<u>896</u>	<u>54.0</u>	892	54.3	4	798	60.6	800	60.5	<u>800</u>	<u>60.5</u>		
462.libquantum	4	3676	22.5	<u>3453</u>	<u>24.0</u>	3388	24.5	4	3131	26.5	3100	26.7	<u>3100</u>	<u>26.7</u>		
464.h264ref	4	<u>946</u>	<u>93.5</u>	946	93.6	948	93.4	4	899	98.5	<u>899</u>	<u>98.5</u>	897	98.7		
471.omnetpp	4	869	28.8	<u>794</u>	<u>31.5</u>	787	31.8	4	729	34.3	723	34.6	<u>724</u>	<u>34.5</u>		
473.astar	4	844	33.3	<u>795</u>	<u>35.3</u>	790	35.6	4	772	36.4	<u>765</u>	<u>36.7</u>	763	36.8		
483.xalancbmk	4	467	59.1	<u>455</u>	<u>60.7</u>	453	61.0	4	467	59.1	<u>455</u>	<u>60.7</u>	453	61.0		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -O3 -ipo -no-prec-div -ansi-alias

-L/opt/SmartHeap_8_1/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-L/opt/SmartHeap_8_1/lib -lsmarheap

445.gobmk: Same as 400.perlbench

456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2

458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4

462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-opt-streaming-stores always

464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml



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For other inquiries, please contact webmaster@spec.org.

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