



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX200 S3, Intel Xeon processor E5335,
2.0 GHz

SPECint®_rate2006 = 44.1

SPECint_rate_base2006 = 40.7

CPU2006 license: 22

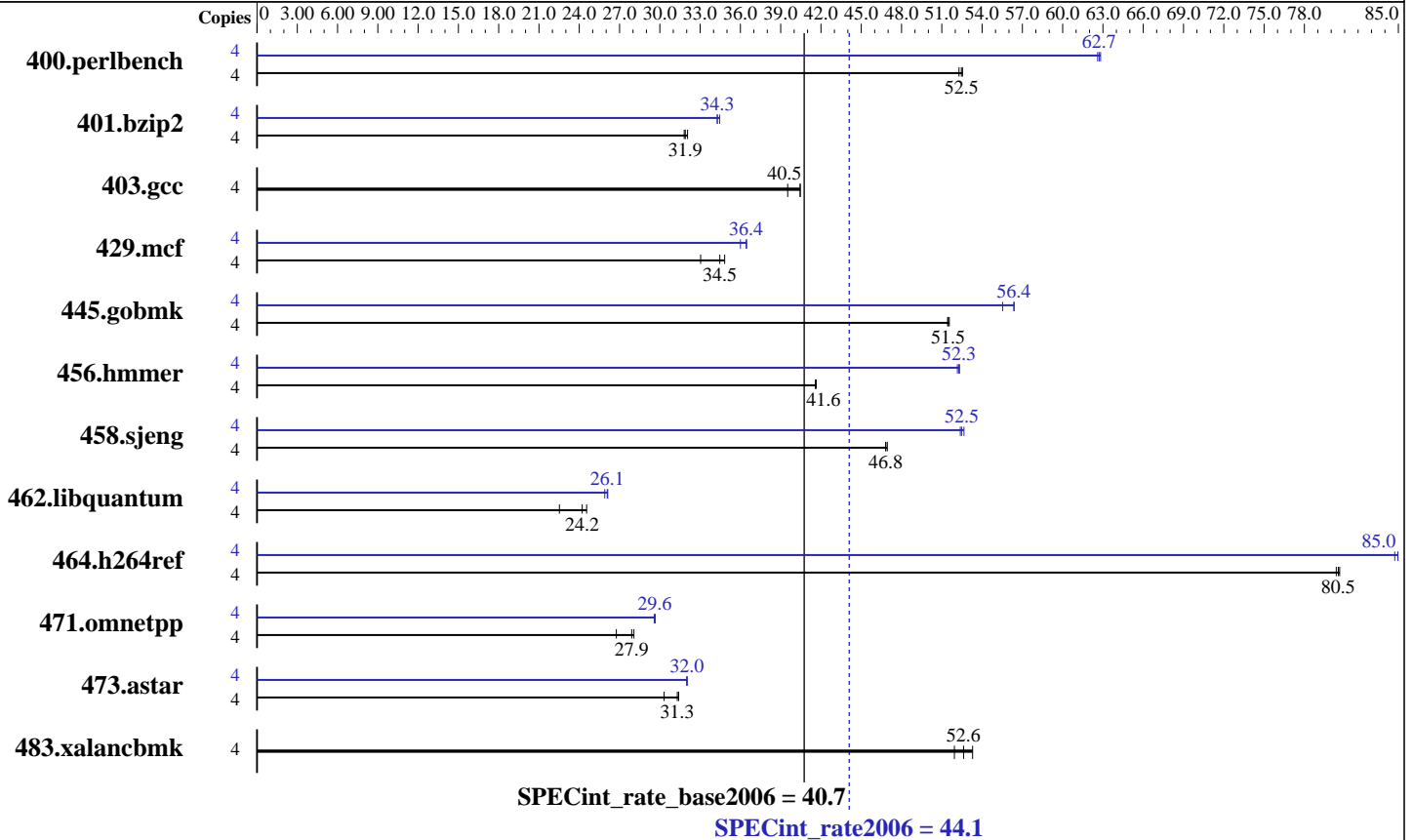
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Aug-2007

Hardware Availability: Feb-2007

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon E5335
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: Seagate ST336754SS (SAS, 36GB, 15000rpm)
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: I_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	748	52.3	<u>745</u>	<u>52.5</u>	744	52.5	4	622	62.8	624	62.6	<u>623</u>	<u>62.7</u>
401.bzip2	4	1213	31.8	<u>1210</u>	<u>31.9</u>	1204	32.1	4	1126	34.3	1120	34.5	<u>1126</u>	<u>34.3</u>
403.gcc	4	815	39.5	<u>796</u>	<u>40.5</u>	796	40.5	4	815	39.5	<u>796</u>	<u>40.5</u>	796	40.5
429.mcf	4	1104	33.0	<u>1058</u>	<u>34.5</u>	1047	34.8	4	1013	36.0	1000	36.5	<u>1001</u>	<u>36.4</u>
445.gobmk	4	<u>815</u>	<u>51.5</u>	816	51.4	814	51.6	4	<u>744</u>	<u>56.4</u>	756	55.5	744	56.4
456.hmmmer	4	<u>897</u>	<u>41.6</u>	896	41.6	897	41.6	4	<u>714</u>	<u>52.3</u>	715	52.2	713	52.3
458.sjeng	4	<u>1034</u>	<u>46.8</u>	1034	46.8	1031	46.9	4	919	52.7	<u>922</u>	<u>52.5</u>	924	52.4
462.libquantum	4	3680	22.5	<u>3422</u>	<u>24.2</u>	3376	24.5	4	3200	25.9	<u>3178</u>	<u>26.1</u>	3173	26.1
464.h264ref	4	1101	80.4	<u>1099</u>	<u>80.5</u>	1098	80.6	4	1042	85.0	1045	84.7	<u>1042</u>	<u>85.0</u>
471.omnetpp	4	934	26.8	<u>896</u>	<u>27.9</u>	891	28.1	4	845	29.6	843	29.7	<u>844</u>	<u>29.6</u>
473.astar	4	926	30.3	<u>897</u>	<u>31.3</u>	894	31.4	4	877	32.0	877	32.0	<u>877</u>	<u>32.0</u>
483.xalancbmk	4	531	51.9	<u>525</u>	<u>52.6</u>	518	53.3	4	531	51.9	<u>525</u>	<u>52.6</u>	518	53.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -O3 -ipo -no-prec-div -ansi-alias

-L/opt/SmartHeap_8_1/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-L/opt/SmartHeap_8_1/lib -lsmarheap

445.gobmk: Same as 400.perlbench

456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2

458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4

462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-opt-streaming-stores always

464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml



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For other inquiries, please contact webmaster@spec.org.

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