



SPEC® CINT2006 Result

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Acer Incorporated
(Test Sponsor: Intel Corporation)

SPECint®2006 = Not Run

Acer Ferrari 5000 (AMD Turion 64 X2 TL-60)

SPECint_base2006 = 7.66

CPU2006 license: 13

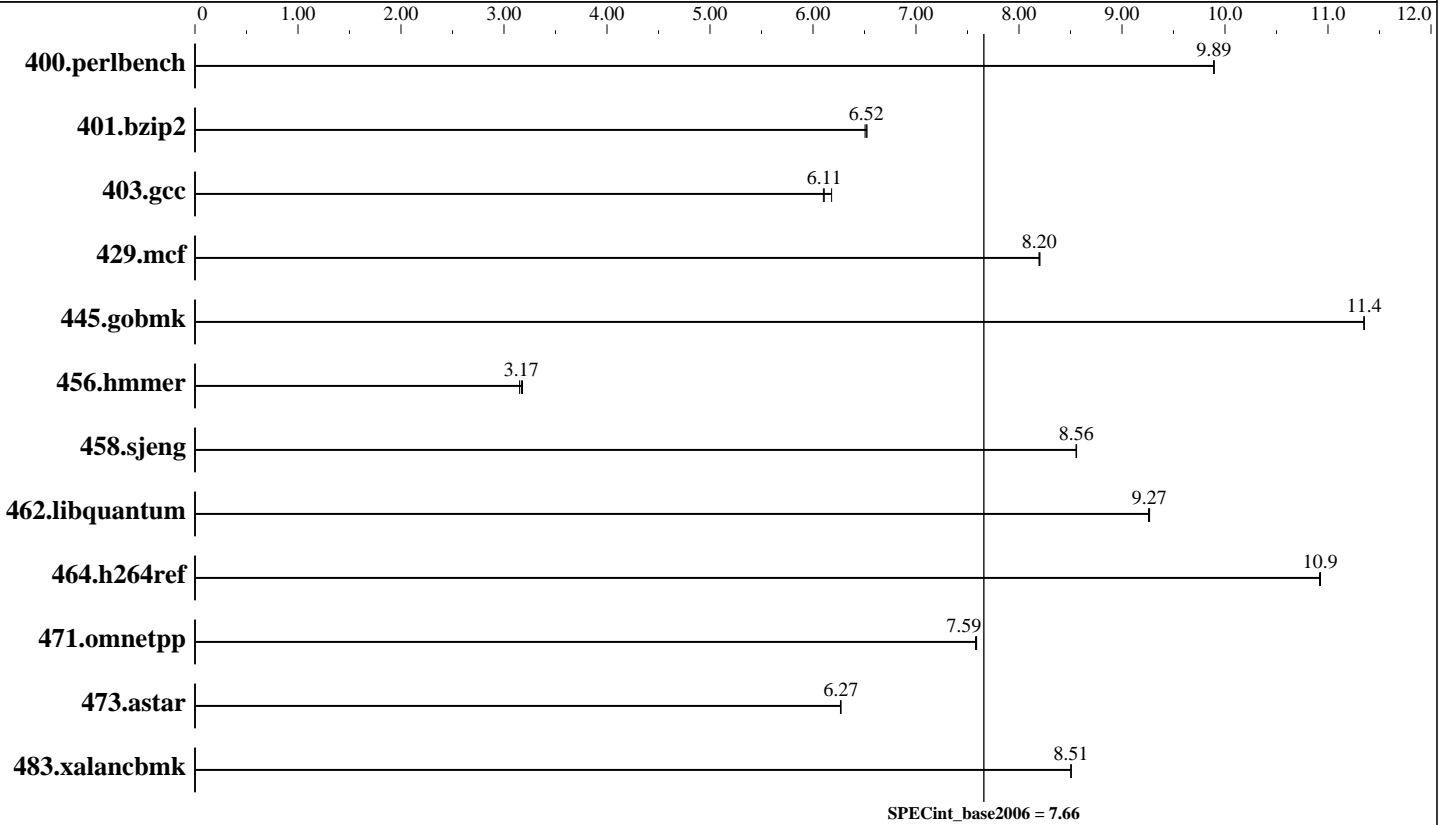
Test date: Jun-2007

Test sponsor: Intel Corporation

Hardware Availability: Jun-2007

Tested by: Intel Corporation

Software Availability: Feb-2007



Hardware

CPU Name: AMD Turion 64 X2 TL-60
CPU Characteristics: 2.0 GHz 2x512KB L2
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 64 KB I + 64 KB D on chip per core
Secondary Cache: 512 KB I+D on chip per core
L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Samsung DDR2-667 CL5)
Disk Subsystem: Hitachi 160GB 5400rpm SATA HDD
Other Hardware: None

Software

Operating System: Windows Vista32 Business
Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: SmartHeap Library Version 8.0 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	988	9.89	988	9.89	987	9.90						
401.bzip2	1483	6.51	1479	6.52	1481	6.52						
403.gcc	1302	6.18	1318	6.11	1320	6.10						
429.mcf	1113	8.20	1113	8.20	1112	8.20						
445.gobmk	924	11.4	924	11.4	924	11.3						
456.hammer	2934	3.18	2944	3.17	2961	3.15						
458.sjeng	1414	8.55	1414	8.56	1414	8.56						
462.libquantum	2236	9.27	2238	9.26	2236	9.27						
464.h264ref	2025	10.9	2027	10.9	2025	10.9						
471.omnetpp	825	7.58	824	7.59	824	7.59						
473.astar	1120	6.27	1120	6.27	1119	6.27						
483.xalancbmk	811	8.50	811	8.51	811	8.51						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-arch:SSE2 -Qprec-div- -O3 -Qipo /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:
-arch:SSE2 -Qprec-div- -O3 -Qipo -Oi- -Qcxx_features /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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