



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = **20.0**

Intel DG965WH motherboard (Intel Core 2 Duo E6700)

SPECint_base2006 = **17.9**

CPU2006 license: 13

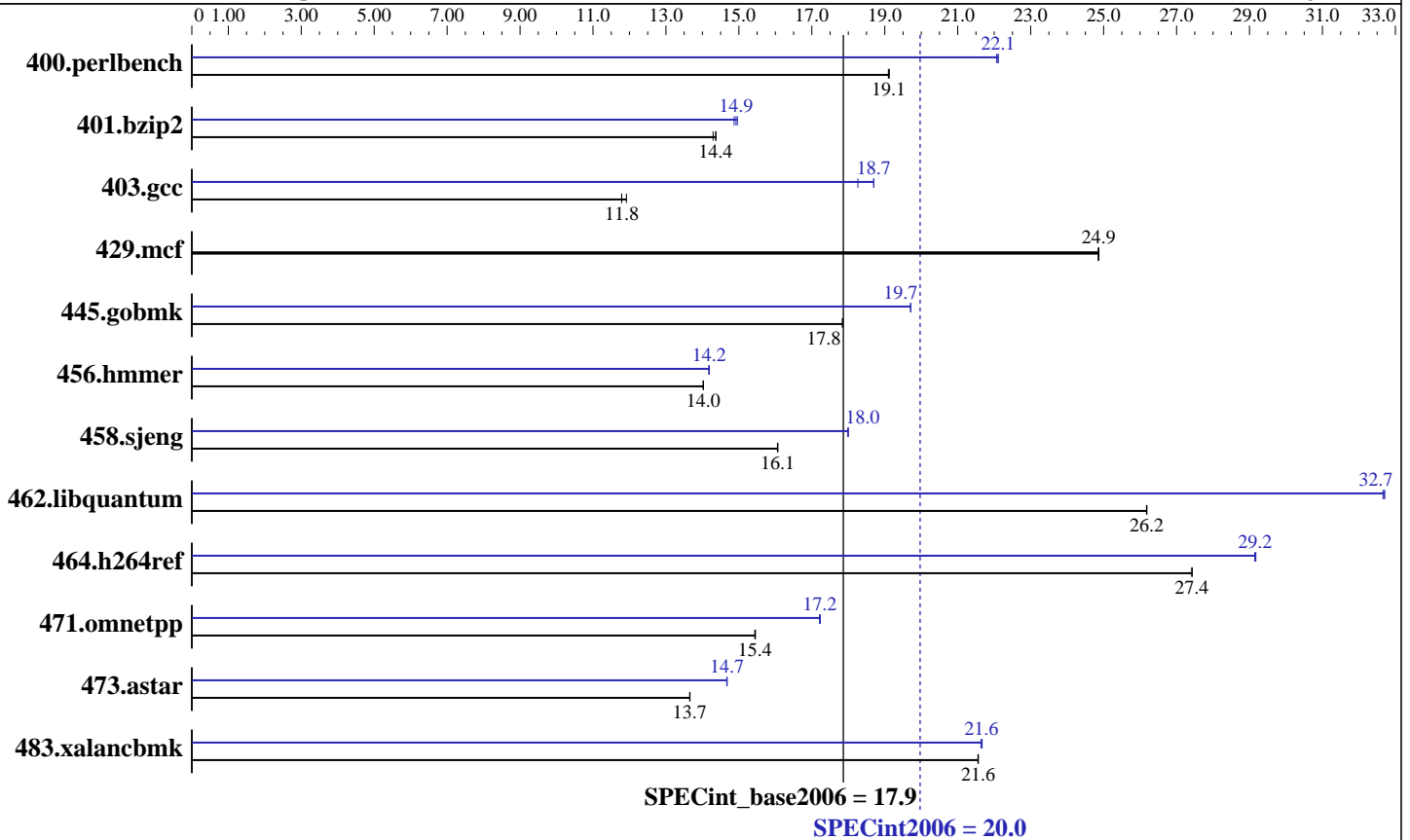
Test date: Jun-2007

Test sponsor: Intel Corporation

Hardware Availability: Aug-2006

Tested by: Intel Corporation

Software Availability: Aug-2006



Hardware

CPU Name: Intel Core 2 Duo E6700
 CPU Characteristics: 2.67 GHz, 1066 MHz bus
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (2 1GB Micron MT16HTF12864AY-80ED4 DDR2 800, CL5)
 Disk Subsystem: Seagate ST3320620AS 320GB Barracuda 7200.10 NCQ SATA II
 Other Hardware: None

Software

Operating System: Windows Vista32 Ultimate
 Compiler: Intel C++ Compiler for IA32 version 10.0
 Build 20070426 Package ID: W_CC_P_10.0.025
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.0 from <http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>511</u>	<u>19.1</u>	511	19.1	512	19.1	443	22.1	<u>442</u>	<u>22.1</u>	442	22.1
401.bzip2	675	14.3	<u>672</u>	<u>14.4</u>	672	14.4	645	15.0	649	14.9	<u>647</u>	<u>14.9</u>
403.gcc	683	11.8	<u>683</u>	<u>11.8</u>	676	11.9	441	18.3	431	18.7	<u>431</u>	<u>18.7</u>
429.mcf	367	24.9	<u>367</u>	<u>24.9</u>	367	24.9	367	24.9	<u>367</u>	<u>24.9</u>	367	24.9
445.gobmk	588	17.8	<u>588</u>	<u>17.8</u>	588	17.8	<u>532</u>	<u>19.7</u>	532	19.7	532	19.7
456.hammer	<u>665</u>	<u>14.0</u>	665	14.0	665	14.0	<u>658</u>	<u>14.2</u>	658	14.2	658	14.2
458.sjeng	<u>753</u>	<u>16.1</u>	753	16.1	754	16.1	673	18.0	<u>672</u>	<u>18.0</u>	672	18.0
462.libquantum	<u>791</u>	<u>26.2</u>	791	26.2	792	26.2	<u>634</u>	<u>32.7</u>	633	32.7	634	32.7
464.h264ref	807	27.4	<u>807</u>	<u>27.4</u>	807	27.4	<u>759</u>	<u>29.2</u>	759	29.2	759	29.1
471.omnetpp	<u>405</u>	<u>15.4</u>	405	15.4	404	15.5	<u>363</u>	<u>17.2</u>	363	17.2	363	17.2
473.astar	514	13.7	<u>514</u>	<u>13.7</u>	514	13.7	<u>479</u>	<u>14.7</u>	479	14.7	478	14.7
483.xalancbmk	<u>320</u>	<u>21.6</u>	320	21.6	320	21.6	319	21.6	318	21.7	<u>319</u>	<u>21.6</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
 Product description located as of 7/2007:
<http://www.intel.com/products/motherboard/DG965WH/index.htm>
 The system bus runs at 1066 MHz
 System has a discrete gfx card - Asus EN8800GTX/HTDP/768M w/ nVidia 8800GTX
 Binaries were built on Windows XP Professional SP2 with 4GB of RAM and /3GB boot switch

Base Compiler Invocation

C benchmarks:
 icl -Qvc7.1 -Qc99

C++ benchmarks:
 icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32



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Base Optimization Flags

C benchmarks:

-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbenc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qprefetch /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
-link /FORCE:MULTIPLE

429.mcf: basepeak = yes

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Peak Optimization Flags (Continued)

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec_div- -Qansi-alias /F512000000
-link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
-Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.42.xml>

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For other inquiries, please contact webmaster@spec.org.

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