



# SPEC® CINT2006 Result

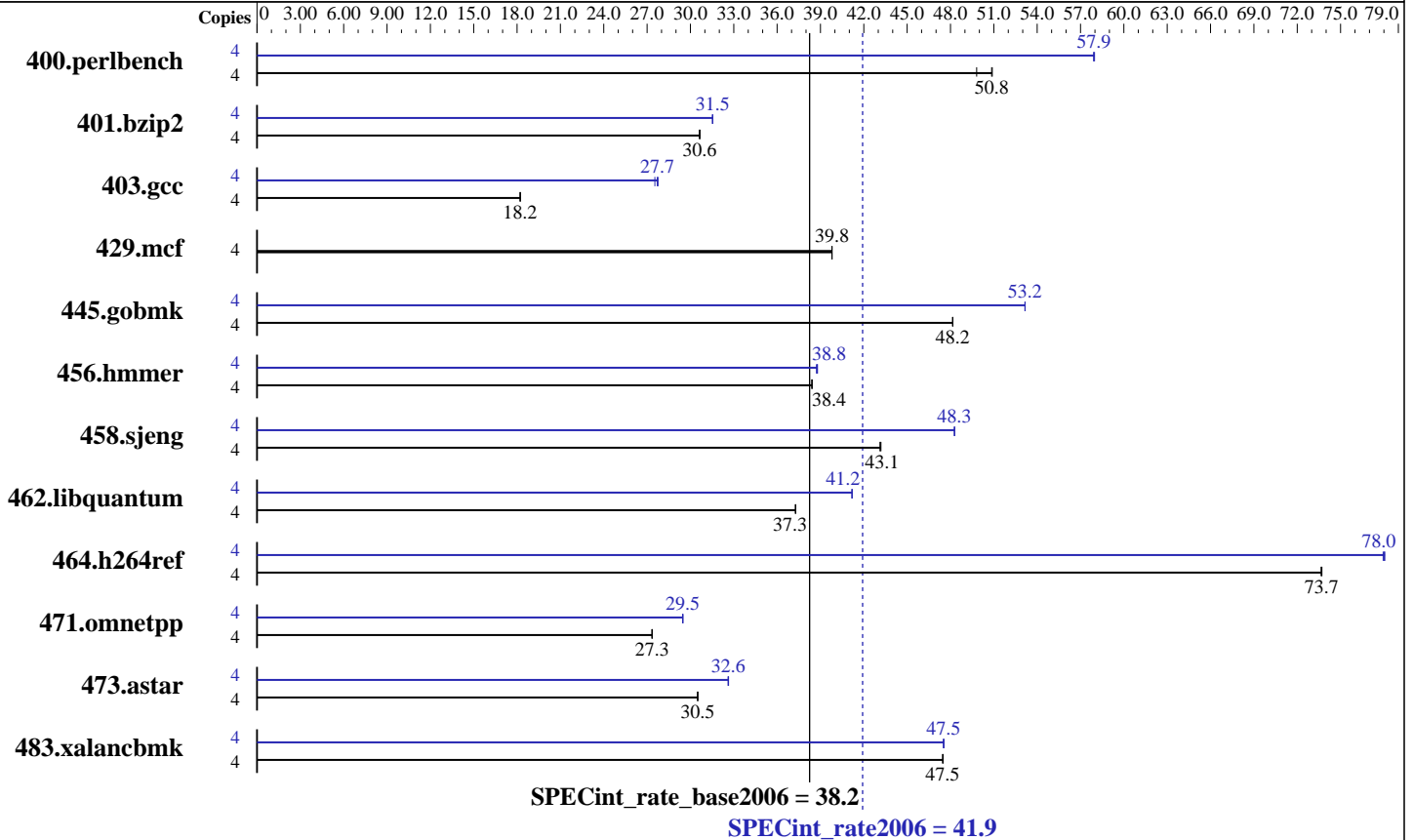
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## Supermicro Motherboard X7DB8

**SPECint®\_rate2006 = 41.9**  
**SPECint\_rate\_base2006 = 38.2**

CPU2006 license: 001176  
Test sponsor: Supermicro  
Tested by: Supermicro

Test date: Jun-2007  
Hardware Availability: May-2007  
Software Availability: Apr-2007



### Hardware

CPU Name: Intel Xeon 5120  
 CPU Characteristics: 1.86GHz 1066MHz System Bus  
 CPU MHz: 1860  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 X 2GB ECC PC2-5300, CL5, FBDIMM)  
 Disk Subsystem: 250GB SATA, 7200RPM  
 Other Hardware: None

### Software

Operating System: Windows Server 2003 Enterprise Edition W/ SP1  
 Compiler: Intel C++ Compiler for IA32 version 10.0  
 Build 20070426 Package ID: W\_CC\_P\_10.0.025  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: SmartHeap Library Version 8.0 from  
<http://www.microquill.com/>



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### Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	785	49.8	<b>769</b>	<b>50.8</b>	768	50.9	4	675	57.9	<b>675</b>	<b>57.9</b>	674	58.0
401.bzip2	4	<b>1260</b>	<b>30.6</b>	1261	30.6	1259	30.7	4	1224	31.5	<b>1224</b>	<b>31.5</b>	1224	31.5
403.gcc	4	<b>1767</b>	<b>18.2</b>	1769	18.2	1766	18.2	4	1169	27.5	1160	27.8	<b>1162</b>	<b>27.7</b>
429.mcf	4	917	39.8	<b>917</b>	<b>39.8</b>	917	39.8	4	917	39.8	<b>917</b>	<b>39.8</b>	917	39.8
445.gobmk	4	871	48.2	871	48.2	<b>871</b>	<b>48.2</b>	4	789	53.2	<b>789</b>	<b>53.2</b>	789	53.2
456.hammer	4	972	38.4	<b>972</b>	<b>38.4</b>	971	38.4	4	<b>962</b>	<b>38.8</b>	964	38.7	962	38.8
458.sjeng	4	<b>1122</b>	<b>43.1</b>	1121	43.2	1122	43.1	4	1002	48.3	<b>1003</b>	<b>48.3</b>	1003	48.3
462.libquantum	4	2223	37.3	2224	37.3	<b>2223</b>	<b>37.3</b>	4	2012	41.2	<b>2012</b>	<b>41.2</b>	2012	41.2
464.h264ref	4	1201	73.7	<b>1201</b>	<b>73.7</b>	1202	73.7	4	1136	77.9	<b>1135</b>	<b>78.0</b>	1134	78.1
471.omnetpp	4	914	27.3	<b>914</b>	<b>27.3</b>	914	27.4	4	<b>848</b>	<b>29.5</b>	849	29.5	848	29.5
473.astar	4	<b>921</b>	<b>30.5</b>	920	30.5	921	30.5	4	860	32.6	<b>861</b>	<b>32.6</b>	861	32.6
483.xalancbmk	4	582	47.5	581	47.5	<b>581</b>	<b>47.5</b>	4	581	47.5	581	47.5	<b>581</b>	<b>47.5</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### General Notes

Tested systems can be used with CSE-825S2-R700LPV case,  
To ensure system stability, a 500W (minimum) ATX power supply [4-pin (+12V), 8-pin (+12V) and 24-pin are required]  
Product description located as of <http://www.supermicro.com/products/motherboard/Xeon1333/5000P/X7DB8.cfm>  
The system bus runs at 1066 MHz  
"start /b /wait /affinity" used to bind processes to CPUs.

### Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
  
C++ benchmarks:  
icl -Qvc7.1

### Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32



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## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Peak Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qansi-alias  
-Qprefetch /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE

401.bzip2: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
shlw32m.lib -link /FORCE:MULTIPLE

403.gcc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast /F512000000  
-link /FORCE:MULTIPLE

429.mcf: basepeak = yes

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## Peak Optimization Flags (Continued)

```

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
           -Qprec_div- -Qansi-alias /F512000000
           -link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
           -Qansi-alias /F512000000 shlw32m.lib
           -link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
           /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

462.libquantum: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
                -Ob0 -Qprefetch -Qopt-streaming-stores:always /F512000000
                shlw32m.lib -link /FORCE:MULTIPLE

464.h264ref: Same as 456.hmmer

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

```

## Peak Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml>

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 For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
 Report generated on Tue Jul 22 13:19:56 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
 Originally published on 24 July 2007.