



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Rg-1
(Intel Xeon processor X5355)

SPECint®2006 = 18.9

SPECint_base2006 = 17.2

CPU2006 license: 9006

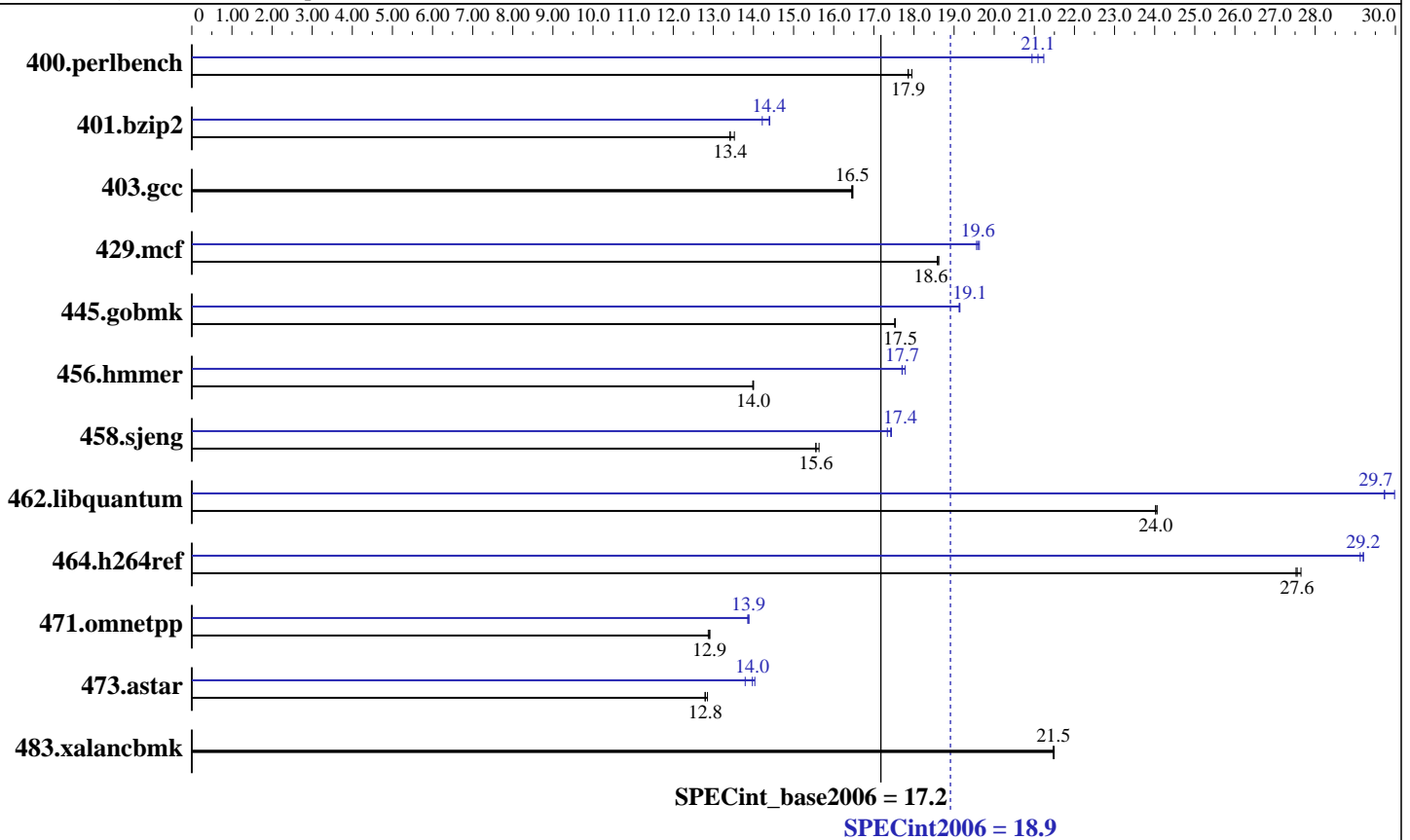
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jun-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73.2 GB SAS, 15000RPM
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070426 Package ID: l_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	544	17.9	547	17.9	<u>547</u>	<u>17.9</u>	<u>463</u>	<u>21.1</u>	460	21.2	467	20.9
401.bzip2	<u>719</u>	<u>13.4</u>	714	13.5	719	13.4	670	14.4	679	14.2	<u>671</u>	<u>14.4</u>
403.gcc	<u>489</u>	<u>16.5</u>	489	16.4	489	16.5	<u>489</u>	<u>16.5</u>	489	16.4	489	16.5
429.mcf	<u>490</u>	<u>18.6</u>	491	18.6	490	18.6	465	19.6	466	19.6	<u>465</u>	<u>19.6</u>
445.gobmk	<u>598</u>	<u>17.5</u>	599	17.5	598	17.5	<u>548</u>	<u>19.1</u>	548	19.1	548	19.1
456.hmmer	667	14.0	<u>667</u>	<u>14.0</u>	667	14.0	<u>527</u>	<u>17.7</u>	527	17.7	525	17.8
458.sjeng	778	15.6	<u>778</u>	<u>15.6</u>	774	15.6	698	17.3	<u>695</u>	<u>17.4</u>	694	17.4
462.libquantum	863	24.0	<u>862</u>	<u>24.0</u>	861	24.1	697	29.7	<u>697</u>	<u>29.7</u>	691	30.0
464.h264ref	800	27.6	804	27.5	<u>803</u>	<u>27.6</u>	758	29.2	<u>758</u>	<u>29.2</u>	760	29.1
471.omnetpp	486	12.9	<u>484</u>	<u>12.9</u>	484	12.9	<u>450</u>	<u>13.9</u>	451	13.9	450	13.9
473.astar	549	12.8	<u>548</u>	<u>12.8</u>	546	12.9	509	13.8	<u>502</u>	<u>14.0</u>	500	14.0
483.xalancbmk	321	21.5	<u>321</u>	<u>21.5</u>	321	21.5	321	21.5	<u>321</u>	<u>21.5</u>	321	21.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

The Express5800/120Rg-1(Intel Xeon processor X5355) and the Express5800/120Ri-2(Intel Xeon processor X5355) models are electronically equivalent. The results have been measured on a Express5800/120Ri-2(Intel Xeon processor X5355) model.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/cce/10.0.023/bin/icc
456.hmmer: /opt/intel/cce/10.0.023/bin/icc

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include
-prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include
-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-ic10-linux-flags.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-ic10-linux-flags.xml>

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