



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®_rate2006 = Not Run

ProLiant DL140 G3
(2.66 GHz, Intel Xeon processor X5355)

SPECfp_rate_base2006 = 56.3

CPU2006 license: 3

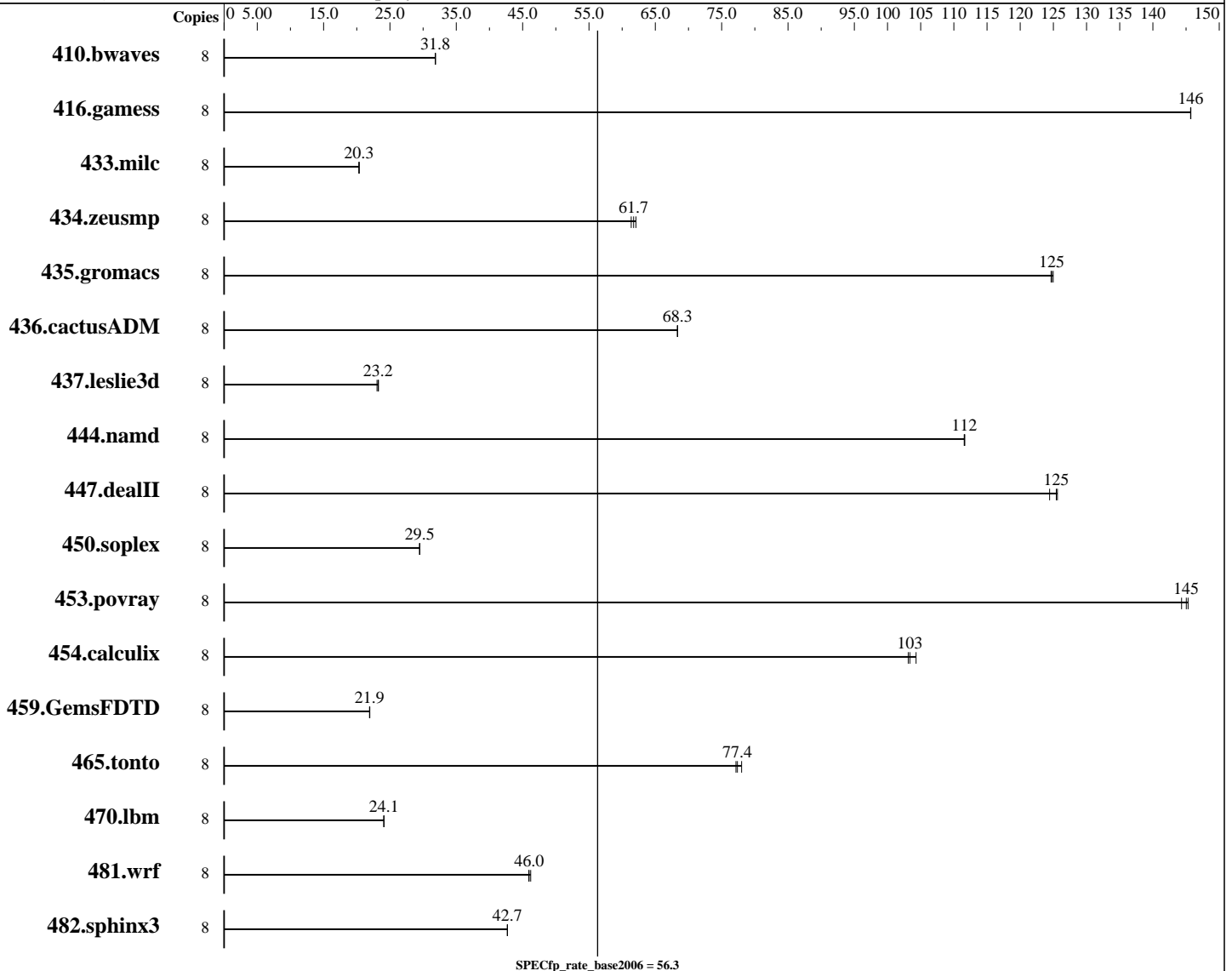
Test date: May-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Mar-2007



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus
 CPU MHz: 2660
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
 Build 20070320, Package ID: 1_cc_c_9.1.049
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
 Build 20070320, Package ID: 1_fc_c_9.1.045
 Auto Parallel: No

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 1x36 GB 10 K SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3415	31.8	3413	31.9	3414	31.8							
416.gamess	8	1075	146	1075	146	1075	146							
433.milc	8	3610	20.3	3611	20.3	3609	20.3							
434.zeusmp	8	1173	62.1	1186	61.4	1179	61.7							
435.gromacs	8	458	125	458	125	457	125							
436.cactusADM	8	1399	68.3	1398	68.4	1399	68.3							
437.leslie3d	8	3231	23.3	3263	23.0	3238	23.2							
444.namd	8	575	112	575	112	575	112							
447.dealII	8	730	125	735	124	729	126							
450.soplex	8	2262	29.5	2265	29.5	2262	29.5							
453.povray	8	293	145	293	145	295	144							
454.calculix	8	638	103	640	103	633	104							
459.GemsFDTD	8	3870	21.9	3870	21.9	3864	22.0							
465.tonto	8	1009	78.0	1017	77.4	1020	77.2							
470.lbm	8	4563	24.1	4563	24.1	4562	24.1							
481.wrf	8	1944	46.0	1945	45.9	1934	46.2							
482.sphinx3	8	3650	42.7	3651	42.7	3653	42.7							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Adjacent Sector Prefetch Disabled in BIOS.
taskset utility used to bind CPU(s) to processes
ulimit -s unlimited set

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>



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For other inquiries, please contact webmaster@spec.org.

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