SPEC® CFP2006 Result

Bull SAS

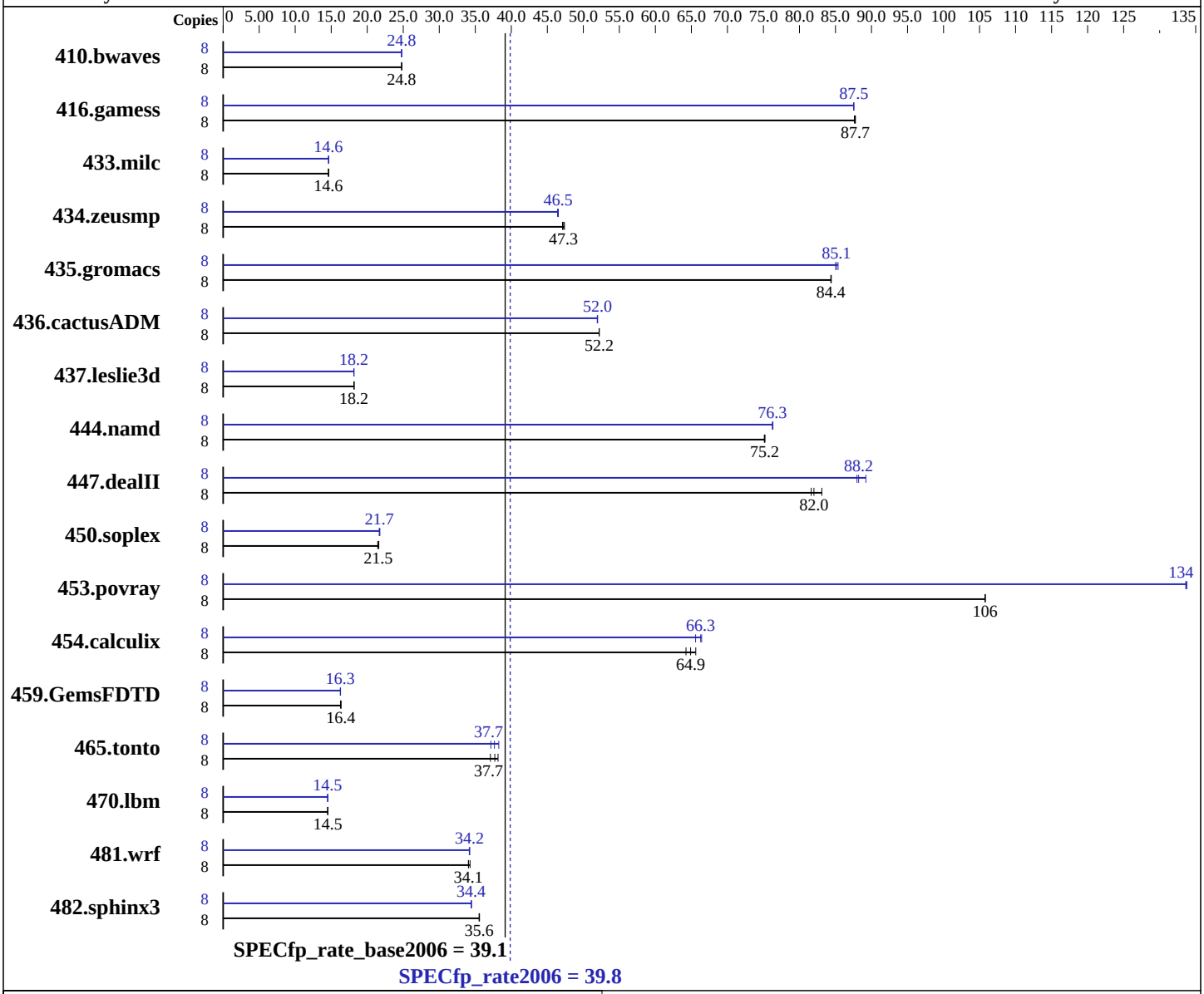
NovaScale B260 (Intel Xeon processor L5320,1.86GHz)

SPECfp®_rate2006 = 39.8

SPECfp_rate_base2006 = 39.1

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Mar-2007
Hardware Availability: Apr-2007
Software Availability: Dec-2006



Hardware

CPU Name:	Intel Xeon L5320
CPU Characteristics:	1.86 GHz, 8MB L2, 1066MHz bus
CPU MHz:	1860
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1 to 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Windows Server 2003 Enterprise Edition (32 bits) Service Pack1
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.033 Build no 20061103Z Intel Fortran Compiler for IA32 version 9.1 Package ID W_FC_C_9.1.033 Build no 20061103Z Microsoft Visual Studio .NET 2003 (lib & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default

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L3 Cache: None
Other Cache: None
Memory: 8 GB (2GB DIMMx4, FB-DIMM PC2-5300F ECC CL5)
Disk Subsystem: 73 GB SAS, 10000RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	4385	24.8	4389	24.8	**4386**	**24.8**	8	**4387**	**24.8**	4393	24.7	4385	24.8
416.gamess	8	**1785**	**87.7**	1785	87.8	1788	87.6	8	1789	87.5	**1789**	**87.5**	1790	87.5
433.milc	8	5016	14.6	5029	14.6	**5019**	**14.6**	8	5007	14.7	**5020**	**14.6**	5025	14.6
434.zeusmp	8	**1541**	**47.3**	1537	47.4	1545	47.1	8	1569	46.4	1564	46.5	**1565**	**46.5**
435.gromacs	8	677	84.4	677	84.4	**677**	**84.4**	8	**671**	**85.1**	672	85.0	669	85.4
436.cactusADM	8	**1831**	**52.2**	1832	52.2	1831	52.2	8	1839	52.0	**1840**	**52.0**	1841	51.9
437.leslie3d	8	4138	18.2	4140	18.2	**4140**	**18.2**	8	**4142**	**18.2**	4141	18.2	4145	18.1
444.namd	8	**853**	**75.2**	853	75.2	854	75.1	8	842	76.2	**841**	**76.3**	841	76.3
447.dealII	8	**1116**	**82.0**	1121	81.6	1101	83.1	8	1040	88.0	1026	89.2	**1038**	**88.2**
450.soplex	8	3110	21.5	3087	21.6	**3096**	**21.5**	8	3066	21.8	**3073**	**21.7**	3079	21.7
453.povray	8	402	106	**402**	**106**	402	106	8	**318**	**134**	319	134	318	134
454.calculix	8	1027	64.2	**1017**	**64.9**	1006	65.6	8	1006	65.6	**996**	**66.3**	994	66.4
459.GemsFDTD	8	**5191**	**16.4**	5209	16.3	5191	16.4	8	5208	16.3	**5213**	**16.3**	5220	16.3
465.tonto	8	2122	37.1	**2088**	**37.7**	2063	38.2	8	2057	38.3	2118	37.2	**2091**	**37.7**
470.lbm	8	**7570**	**14.5**	7568	14.5	7573	14.5	8	**7574**	**14.5**	7575	14.5	7573	14.5
481.wrf	8	**2623**	**34.1**	2625	34.0	2607	34.3	8	**2613**	**34.2**	2610	34.2	2616	34.2
482.sphinx3	8	4381	35.6	**4384**	**35.6**	4390	35.5	8	4523	34.5	4528	34.4	**4528**	**34.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

Base Optimization Flags

C benchmarks:
`-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icl -Qvc7.1 -Qc99 ifort`

Peak Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`

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Peak Portability Flags (Continued)

453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000
-link /FORCE:MULTIPLE

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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