



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint[®]2006 = 11.9

ProLiant BL480c
(1.86 GHz, Intel Xeon processor E5320)

SPECint_base2006 = 11.4

CPU2006 license: 3

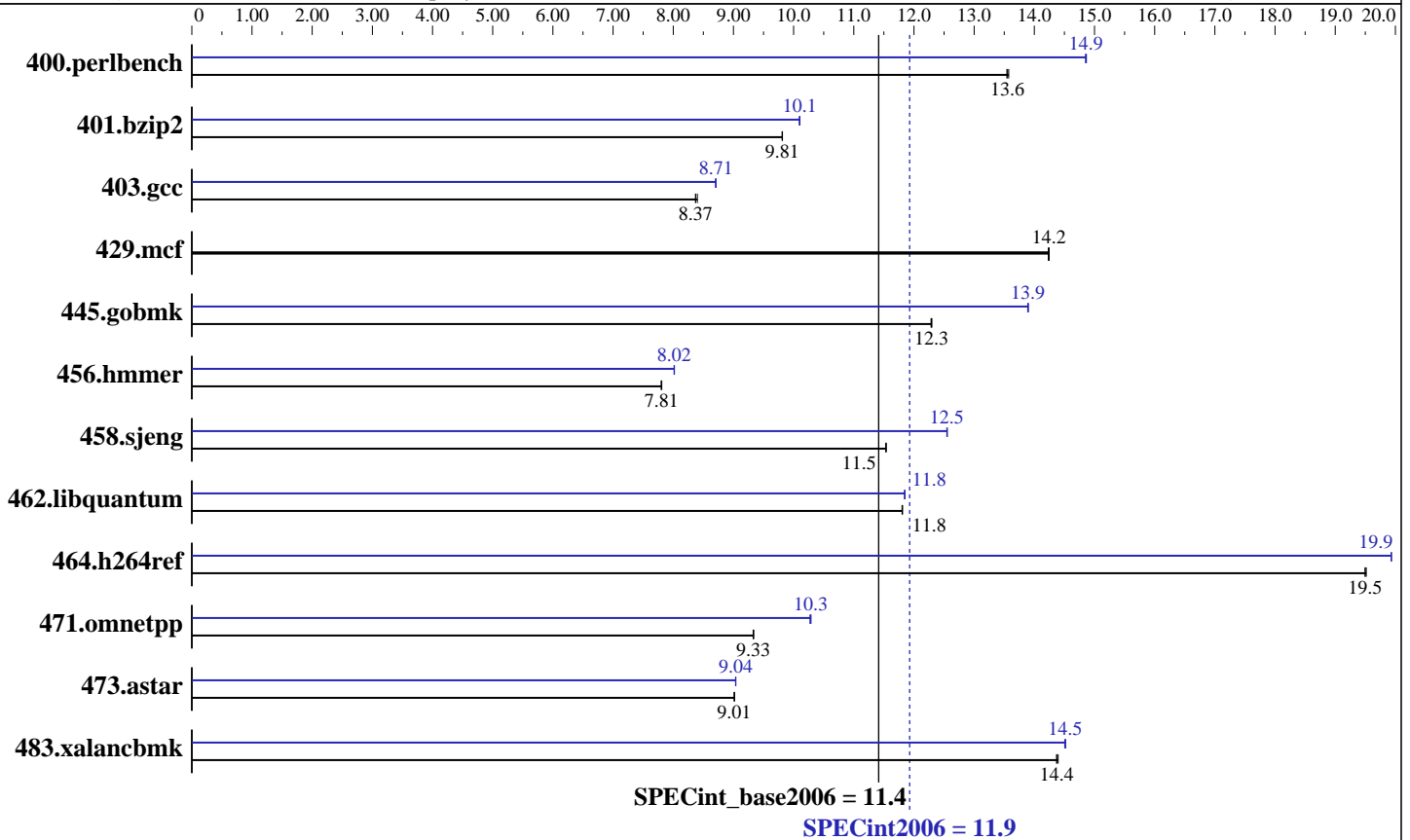
Test date: Mar-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 10 K SAS
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise x64 Edition SP1
 Compiler: Intel C++ Compiler for 32-bit applications, Version 9.1, Build 20061103Z
 Package ID: W_CC_C_9.1.033
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant BL480c
(1.86 GHz, Intel Xeon processor E5320)

SPECint2006 = **11.9**

SPECint_base2006 = **11.4**

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Mar-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	720	13.6	<u>721</u>	<u>13.6</u>	721	13.5	<u>657</u>	<u>14.9</u>	657	14.9	658	14.9
401.bzip2	983	9.81	984	9.81	<u>983</u>	<u>9.81</u>	955	10.1	<u>955</u>	<u>10.1</u>	956	10.1
403.gcc	958	8.40	962	8.37	<u>962</u>	<u>8.37</u>	925	8.70	924	8.71	<u>924</u>	<u>8.71</u>
429.mcf	640	14.2	641	14.2	<u>640</u>	<u>14.2</u>	640	14.2	641	14.2	<u>640</u>	<u>14.2</u>
445.gobmk	853	12.3	854	12.3	<u>853</u>	<u>12.3</u>	<u>755</u>	<u>13.9</u>	755	13.9	755	13.9
456.hammer	1195	7.81	<u>1195</u>	<u>7.81</u>	1196	7.80	1163	8.02	1164	8.01	<u>1164</u>	<u>8.02</u>
458.sjeng	1049	11.5	1049	11.5	<u>1049</u>	<u>11.5</u>	<u>964</u>	<u>12.5</u>	964	12.6	964	12.5
462.libquantum	<u>1755</u>	<u>11.8</u>	1755	11.8	1756	11.8	<u>1749</u>	<u>11.8</u>	1748	11.9	1750	11.8
464.h264ref	1135	19.5	1134	19.5	<u>1135</u>	<u>19.5</u>	<u>1110</u>	<u>19.9</u>	1110	19.9	1110	19.9
471.omnetpp	<u>670</u>	<u>9.33</u>	669	9.34	670	9.33	<u>608</u>	<u>10.3</u>	609	10.3	608	10.3
473.astar	778	9.02	779	9.01	<u>779</u>	<u>9.01</u>	777	9.04	777	9.04	<u>777</u>	<u>9.04</u>
483.xalancbmk	480	14.4	479	14.4	<u>480</u>	<u>14.4</u>	<u>475</u>	<u>14.5</u>	475	14.5	476	14.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint2006 = 11.9

ProLiant BL480c
(1.86 GHz, Intel Xeon processor E5320)

SPECint_base2006 = 11.4

CPU2006 license: 3

Test date: Mar-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006

Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint2006 = 11.9

ProLiant BL480c
(1.86 GHz, Intel Xeon processor E5320)

SPECint_base2006 = 11.4

CPU2006 license: 3

Test date: Mar-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006

Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:44:58 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 March 2007.