



# SPEC<sup>®</sup> CFP2006 Result

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## Hewlett-Packard Company

SPECfp<sup>®</sup>\_rate2006 = 58.6

ProLiant BL480c  
(2.66 GHz, Intel Xeon processor X5355)

SPECfp\_rate\_base2006 = 57.7

CPU2006 license: 3

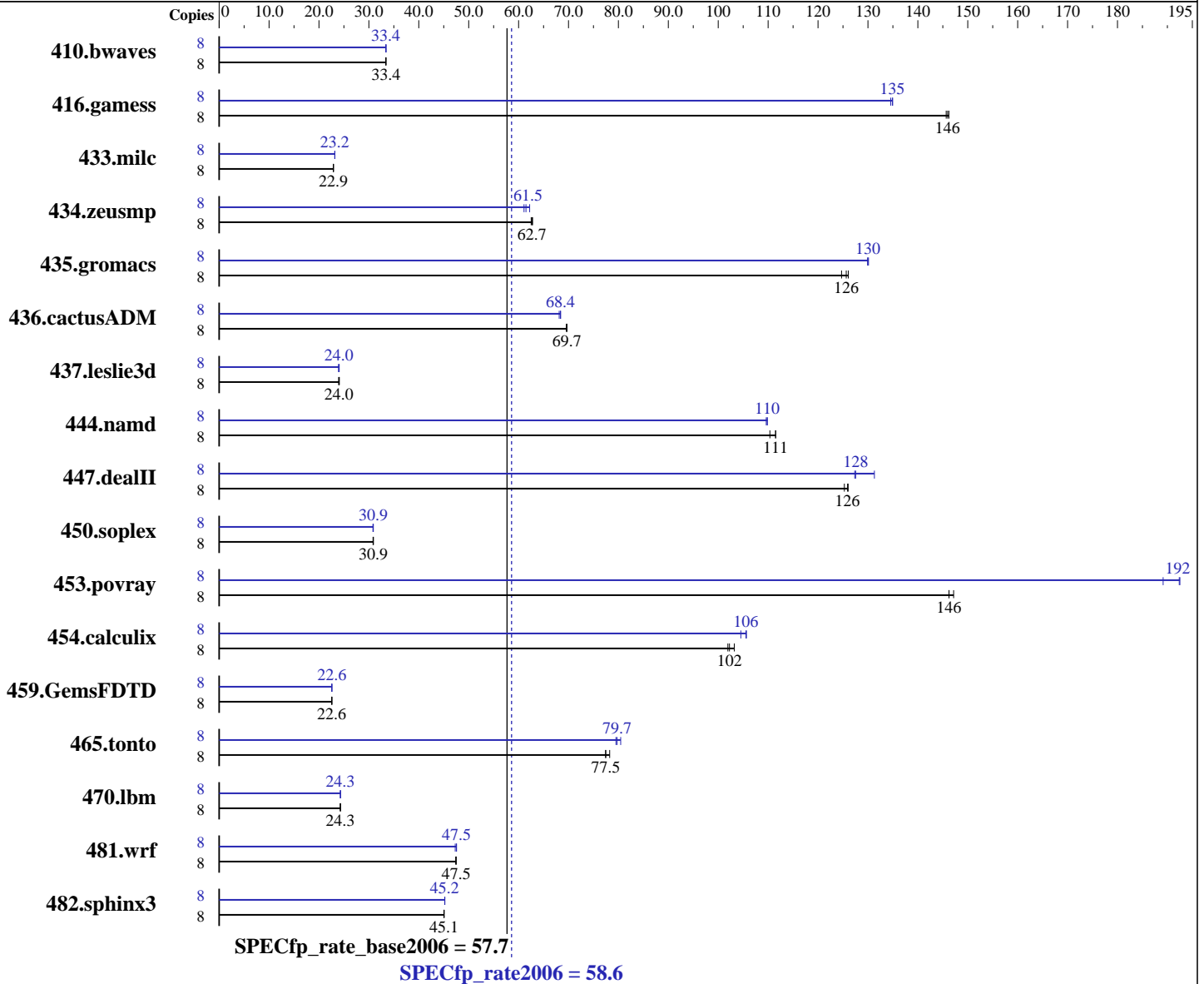
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333MHz system bus  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64)  
 kernel 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1  
 Build 20061101, Package ID: 1\_cc\_c\_9.1.045  
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1  
 Build 20061101, Package ID: 1\_fc\_c\_9.1.040  
 Auto Parallel: No

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L3 Cache: None  
Other Cache: None  
Memory: 16 GB (8x2 GB PC2-5300F CL5)  
Disk Subsystem: 1x72 GB 10k SAS  
Other Hardware: None

File System: ext2  
System State: Multi-user run level 3  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	8	3256	33.4	<b><u>3253</u></b>	<b><u>33.4</u></b>	3252	33.4	8	3253	33.4	3253	33.4	<b><u>3253</u></b>	<b><u>33.4</u></b>		
416.gamess	8	1071	146	1075	146	<b><u>1073</u></b>	<b><u>146</u></b>	8	<b><u>1161</u></b>	<b><u>135</u></b>	1161	135	1164	135		
433.milc	8	<b><u>3208</u></b>	<b><u>22.9</u></b>	3210	22.9	3206	22.9	8	3169	23.2	<b><u>3168</u></b>	<b><u>23.2</u></b>	3167	23.2		
434.zeusmp	8	1159	62.8	1164	62.5	<b><u>1161</u></b>	<b><u>62.7</u></b>	8	1192	61.1	<b><u>1184</u></b>	<b><u>61.5</u></b>	1170	62.2		
435.gromacs	8	453	126	458	125	<b><u>455</u></b>	<b><u>126</u></b>	8	<b><u>439</u></b>	<b><u>130</u></b>	439	130	440	130		
436.cactusADM	8	1372	69.7	<b><u>1372</u></b>	<b><u>69.7</u></b>	1374	69.6	8	1403	68.1	<b><u>1398</u></b>	<b><u>68.4</u></b>	1397	68.4		
437.leslie3d	8	3122	24.1	3141	23.9	<b><u>3132</u></b>	<b><u>24.0</u></b>	8	<b><u>3136</u></b>	<b><u>24.0</u></b>	3143	23.9	3125	24.1		
444.namd	8	575	112	<b><u>575</u></b>	<b><u>111</u></b>	581	110	8	<b><u>584</u></b>	<b><u>110</u></b>	585	110	584	110		
447.dealII	8	731	125	<b><u>727</u></b>	<b><u>126</u></b>	726	126	8	<b><u>718</u></b>	<b><u>128</u></b>	697	131	719	127		
450.soplex	8	2159	30.9	<b><u>2161</u></b>	<b><u>30.9</u></b>	2164	30.8	8	2165	30.8	2160	30.9	<b><u>2161</u></b>	<b><u>30.9</u></b>		
453.povray	8	<b><u>291</u></b>	<b><u>146</u></b>	289	147	291	146	8	225	189	<b><u>221</u></b>	<b><u>192</u></b>	221	193		
454.calculix	8	639	103	647	102	<b><u>645</u></b>	<b><u>102</u></b>	8	<b><u>625</u></b>	<b><u>106</u></b>	625	106	631	105		
459.GemsFDTD	8	3763	22.6	3756	22.6	<b><u>3761</u></b>	<b><u>22.6</u></b>	8	<b><u>3758</u></b>	<b><u>22.6</u></b>	3761	22.6	3757	22.6		
465.tonto	8	1006	78.3	<b><u>1016</u></b>	<b><u>77.5</u></b>	1017	77.4	8	978	80.5	990	79.5	<b><u>987</u></b>	<b><u>79.7</u></b>		
470.lbm	8	4526	24.3	4525	24.3	<b><u>4525</u></b>	<b><u>24.3</u></b>	8	4523	24.3	<b><u>4523</u></b>	<b><u>24.3</u></b>	4523	24.3		
481.wrf	8	1882	47.5	1885	47.4	<b><u>1882</u></b>	<b><u>47.5</u></b>	8	<b><u>1879</u></b>	<b><u>47.5</u></b>	1889	47.3	1879	47.6		
482.sphinx3	8	<b><u>3460</u></b>	<b><u>45.1</u></b>	3460	45.1	3461	45.1	8	3450	45.2	<b><u>3449</u></b>	<b><u>45.2</u></b>	3447	45.2		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.  
Adjacent Sector Prefetch Disabled in BIOS.  
"/usr/bin/taskset" used to bind processes to CPUs.  
Environment stack size set to 'unlimited'

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

## Peak Compiler Invocation

C benchmarks:

icc

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## Peak Compiler Invocation (Continued)

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

C++ benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

Fortran benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast

Benchmarks using both Fortran and C:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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