



# SPEC® CFP2006 Result

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## Hewlett-Packard Company

SPECfp®\_rate2006 = 34.1

ProLiant ML370 G5  
(2.66 GHz, Intel Xeon processor X5355)

SPECfp\_rate\_base2006 = 33.5

CPU2006 license: 3

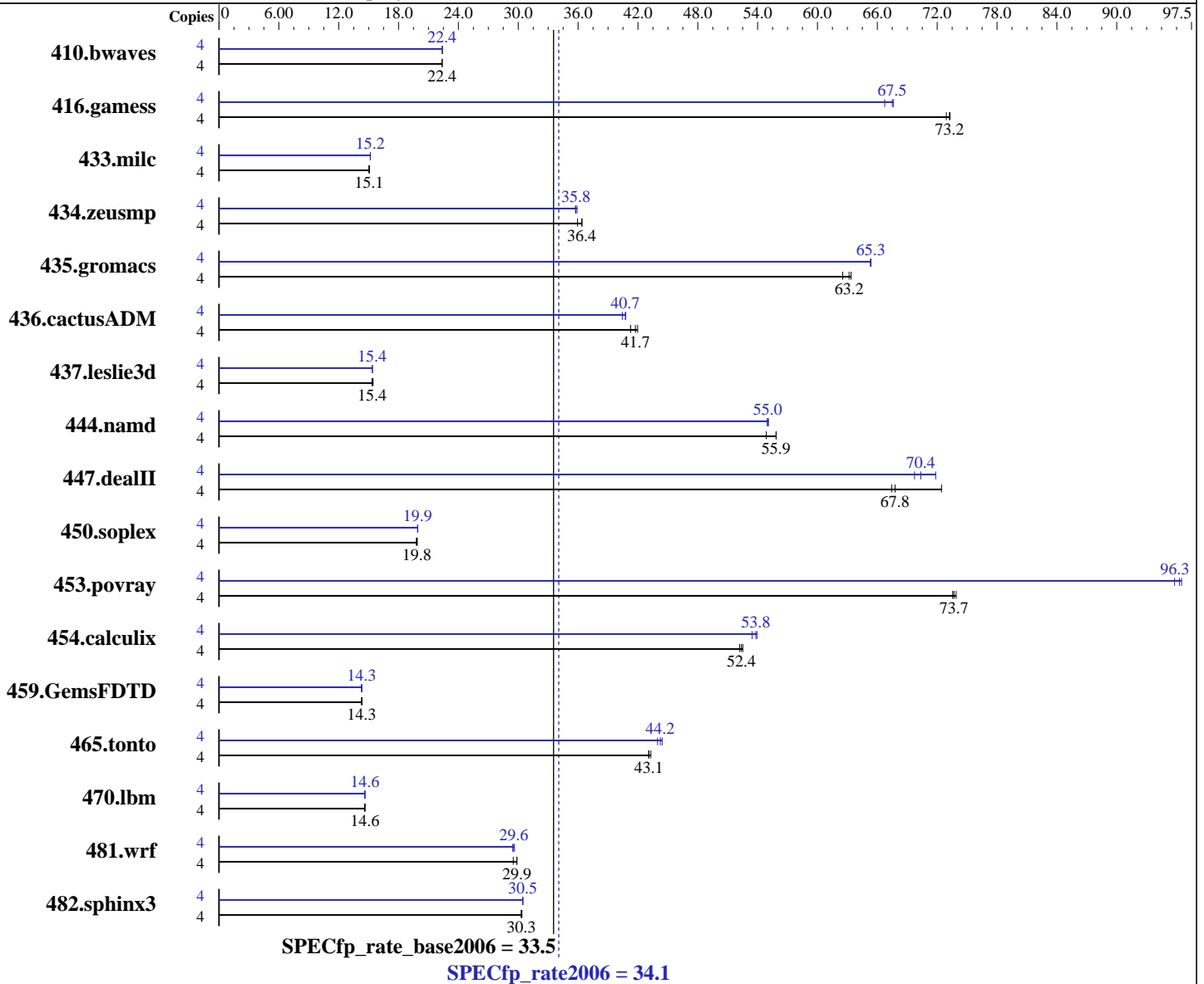
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333MHz system bus  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64)  
 kernel 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1  
 Build 20061101, Package ID: 1\_cc\_c\_9.1.045  
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1  
 Build 20061101, Package ID: 1\_fc\_c\_9.1.040  
 Auto Parallel: No

Continued on next page



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L3 Cache: None  
Other Cache: None  
Memory: 16 GB (8x2 GB PC2-5300 CL5)  
Disk Subsystem: 2x72 GB 10k SAS  
Other Hardware: None

File System: ext2  
System State: Multi-user run level 3  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	2428	22.4	2432	22.4	<u>2431</u>	<u>22.4</u>	4	<u>2431</u>	<u>22.4</u>	2432	22.4	2425	22.4
416.gamess	4	1074	72.9	1069	73.3	<u>1069</u>	<u>73.2</u>	4	1173	66.7	<u>1160</u>	<u>67.5</u>	1159	67.6
433.milc	4	<u>2439</u>	<u>15.1</u>	2438	15.1	2440	15.0	4	2421	15.2	2419	15.2	<u>2419</u>	<u>15.2</u>
434.zeusmp	4	<u>1001</u>	<u>36.4</u>	1013	35.9	1000	36.4	4	1019	35.7	1014	35.9	<u>1018</u>	<u>35.8</u>
435.gromacs	4	451	63.4	457	62.5	<u>452</u>	<u>63.2</u>	4	437	65.3	437	65.4	<u>437</u>	<u>65.3</u>
436.cactusADM	4	1139	42.0	<u>1145</u>	<u>41.7</u>	1158	41.3	4	1182	40.4	1172	40.8	<u>1173</u>	<u>40.7</u>
437.leslie3d	4	2433	15.5	<u>2449</u>	<u>15.4</u>	2451	15.3	4	<u>2446</u>	<u>15.4</u>	2446	15.4	2446	15.4
444.namd	4	574	55.9	<u>574</u>	<u>55.9</u>	585	54.9	4	<u>583</u>	<u>55.0</u>	582	55.1	584	55.0
447.dealII	4	679	67.4	<u>675</u>	<u>67.8</u>	632	72.4	4	656	69.7	<u>650</u>	<u>70.4</u>	637	71.9
450.soplex	4	1678	19.9	<u>1682</u>	<u>19.8</u>	1686	19.8	4	1674	19.9	<u>1675</u>	<u>19.9</u>	1675	19.9
453.povray	4	<u>289</u>	<u>73.7</u>	289	73.6	288	73.9	4	221	96.5	<u>221</u>	<u>96.3</u>	222	95.8
454.calculix	4	628	52.5	<u>630</u>	<u>52.4</u>	632	52.2	4	612	53.9	<u>613</u>	<u>53.8</u>	617	53.4
459.GemsFDTD	4	2969	14.3	<u>2965</u>	<u>14.3</u>	2963	14.3	4	<u>2965</u>	<u>14.3</u>	2972	14.3	2964	14.3
465.tonto	4	909	43.3	914	43.1	<u>913</u>	<u>43.1</u>	4	886	44.4	<u>890</u>	<u>44.2</u>	895	44.0
470.lbm	4	<u>3755</u>	<u>14.6</u>	3765	14.6	3754	14.6	4	<u>3754</u>	<u>14.6</u>	3765	14.6	3753	14.6
481.wrf	4	1515	29.5	1496	29.9	<u>1496</u>	<u>29.9</u>	4	1517	29.4	1508	29.6	<u>1512</u>	<u>29.6</u>
482.sphinx3	4	2567	30.4	2573	30.3	<u>2571</u>	<u>30.3</u>	4	2560	30.4	2559	30.5	<u>2559</u>	<u>30.5</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.  
Adjacent Sector Prefetch Disabled in BIOS.  
"start /b /wait /affinity" used to bind processes to CPU(s).  
"ulimit -s unlimited" set

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

## Peak Compiler Invocation

C benchmarks:

icc

Continued on next page



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## Peak Compiler Invocation (Continued)

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

C++ benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

Fortran benchmarks:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast

Benchmarks using both Fortran and C:  
-prof\_gen(pass 1) -prof\_use(pass 2) -fast -auto\_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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