



SPEC[®] CINT2006 Result

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Itautec

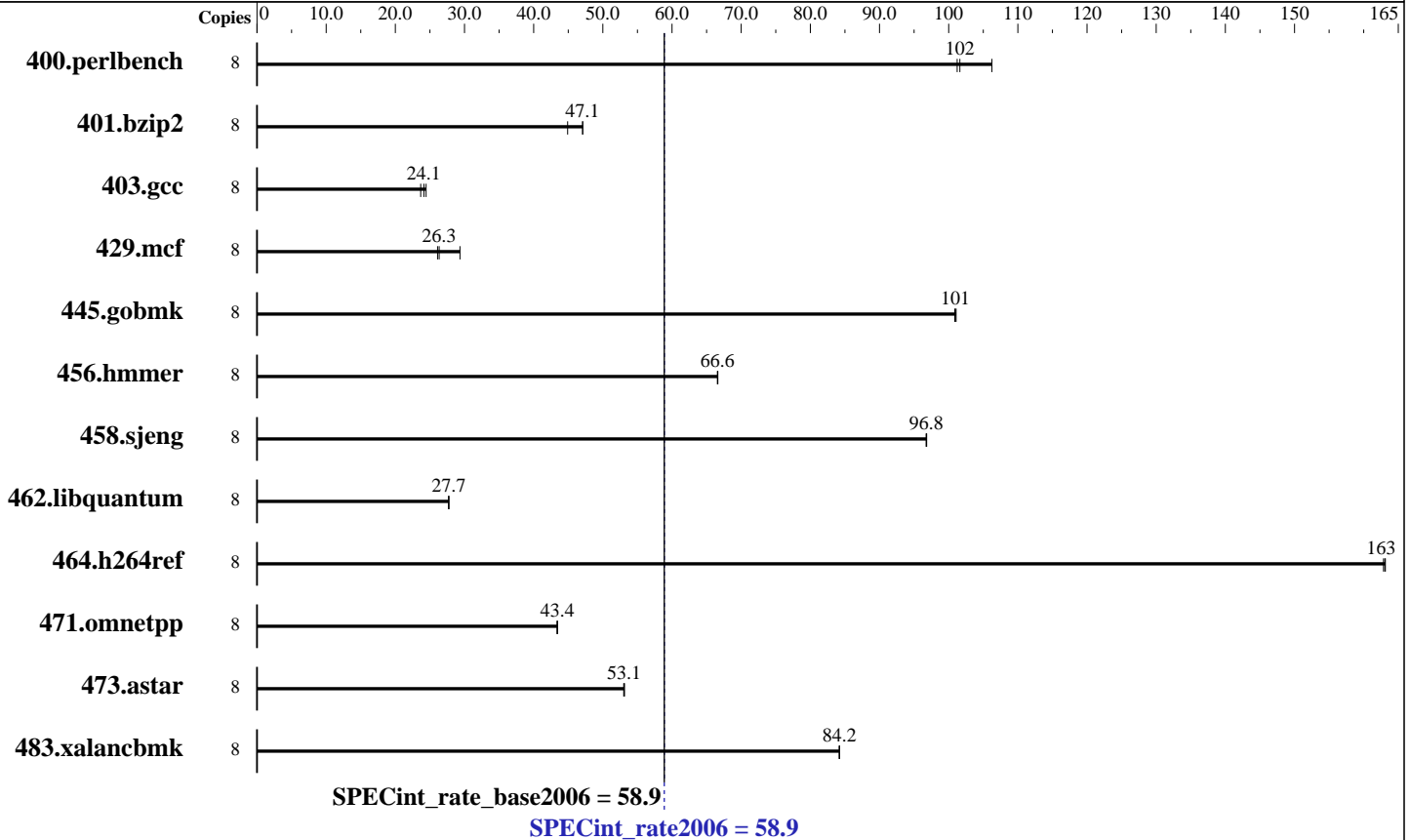
SPECint[®]_rate2006 = 58.9

Servidor Itautec LX211 (Intel Xeon E5335)

SPECint_rate_base2006 = 58.9

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name: Intel Xeon E5335
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1-2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 6 GB (6x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
 Disk Subsystem: 120 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.025 Build no 20060519Z
 Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: Microquill SmartHeap Library v.8.0 for SMP



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	736	106	772	101	769	102	8	736	106	772	101	769	102
401.bzip2	8	1640	47.1	1719	44.9	1639	47.1	8	1640	47.1	1719	44.9	1639	47.1
403.gcc	8	2721	23.7	2634	24.4	2668	24.1	8	2721	23.7	2634	24.4	2668	24.1
429.mcf	8	2486	29.3	2795	26.1	2770	26.3	8	2486	29.3	2795	26.1	2770	26.3
445.gobmk	8	832	101	830	101	831	101	8	832	101	830	101	831	101
456.hammer	8	1121	66.6	1121	66.6	1120	66.6	8	1121	66.6	1121	66.6	1120	66.6
458.sjeng	8	1000	96.8	1000	96.8	1001	96.7	8	1000	96.8	1000	96.8	1001	96.7
462.libquantum	8	5978	27.7	5981	27.7	5979	27.7	8	5978	27.7	5981	27.7	5979	27.7
464.h264ref	8	1086	163	1087	163	1085	163	8	1086	163	1087	163	1085	163
471.omnetpp	8	1151	43.4	1152	43.4	1151	43.4	8	1151	43.4	1152	43.4	1151	43.4
473.astar	8	1059	53.0	1058	53.1	1057	53.1	8	1059	53.0	1058	53.1	1057	53.1
483.xalancbmk	8	655	84.2	656	84.2	656	84.2	8	655	84.2	656	84.2	656	84.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlSMPMt.lib -link /FORCE:MULTIPLE
C++ benchmarks:
-fast -Qcxx_features /F512000000 shlSMPMt.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml>



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