



SPEC[®] CINT2006 Result

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Itaotec

SPECint[®]_rate2006 = 51.2

Servidor Itaotec LX211 (Intel Xeon E5320)

SPECint_rate_base2006 = 51.2

CPU2006 license: 9001

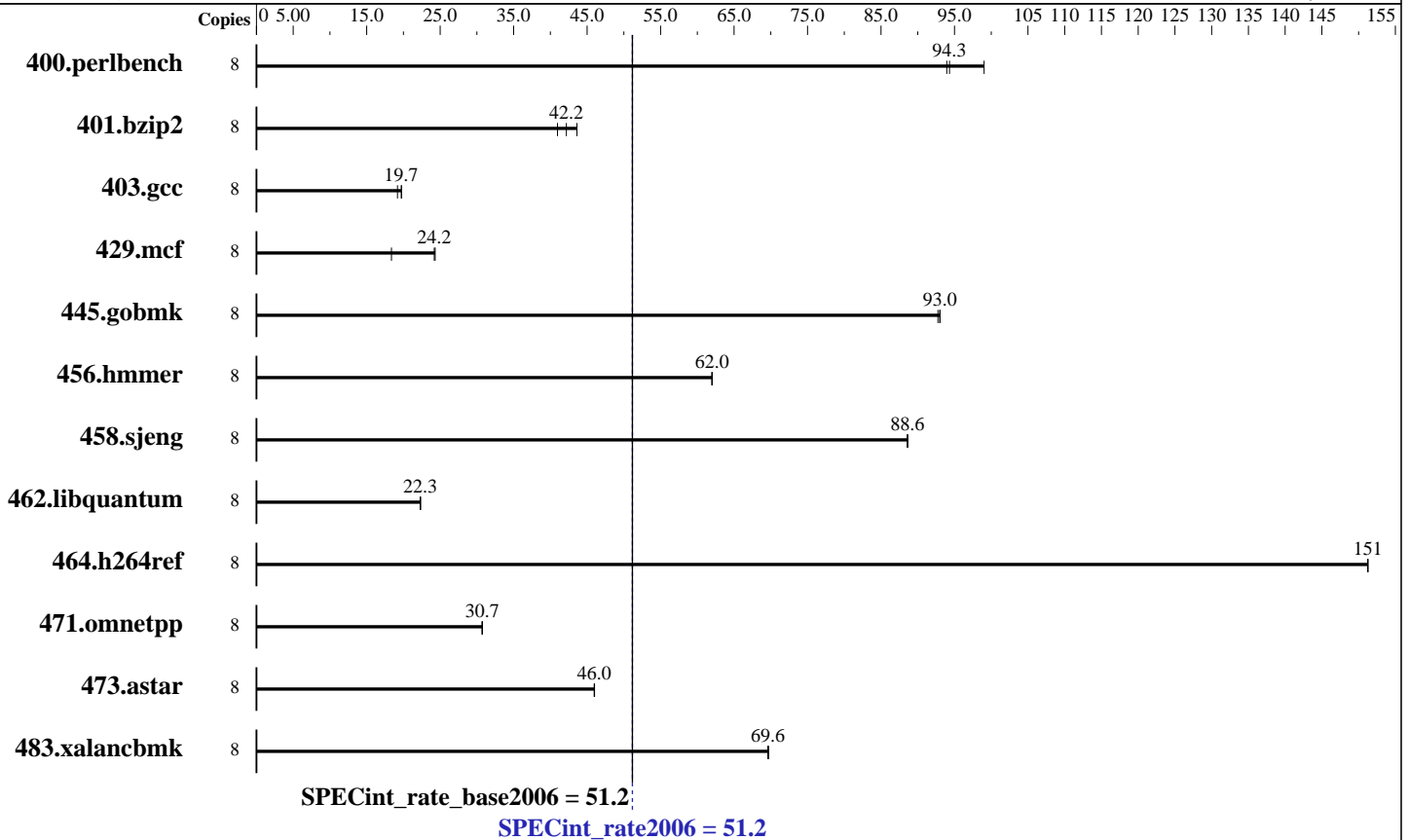
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Feb-2007

Hardware Availability: Feb-2007

Software Availability: May-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1-2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 6 GB (6x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
 Disk Subsystem: 120 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.025 Build no 20060519Z
 Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: Microquill SmartHeap Library v.8.0 for SMP



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	789	99.0	832	94.0	<u>829</u>	<u>94.3</u>	8	789	99.0	832	94.0	<u>829</u>	<u>94.3</u>
401.bzip2	8	<u>1830</u>	<u>42.2</u>	1884	41.0	1770	43.6	8	<u>1830</u>	<u>42.2</u>	1884	41.0	1770	43.6
403.gcc	8	<u>3266</u>	<u>19.7</u>	3356	19.2	3263	19.7	8	<u>3266</u>	<u>19.7</u>	3356	19.2	3263	19.7
429.mcf	8	3000	24.3	3971	18.4	<u>3015</u>	<u>24.2</u>	8	3000	24.3	3971	18.4	<u>3015</u>	<u>24.2</u>
445.gobmk	8	<u>902</u>	<u>93.0</u>	902	93.0	905	92.7	8	<u>902</u>	<u>93.0</u>	902	93.0	905	92.7
456.hammer	8	1204	62.0	1203	62.0	<u>1204</u>	<u>62.0</u>	8	1204	62.0	1203	62.0	<u>1204</u>	<u>62.0</u>
458.sjeng	8	1093	88.6	<u>1093</u>	<u>88.6</u>	1092	88.6	8	1093	88.6	<u>1093</u>	<u>88.6</u>	1092	88.6
462.libquantum	8	7436	22.3	<u>7431</u>	<u>22.3</u>	7422	22.3	8	7436	22.3	<u>7431</u>	<u>22.3</u>	7422	22.3
464.h264ref	8	1171	151	1170	151	<u>1170</u>	<u>151</u>	8	1171	151	1170	151	<u>1170</u>	<u>151</u>
471.omnetpp	8	1626	30.8	1628	30.7	<u>1628</u>	<u>30.7</u>	8	1626	30.8	1628	30.7	<u>1628</u>	<u>30.7</u>
473.astar	8	1222	46.0	<u>1221</u>	<u>46.0</u>	1221	46.0	8	1222	46.0	<u>1221</u>	<u>46.0</u>	1221	46.0
483.xalancbmk	8	<u>793</u>	<u>69.6</u>	792	69.7	793	69.6	8	<u>793</u>	<u>69.6</u>	792	69.7	793	69.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlSMPMt.lib -link /FORCE:MULTIPLE
C++ benchmarks:
-fast -Qcxx_features /F512000000 shlSMPMt.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml>



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