SPEC® CFP2006 Result

Itautec

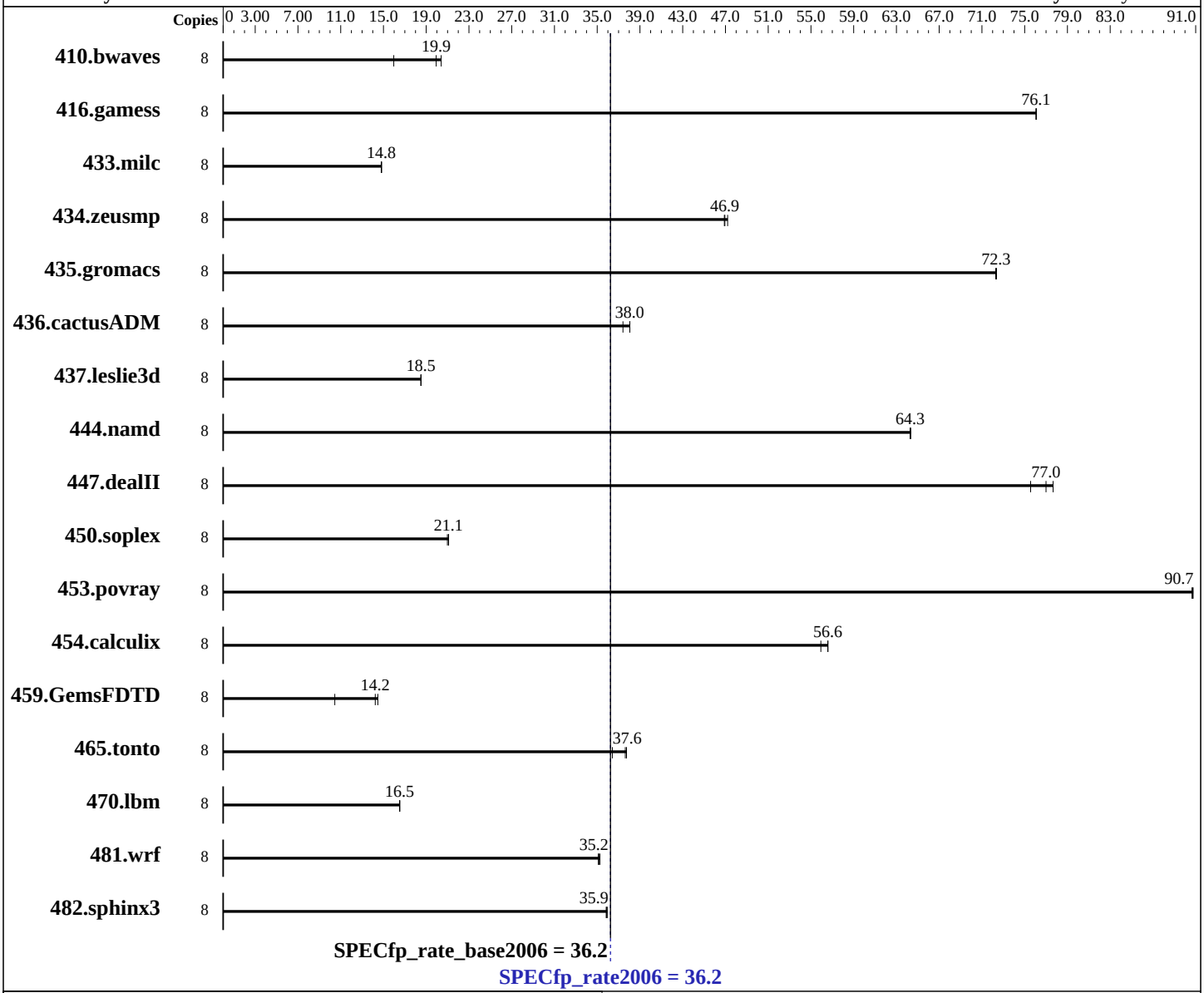
Servidor Itautec LX211 (Intel Xeon E5310)

SPECfp®_rate2006 = 36.2

SPECfp_rate_base2006 = 36.2

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1066MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Intel Fortran Compiler for IA32 version 9.1 Package ID W_FC_C_9.1.025 Build no 20060519Z Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 6 GB (6x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem: 120 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	6810	16.0	**5454**	**19.9**	5329	20.4	8	6810	16.0	**5454**	**19.9**	5329	20.4
416.gamess	8	2059	76.1	**2059**	**76.1**	2059	76.1	8	2059	76.1	**2059**	**76.1**	2059	76.1
433.milc	8	**4957**	**14.8**	4963	14.8	4957	14.8	8	**4957**	**14.8**	4963	14.8	4957	14.8
434.zeusmp	8	1552	46.9	**1551**	**46.9**	1542	47.2	8	1552	46.9	**1551**	**46.9**	1542	47.2
435.gromacs	8	**790**	**72.3**	790	72.3	789	72.4	8	**790**	**72.3**	790	72.3	789	72.4
436.cactusADM	8	2512	38.1	**2513**	**38.0**	2555	37.4	8	2512	38.1	**2513**	**38.0**	2555	37.4
437.leslie3d	8	4059	18.5	4067	18.5	**4061**	**18.5**	8	4059	18.5	4067	18.5	**4061**	**18.5**
444.namd	8	998	64.3	**998**	**64.3**	997	64.3	8	998	64.3	**998**	**64.3**	997	64.3
447.dealII	8	1178	77.7	**1189**	**77.0**	1211	75.6	8	1178	77.7	**1189**	**77.0**	1211	75.6
450.soplex	8	3163	21.1	**3164**	**21.1**	3179	21.0	8	3163	21.1	**3164**	**21.1**	3179	21.0
453.povray	8	**469**	**90.7**	469	90.7	469	90.8	8	**469**	**90.7**	469	90.7	469	90.8
454.calculix	8	1166	56.6	**1166**	**56.6**	1180	55.9	8	1166	56.6	**1166**	**56.6**	1180	55.9
459.GemsFDTD	8	5863	14.5	**5964**	**14.2**	8125	10.4	8	5863	14.5	**5964**	**14.2**	8125	10.4
465.tonto	8	2086	37.7	**2092**	**37.6**	2162	36.4	8	2086	37.7	**2092**	**37.6**	2162	36.4
470.lbm	8	6658	16.5	6650	16.5	**6657**	**16.5**	8	6658	16.5	6650	16.5	**6657**	**16.5**
481.wrf	8	**2542**	**35.2**	2546	35.1	2535	35.2	8	**2542**	**35.2**	2546	35.1	2535	35.2
482.sphinx3	8	4339	35.9	4350	35.8	**4344**	**35.9**	8	4339	35.9	4350	35.8	**4344**	**35.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

Base Optimization Flags

C benchmarks:
`-fast /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `basepeak = yes`

450.soplex: `basepeak = yes`

453.povray: `basepeak = yes`

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `basepeak = yes`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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