



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®2006 = Not Run

IBM BladeCenter HS21 XM (Intel Xeon E5345)

SPECfp_base2006 = 11.8

CPU2006 license: 11

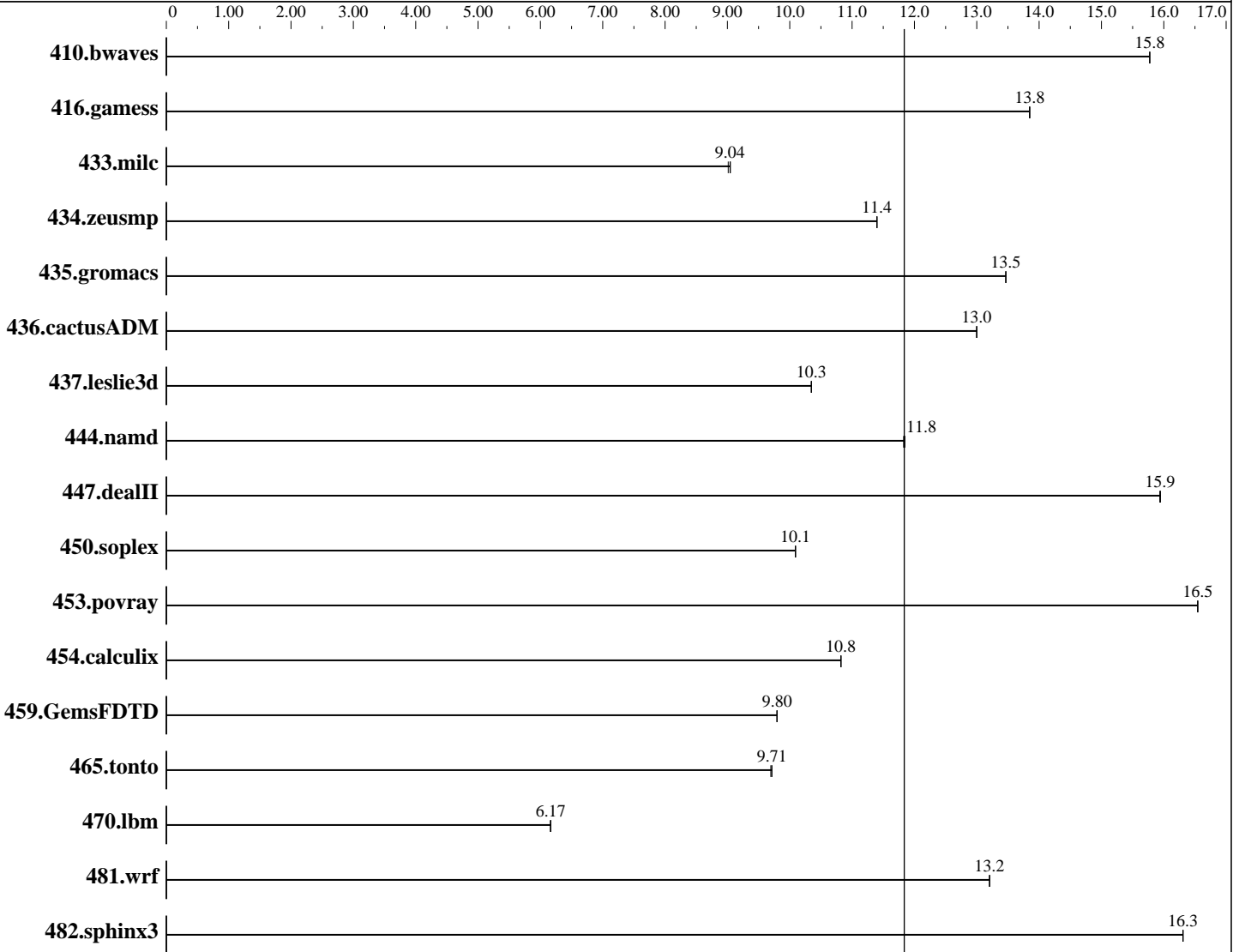
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2007

Hardware Availability: Feb-2007

Software Availability: Aug-2006



SPECfp_base2006 = 11.8

Hardware

CPU Name: Intel Xeon E5345
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816
 Intel Fortran Compiler for IA32 version 9.1 Build no 20060816
 Microsoft Visual Studio .Net 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS

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L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2GB DDR2-5300F ECC)
 Disk Subsystem: 1 x 74 GB SAS, 1000 RPM
 Other Hardware: None

System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Smart Heap Library, Version 8

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	862	15.8	861	15.8	862	15.8						
416.gamess	1414	13.8	1414	13.8	1414	13.9						
433.milc	1015	9.05	1015	9.04	1019	9.01						
434.zeusmp	798	11.4	798	11.4	798	11.4						
435.gromacs	530	13.5	530	13.5	530	13.5						
436.cactusADM	919	13.0	919	13.0	919	13.0						
437.leslie3d	909	10.3	909	10.3	908	10.3						
444.namd	678	11.8	677	11.8	677	11.8						
447.dealII	717	15.9	718	15.9	718	15.9						
450.soplex	826	10.1	826	10.1	827	10.1						
453.povray	322	16.5	322	16.5	322	16.5						
454.calculix	762	10.8	762	10.8	762	10.8						
459.GemsFDTD	1083	9.80	1083	9.80	1083	9.80						
465.tonto	1015	9.70	1013	9.71	1013	9.71						
470.lbm	2231	6.16	2228	6.17	2228	6.17						
481.wrf	846	13.2	846	13.2	846	13.2						
482.sphinx3	1195	16.3	1195	16.3	1195	16.3						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc7.1 -Qc99 ifort



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Base Portability Flags

```

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.deallI: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
           -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

```

Base Optimization Flags

```

C benchmarks:
  -fast /F950000000 shlw32m.lib          -link /FORCE:MULTIPLE

C++ benchmarks:
  -fast -Qcxx_features /F950000000 shlw32m.lib
  -link /FORCE:MULTIPLE

Fortran benchmarks:
  -fast /F950000000          -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
  -fast /F950000000          -link /FORCE:MULTIPLE

```

The flags file that was used to format this result can be browsed at <http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link: <http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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